## Solar Powered Drones Power Electronics

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# Solar Powered Drones

### **Power Electronics**

by

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### Abstract and preface

#### Abstract

Sustainable energy technologies are ever-increasing in popularity, not only among companies but also among consumers. Photovoltaic, or PV, cells are amongst the most well known in this field. This project was set up for designing a PV-powered UAV that is able to generate an albedo map of an area equal or bigger than the campus of the TU Delft in a single run. PV cells, however, are highly susceptible to changes in weather conditions and have a range of voltages at which these cells can supply power. Besides that, unnecessary losses occur if the so-called 'Maximum Power Point' (MPP) is not tracked. In this thesis, a boost converter is proposed for the aforementioned system to implement the maximum power point tracking and supply the, by the PV system produced, power as efficiently as possible to the UAV power system.

#### **Preface**

We want to extend our sincere thanks to our supervisor Patrizio Manganiello for guiding us through this project. His help has been indispensable in the process, be it in terms of technical support or care and patience. Furthermore, we would also like to thank the assisting supervisors Mirco Muttillo and Andres Calcabrini for their help. We know that the current times have also been challenging for our supervisors and that they did their utmost to bring this project to a successful ending.

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# 1

### Introduction

#### 1.1. Problem analysis

Drones in general are mostly in small form factors and don't have much space available for energy storage. Oil-based solutions such as petrol and diesel engines allow for more energy storage but the biggest drawback of these solutions is that they are polluting and non-sustainable. Therefore, the feasibility of adding PV cells on top of the wings of a Skywalker X8 is checked.



Figure 1.1: Overview of the system surrounding the power converter

In Figure 1.1, the inter-connectivity of the converter within the UAV, and some important numbers that will be considered in more detail later in this thesis are shown. In Appendix B, the complete documentation of the UAV is given.

#### 1.2.1. Irradiance

The maximum power yield of a PV cell is very dependent on the amount of light that is impinging onto it. This amount is represented by the irradiance G  $[W/m^2]$ . In Figure 1.2 the expected irradiance in June based on weather data that was provided to aid this research.



Figure 1.2: Irradiance in June in the Netherlands [9]

#### 1.2.2. Maximum power point tracking

A PV cell's performance is dependent on multiple factors, such as the irradiance and temperature. The power delivery of the cell is determined by a so-called I-V curve. By using this curve the optimal point of operation, or MPP, can be determined. At this point, the output power of the PV cell is highest. The converter that will be designed in this thesis should be able to alter the operating point of the PV system to ensure that the maximum amount of power is delivered to the power system of the UAV.

#### 1.3. Thesis synopsis

In this thesis a certain structure will be followed that coincides with the designing process. First a current state of the art is given. Then, the design, implementation are considered. Subsequently, the results will be validated and limitations to these results will be discussed. Finally, a conclusion and some recommendations are given.

# 2

### Requirements

#### 2.1. Requirements of the total system

The goal of this project is to implement solar panels in a UAV to increase its range, whereas the minimum flight range should cover the area of the TU Delft. This is in order to enable an albedo map to be captured of the area.

#### 2.1.1. Assumptions

We assume the following conditions are met to operate our UAV.

- There is a large enough grass field nearby the location of operation to take off and land a fixed wing UAV.
- The operator flying the UAV is at least certified for ROC and the necessary permits for flying in the area are obtained.

#### 2.1.2. Mandatory requirements

Functional requirements

- The UAV should be able to capture an area the size of the TU Delft campus in a single flight in a maximum time of 60 minutes.
- The UAV should be able to fly 5 times during one day, 2 times before noon, 1 time at noon and 2 times after noon, between flights the battery should be replaced or recharged.
- The UAV's minimum flight altitude is 100m and maximum flight altitude is 120m.
- The UAV should be able to fly with and without solar panels.
- The components of the UAV should be able to be attached into the UAV.
- The weight and size of the components should not prevent the UAV's ability to fly.
- The camera should be able to create images suited for albedo mapping with a GSD (Ground Spatial Distance) of at least 20 cm at a flight height of 120 meters.
- The UAV should be controllable using an autopilot for efficient albedo mapping.
- The UAV should be able to take off and land in a controllable manner.

#### Non-functional requirements

- The UAV should be less than 4kg to fall within the legal classification of a small UAV.
- The UAV should be able to operate at temperatures ranging from -10 to +  $40^{\circ}C$
- Components should be commercial available
- Prices of chosen components should be market conform

#### Trade off requirements

- The weight of the UAV should be as low as possible to increase flight time.
- The flight path should be optimized to increase area efficiency.
- The effective wing area should be as high as possible to increase power generated by the PV generator.

• The flight speed of the UAV should be as high as possible while keeping the quality of the pictures as high as possible.

#### **2.2.** Power converter requirements

The goal of this project is to design a DC/DC power converter that is able to convert power from the PV, which can be used to power the UAV, to increase the flight range. In order to achieve this, the DC/DC power converter...

#### 2.2.1. Functional requirements

- · Can handle the maximum input power, voltage and current from the PV system
- Is able to simultaneously supply the UAV and charge the batteries, while the output voltage matches the required voltage for both solutions.
- · Controllable to match the Maximum Power Point load of the PV system.
- Has integrated sensors to measure the quantities needed to perform MPPT and switch to battery charging/discharging based on the power drawn by the system and delivered by the PV system.
- Is controlled by a digital controller.

#### 2.2.2. Non-Functional requirements

- Has a minimum efficiency of 85%
- Should have a maximum output voltage ripple of 0.5%

#### 2.2.3. Trade-off requirements

- Has an automatic analog fallback circuit for when the (MPPT) controller fails. Possibly, this can be extended to a MPPT feedback solution.
- · Has the conversion efficiency optimized for the average expected PV power.

# **3** State of the art

#### 3.1. Switching type converters

Switching type converters are the most commonly used converter class, where higher efficiencies are needed. These converters have much higher efficiencies than linear converters with efficiencies up to 98% [18]. It operates the voltage by using a transistor as a switch. Due to the high frequency often used, both the required capacitor and inductor values are a lot lower in general.

#### 3.1.1. PWM converters

PWM, Pulse-Width Modulation, or Chopper, converters are the most commonly used type of switching converters. They allow for voltage regulation and control [7], which is convenient when the input and/or output voltage are variable.

#### Buck converter

A buck, or step-down, converter is used to convert a DC voltage to a lower DC voltage of the same polarity [8]. It generally has very low power loss, which results in a very high efficiency. As it is only able to step down the voltage, it is unable to perform when the load value is smaller than the optimal MPPT impedance, which results in debatable overall results [18]. An example of a buck converter is shown in Figure 3.1.

#### Boost converter

A boost, or step-up, converter is used to do the opposite of a buck converter. It is able to convert a lower DC voltage to a higher DC voltage of the same polarity. A boost converter is also very efficient in its operation. Contrary to buck and buck-boost solutions, this kind of converter is only able to step up the voltage, which results in its inability to perform when the impedance of the PV system is higher than the impedance of the converter [12][18]. An example of a boost converter is shown in Figure 3.2.



Figure 3.1: Buck converter [8]



Figure 3.2: Boost converter [8]

#### (inverting) Buck-Boost converter

A buck-boost, or step-up/step-down, converter is a combination of a buck and a boost converter. It can both convert a higher DC voltage to a lower DC voltage and the other way around. Contrary to the buck

and boost converter, it has a non-operating spectrum, which means it is able to match the apparent impedance for all values of the optimal impedance for the MPPT. For an inverting type buck-boost, another major difference is that it inverts the polarity of the voltage. An example of such a circuit can be seen in Figure 3.3a. The non-inverting, as shown in Figure 3.3b, has a higher efficiency than its inverting counterpart and is therefore most likely a better solution for efficient power transfer [18].



Figure 3.3: Two basic buck-boost converter topologies

**Flyback converter** is a variation on the standard buck-boost converter. A coupled inductor is placed between the input and the output, additionally, which makes it an isolated converter. It is also able to both step-up and step-down. Contrary to the standard buck-boost topology, it is non-inverting, which means the output voltage has the same polarity as the input voltage.

A flyback converter is very efficient but there are a few complications. First of all, a coupled inductor can be quite bulky when the switching frequency is chosen to be lower. Secondly, also due to the coupled inductor, active clamps, or snubbers, need to be used to counter the magnetic flux losses. If no form of switching loss reduction is used, the efficiency of the system will go down significantly.

**Four-Switch buck-boost** converter is another variance on the standard buck-boost converter, as is shown in Figure 3.5. As the name implies, it is driven by four switches instead of two. The main advantage of this converter is that it can be controlled by two separate duty cycles, which means more freedom [14]. Besides that, the diodes have been replaced. For low-power, low-frequency systems, this results in a higher efficiency. A drawback is that it adds complexity to the system.



Figure 3.4: Flyback (Buck-Boost) converter [8]



Figure 3.5: Four-Switch buck-boost converter [14]

**Ćuk** converters are an inverting buck-boost type converter. The main advantages of this type of converter is that the input is almost ripple-free, it has a continuous input current and the switch terminal is connected to the ground, which makes the gate drive design for switching less complex. A major drawback, however, is that the coupling capacitor needs to be large and expensive [18]. Additionally, this capacitor is put under much stress, which leads to less robustness for the overall circuit. An example of a Ćuk converter can be seen in Figure 3.6.

**SEPIC** or a Single-Ended Primary Inductor Converter, is another type of non-inverting buck-boost type converters. It consists of a boost and inverted buck-boost converter added together. Like the Ćuk converter, it has the switch terminal connected to the ground. Furthermore, unlike the Ćuk converter it does not mostly operate over the coupling capacitor but over both a capacitor and inductor, which results in less stress on the individual components [18].



Figure 3.6: Ćuk converter [19]



Figure 3.7: SEPIC converter [19]

#### Switched coupled capacitors

A switched coupled capacitors, or charge pump, converter is a step-up or step-down type converter that has no inductors within its circuit. It instead delivers power by charging and discharging capacitors. Its efficiency and complexity are in between the inductive switch type and linear converters. However, charge pump gains are fixed [11]. Therefore, this type of converter will not be considered as a possible solution for this project.

#### **3.1.2.** Switching losses

Switching losses occur due to the fall and rise time of the MOSFET. In Equation 3.1[16], the switching losses are shown.

$$P_{loss_{switch}} = \frac{(t_r + t_f)}{2} \cdot V_{PV} \cdot I_{out} \cdot f_s \qquad (3.1)$$



Figure 3.8: Hard switching and resonance switching visualised [15]

#### Hard switching

By default, voltage converters that use switches have switching losses caused by hard switching. Hard switching is a switching method that uses the device its own switching ability [1]. An example of hard switching can be seen in the left of Figure 3.8. In this Figure, the effect of non-zero current or voltage switching is shown.

#### **Resonant switching**

Resonant switching is a more efficient method of switching. This method allows for (near) zero voltage switching and/or zero current switching, which leads to decreased losses. This is especially significant for very high switching frequencies, such as frequencies above 500kHz [15]. Within the given time frame, implementing resonant switching will not be considered in this thesis, but it could be interesting for future research.

#### **3.2. MOSFET switches**

#### **3.2.1. Introduction to MOSFETS**

MOSFET's have two major advantages over bipolar junction transistors, the first advantage being the ease of use of MOSFET's for high frequency switching applications. Besides that, once the MOSFET transistor is turned on, its drive current is near zero. This results in a higher efficiency for converters[4].

#### **3.2.2. Power MOSFET models**

Figure 3.9 shows MOSFET models for three different problem areas. The first model is based on the actual structure of the MOSFET device and can be used mainly for DC analysis. The second model can used very effectively to model the dc/dt induced breakdown characteristics of a MOSFET. The last models shows the most important parasitic components for when the MOSFET is switching. [4]



Figure 3.9: Power MOSFET models[4]

#### 3.2.3. Switch procedure and the Miller plateau region

The switch procedure of a MOSFET can be divided into four intervals. When the MOSFET is turned on, the input capacitance of the device is charged from 0V to  $V_{th}$ . These capacities are connected to the drain and source, shown in the third model of figure 3.9. After the threshold voltage is reached, the MOSFET becomes in a linear operating mode. During this period, the charging of the input capacitance continues and the the drain current increases, shown in equation 3.2, proportional to the gate voltage. When the gate voltage reaches the Miller plateau level, the drain current becomes stable in the third period starts . The miller plateau level is described in equation 3.3 or could be found in the datasheet. In this period, the gate current starts discharging the  $C_{GD}$  capacitor of the MOSFET what induces a fast drain-source voltage drop and so decreases the on-resistance rapidly. A remarkable point, the gain voltage stays stable at the Miller Plateau level during this period. That is why it is called the Miller Plateau region. In the last step the gate capacitors will charge to the final driver voltage and the onresistance will improve to it's final value. The same procedure in the other way around follows when the MOSFET is turned off[4].

$$I_D = (V_{GS} - V_{th}) * g_{fs}$$
(3.2)  $V_{GS,Miller} = V_{th} + \frac{I_D}{g_{fs}}$ (3.3)

The trans conductance,  $g_{fs}$  showed in equation 3.4, is the small signal relationship between drain current and gate-to-source voltage as shown in equation. 3.4.

$$g_{fs} = \frac{dI_D}{dV_{Gs}} \tag{3.4}$$

1

#### 3.2.4. Critical Power Losses

**Gate drive loss** [4] Gate drive loss arise by turning on or off the MOSFET, which results in charging or discharging the parasitic capacitors inside the MOSFET. The power loss is described by Equation 3.5, where  $V_{DRV}$  is the amplitude of the gate drive waveform,  $f_{DRV}$  the gate-drive frequency, and  $Q_G$  is the gate charge.

$$P_{Gate} = V_{DRV} * Q_G * f_{DRV} \tag{3.5}$$

**Driver power dissipation** 

$$P_{DRV,ON} = \frac{1}{2} * \frac{R_{HI} * V_{DRV} * Q_G * f_{DRV}}{R_{HI} + R_{Gate} + R_{G,I}} \quad (3.6) \qquad P_{DRV,OFF} = \frac{1}{2} * \frac{R_{LO} * V_{DRV} * Q_G * f_{DRV}}{R_{LO} + R_{Gate} + R_{G,I}} \quad (3.7)$$

$$P_{DRV} = P_{DRV,ON} + P_{DRV,OFF} \tag{3.8}$$

#### Total switching loss approximation[4]

Calculating the exact switching losses is almost impossible. The following described linear approximation seems to be a reasonable enough compromise to estimate switching losses in the MOSFET.

$$P_{SW} = \frac{V_{DS(off)} * I_L}{2} * \frac{t2 + t3}{T}$$
(3.9)

where

$$t2 = C_{ISS} * \frac{V_{GS,Miller} - V_{TH}}{I_{G2}}, t3 = C_{RSS} * \frac{V_{DS,off}}{I_{G3}},$$
(3.10)

$$I_{G2} = \frac{V_{DRV} - 0.5 * (V_{GS,Miller} + V_{TH})}{R_{HI} + R_{GATE} + R_{G,I}}, I_{G3} = \frac{V_{DRV} - V_{GS,Miller}}{R_{HI} + R_{GATE} + R_{G,I}}$$
(3.11)

#### 3.2.5. Effect of source inductance on switching performance

For the switching performance is the source inductance, which stems from the wires, is the most dominant of all parasitic components. When switching and the gate current is changing very rapidly, the current must flow through the source inductor and will be slowed down. With as result, a longer switching time. After that, the source inductor and capacitance  $C_{ISS}$  of the MOSFET (described in the datasheet) form a resonant circuit, as shown in figure 3.10, what induces the oscillatory spikes observed in most gate drive circuits. This oscillation can be damped by the series resistive components of the loop, which include the driver output impedance, the external gate resistor, and the internal gate mesh resistor. For the optimum performance, the gate resistance can be calculated by equation 3.12 where  $C_{ISS}$  and  $L_S$  are given by the MOSFET datasheet.

The second effect of the source inductance is a negative feedback whenever the drain current of the device is changing rapidly in the second period in the switching process when the MOSFET is turned on (or third when turned off). A significant voltage has to be applied across the source inductance to increase the drain current quickly. Consequently, the voltage will drop across the drive impedance with as result a lower change in drain current. The balance between both is established through the negative feedback by the source inductor. [4]

$$R_{gate,opt} = 2 \cdot \sqrt{\frac{L_s}{C_i ss} - (R_{drv} + R_{g,i})} \quad (3.12)$$



Figure 3.10: Gate-Drive Resonant Circuit Components, fig 7 in [4]

# 4 Design

#### 4.1. Introduction

This chapter explains the design choices made in the process of creating the power converter. First, the constraints are elaborated on, then the decided topology is explained, and finally, the corresponding design equations are worked out.

#### 4.2. Constraints

Some design constraints regarding the converter need to be taken into account. First of all, because the converter will be eventually implemented inside a UAV with PV cells, there are two options regarding the power supply of this system. The first option is that the UAV flies on the PV power alone. However, it is already shown in Appendix B that it already consumes more power, while cruising, than the PV system is able to supply. The second option is the attachment of a battery to the system that is able to provide the system with the necessary power when needed. It is clear that the second option is the only possible option for this particular case. For this reason, a battery is attached to the converter in parallel to the load, as is shown in figure 4.1. As previously stated in Appendix B, a brush-less DC motor is able to operate more efficiently at higher voltages. Due to this, a battery with a nominal voltage of 18.5V was chosen, which equals five cells connected in series to match the maximum voltage the motor allows. When discharging, the voltage can drop down with 12.5V as a minimum, and the maximum charging voltage of the battery equals 21.0V. Therefore, an output range of 12.5V to 21.0V needs to be accounted for.



Figure 4.1: Simplified blackbox design of the DC-DC converter, including design constraints

#### 4.3. Topology

In the requirements, it is specified that the converter is able to operate at the voltages that are supplied to it by the PV system attached to the UAV. More specifically, for the design of the converter, it is assumed from the PV specifications that the input voltage is ranging from 8 - 12.5V. The chosen topology must be able to convert this voltage to the voltage of the battery. Since the battery voltage is always higher than the assumed output voltage of the PV system, a boost converter topology was chosen.

#### 4.3.1. Boost converter

A boost, or step-up, converter is able to convert a lower DC voltage to a higher DC voltage of the same polarity. Next to the relatively simple implementation of a boost converter, it is also very efficient in its operation.



Figure 4.2: Topology of an ideal boost converter

In Figure 4.2, a representation of an ideal boost converter is shown. When the switch is closed, a current will start to flow through the inductor, charging it. When the switch is opened, a current will start to flow from the charged inductor towards the right-hand part of the circuit, inducing a voltage in the capacitor, due to the change in current. When the switch is closed once more, the capacitor will discharge and keep the output voltage more or less constant.

#### **4.4. Design equations**

Now that the topology has been selected, an analysis of the circuit needs to be done in order to assess the design equations for the converter. Furthermore, a steady-state analysis has been done later on to assess the relation between the input and output voltage and the non-idealities of the circuit, of which the complete calculation can be found in Appendix C.

#### 4.4.1. Continuous vs. discontinuous conduction mode

Before assessing the formulas for the components, an important choice has to be made, whether the converter will operate in continuous conduction mode (CCM) or discontinuous conduction mode(DCM). In continuous mode, the energy storage of the inductor never reaches zero. On the other side, this does happen when in DCM, which implies that  $L_{DCM} < L_{CCM}$ . Taking a look at Ohm's law provides us with the fact that a lower inductance will result in a higher peak-to-peak current, which is the effect of higher ripple current. Therefore, CCM is chosen as the operating mode of the converter, resulting in a duty cycle for the switch that is given by Equation 4.1. Besides that, losses may occur since not all supplied power by the PV system can be stored. Both of the issues described will be covered in the next sections.

$$D = 1 - \frac{V_{PV}}{V_{out}} \tag{4.1}$$

#### 4.4.2. **Ripple**

Ripple currents and voltages are deviations on top of the average values. Most of the time, ripple values are expressed in both a percentage and in amperes or volt, respectively. For this converter, the main requirements have been set to limit ripple at the input and the voltage ripple at the output.

#### Input ripple

As this converter will be connected to a PV system, the MPP will be controlled by it as well. Depending on which method is used for the MPPT, the input current ripple may play a significant role in the accuracy of the chosen method. The method that was chosen by for this vehicle's cells, perturb and observe, or simply P&O, is also susceptible to this variation. Hence, the input ripple needs to be limited in order to minimize the deviation of the MPPT.

$$I_{pp} = r_l \cdot I_{max} \tag{4.2}$$

$$L = \frac{|V_{PV} - V_{out}| \cdot T_{on}}{I_{pp}}, \text{ where } T_{on} = D \cdot T_{sw}$$
(4.3)

The formula for a ripple current is given by Equation 4.2. In this case, the peak-to-peak current ripple is defined as a percentage of the maximum current provided by the PV system. Using Ohm's law and applying a switching frequency yields Equation 4.3 for the value of the inductor.

Another thing that is not caused by the input ripple but can influence it is the total energy storage of the left-hand side of the circuit. The maximum energy storage needed is given by Equation 4.4, at the maximum PV power and highest on time for the switch. The maximum energy that can be stored in an inductor can now be calculated by means of Equation 4.5. Dependent on the energy storage capability of the inductor, a more substantial inductance value or an additional input capacitance can be added for  $E_{L,MAX} < E_{store,MAX}$ . The addition of an input capacitance leads to LC filter characteristics and is able to smooth the input voltage, on top of that. Therefore if the need for this arises, a capacitor will be implemented besides the already present inductance. The need for such a capacitor is described under Chapter 5.

$$E_{store,MAX} = P_{PV,MAX} \cdot T_{sw} \tag{4.4}$$

$$E_{L,MAX} = \frac{1}{2} \cdot L \cdot I_{MPP,MAX}^2 \tag{4.5}$$

$$E_{C,MAX} = \frac{1}{2} \cdot C \cdot V_{MPP,MAX}^2 \tag{4.6}$$

#### **Output ripple**

The output ripple is another ripple that is taken into consideration for this converter. As the converter is directly connected to the battery, it is best to ensure that the ripple is minimal, and it is less likely to heat up due to ripple in the operating voltage, subsequently. Defining the ripple as by Equation 4.7 and using the same principle as for deriving Equation 4.3 by the voltage/current relationship of capacitors, Equation 4.8 is obtained.

$$V_{pp} = r_V * V_{out} \tag{4.7}$$

$$C_{out} = \frac{T_{on}}{\left((V_{pp}/I_{pp})\right)} \tag{4.8}$$

For the purpose of this converter, the output capacitor will be operating at the same voltage as battery voltage, as it is directly connected. Because the output voltage, therefore, is relatively fixed, during the state, when the switch is closed, the capacitor will stay mostly charged.

# 5 Implementation

In this chapter, the component selection will be discussed. First, an ideal implementation is covered, after which non-idealities are taken into account. Lastly, the simulation model and the system integration of the model are described.

#### **5.1. Ideal implementation**

Before determining the component values of the boost circuit, it has to be assessed what parameters are the most important to take into account. A high efficiency can be obtained by reducing the switching frequency and thus switching losses, as can be derived from Equation 5.1.



$$P_{loss_{switch}} = \frac{(t_r + t_f)}{2} \cdot V_{PV} \cdot I_{out} \cdot f_s$$
(5.1)

Figure 5.1: Capacitance and inductance values versus the switching frequency and output voltage of the converter

However, decreasing the switching frequency will require larger component sizing, which may lead to increased volume and weight and subsequently power loss due to a higher required motor power.

Therefore, a trade-off between the component value and switching frequency needs to be made. Furthermore, the output voltage, which is equal to the battery voltage, needs to be taken into account as well, in order to ensure that the set requirements are met. For the calculations of these components, Equations 4.2, 4.3, 4.7 and 4.8 are used. In Figure 5.1, the results are shown. From this figure, it can be seen that both the capacitance and inductance increase exponentially for a switching frequency lower than 150kHz. At the same time, the decrease in component values for frequencies lower than 150kHz slows down.



Figure 5.2: Capacitance and inducance value versus the battery voltage at 150kHz

Besides this, the effect of the battery voltage on the required component values for a frequency of 150kHz can be observed in Figure 5.2. The maximum value is found at a battery voltage of 18V for the capacitance, and 21V is found for the inductance. In Figure 5.3, the influence of the frequency on the component value can be seen even more clearly. From Figure 5.3, a minimum required value of  $99.37\mu F$  is determined. The same principle applies to the minimum inductance value of  $18.83\mu H$  that is determined from Figure 5.3b. In reality, the closest commercially available component values are  $100\mu F$  and  $22\mu H$ , which are the values that will, therefore, be used inside the simulation models. It is found using Equations 4.4, 4.5 and 4.6 that for these values, in order to store the maximum power that can appear in the left-hand circuit, as is described in Section 4.4.2, an additional input capacitor is required with a minimum value of  $1.14\mu F$ . Once more, a similarly valued, commercially available, counterpart will be rated at a value of  $1.5\mu F$ , which is also the value that will be used for simulation purposes.

#### **5.2.** Non-Ideal implementation

In reality, the circuit that is shown in Figure 4.2 is missing some parameters that are accountable for losses in the circuit. These losses need to be taken into account for the simulation in order to have a realistic model. In Figure 5.4, a non-ideal boost converter topology is shown that has accounted for the losses that will be taken into account.

#### 5.2.1. Components

#### Power losses/Equivalent series resistance (ESR)

Non-ideal components like capacitors and inductors are in fact not without parasitic effects next to its ideal implementation, which can induce losses but also have other properties that can be taken into consideration. After assessing and prioritising these effects, a component can be selected to minimise the power losses.



Figure 5.3: Capacitance and inductance value versus the switching frequency



Figure 5.4: Representation of a non-ideal boost converter

**Inductor** The electrical losses caused by the inductor can be separated into two categories: Core losses and conductor losses. The core losses majorly depend on the type of core and the switching frequency. Because the core losses (mW) are much lower [6] in comparison to the conducting losses(W), the core losses are not into account.

The conductor losses, which is caused by the ESR, which subsequently consists of the DC and AC resistance, come from the wire that the inductor consists of [13]. This results in a new voltage relationship, that can be seen in Equation 5.2 and Equation 5.3. The resistance of this wire is dependent on the length of the wire, but also the diameter or thickness of the wire. The longer the wire, the higher the resistance and weight of the inductor. The thicker the wire, the lower the resistance will be, but it will also increase the weight of the inductor.

Next to the electrical losses, there is also a power loss caused by the inductor's weight. Due to the increase in weight, the UAV will consume more power in order to fly. According to the aerodynamic model, an increase of 1kg leads to an increase in power consumption of 28W. Therefore, a trade-off between the ESR and the weight of the inductor has to be made. Ohm's law for the maximum input current and ESR is compared to the power needed per weight increase in Table 5.1. The possible choices for the inductors are all rated for peak currents of 12 ampere, which has been determined by taking into account the maximum input current, input current ripple, and a safety factor of 1.5. Finally, it is the Pulse Electronics (PA4343.223NLT) inductor that is chosen to implement in this converter due to it showing the least power losses.

Manufacturer	Туре	<b>L</b> (μH)	$I_{max}(A)$	<b>R</b> (mΩ)	Mass $(g)$	$P_{loss_R}(W)$	$P_{loss_{mass}}(W)$	$P_{tot}(W)$
Coilcraft	VER2923	22	9	2,3	37	0,014375	1,0471	1,061475
Wurth Elektronik	74435572200	22	11	14,6	13	0,09125	0,3679	0,45915
Pulse Electronics	PA4343.223NLT	22	12	37	0,123	0,23125	0,0034809	0,2347309
Coilcraft	MSS1583-223MEB	22	10,4	36	4,4	0,225	0,12452	0,34952
Bourns	SRP1265A-220M	22	12	32	6,1	0,2	0,17263	0,37263
Vishay	IHLP6767GZ	22	9,5	20	11,8	0,125	0,33394	0,45894

Table 5.1: Comparison in the power losses of several inductor types



$$V_L = V_{PV} - V_{R_I}$$
, when MOSFET turned on (5.3)

**Capacitor** For the capacitor, there is also an ESR present. In this case, however, the ESR is not directly correlated to the weight of the capacitor. The capacitor's ESR is highly dependent on the type of capacitor that is used. Electrolytic capacitors are low cost but have a high ESR, which induces losses at the output. Ceramic capacitors have a very low ESR and have a good frequency response but have less effective capacitance stability at higher values. Due to the extremely low ESR, ceramic type capacitors are very suitable for an input capacitance [17]. However, at some frequencies, a dip in ESR may occur [3]. Besides that, to ensure good smoothing of the voltage, it is essential that the ESR is not too low [13]. This is why a tantalum type capacitor was chosen for the output instead. Although it has a little higher ESR, the effective capacitance is more stable, and the ESR is never too low. The chosen type for the output capacitor is the KEMET T521X107M025ATE060, which has a capacitance of  $100\mu F$ , a series resistance of  $60m\Omega$  and a rated voltage of 25V, and a Murata GCM21BR71C155MA37, which has a capacitance of  $1.5\mu F$ , a series resistance of  $< 1m\Omega$  and rated voltage of 16V.

#### **Diode losses**

**Forward voltage drop** The forward voltage is present in all non-ideal diodes. The internal resistance of the diode causes a voltage drop at the output. One of the unusual properties of a diode is that the voltage drop remains relatively constant. This means for low-voltage, high-current applications that the power loss is quite high. The power loss in a diode can be calculated by means of Equation 5.4. Schottky diodes have a lower voltage drop than PN-junction diodes and are therefore more prevalent in use for power converters.

$$P_{loss_{diode}} = V_{DF} \cdot I_D \tag{5.4}$$

**Reverse bias leakage current** The reverse bias leakage is current is also something that causes losses when operating a diode. In an ideal situation, the diode prevents current from the output to the input side of circuit. In reality, however, a reverse bias current flows backward through the block of the diode. The higher the bias voltage across the diode, the higher the reverse bias current. For simplicity of the steady-state analysis, the reverse bias current is not taken into account, as the losses originating from this are negligible since  $I_{rev.bias} << I_{out}$ .

**Chosen diode** As previously stated, Schottky diodes have a lower forward voltage than PN-junction diodes. For this reason, this type will also be used in the implementation of the converter. For the selection of the specific diode, the forward current rating and breakdown voltage have been taken into account. The maximum voltage potential between the input and output terminal occurs at a minimum input voltage, of 0*V*, and a maximum output voltage, of 21*V*, which is therefore also the minimum required breakdown voltage. The Rohm RBR30NS30A suffices for both these parameters, with a current rating of 30*A* and a breakdown voltage of 30*V*.

#### MOSFET

The MOSFET is used to switch the inductor current between the ground and the output. The maximum drain-source current ( $I_{DS}$ ) is equal to the max inductor current, which is theoretically 6.25 ampere for the maximum expected power output of the solar panels. However, this current can have an increase when the PV cell temperature or irradiance changes. That's why a safety margin of factor 1.5 is included over the max drain current. After this safety margin, the converter is resistant for peak currents from PV with outside temperatures till 40 degrees and irradiances up to  $1500W/m^2$ , assuming linear scaling of the power. The minimal drain-source voltage ( $V_{DS}$ ) of the MOSFET is determined by the maximum output voltage with the forward diode voltage together. To be able to handle voltage spikes over the MOSFET induced by parasitic switching effects, this voltage is multiplied by a factor 2, which results in a minimal  $V_{DS}$  around 40 volts.

The main losses in a MOSFET switch (equation 5.5) are gate switching losses (equation 5.6) and conduction losses (equation 5.7). While the switching loss in the MOSFET driver is also dependent on the MOSFET, an approximation of the driver loss has been made to get the best overview before selecting the MOSFET. The switching losses are dependent on the values of the switching frequency, driver and gate resistances, and the gate charge  $Q_G$ . To be more specific, the  $Q_G$  is dependent on the gate capacity, which is dependent on the MOSFET design, and the driver voltage.

In order to reduce the conduction losses, the on-resistance  $R_{DS}$  should be minimized.  $R_{DS}$  is dependent on the MOSFET design and the gate, or driver, voltage. When minimizing the total power loss, the gate capacity and on-resistance must be low as possible. Unfortunately, as is determined by the design of a MOSFET, a lower  $Q_G$  results in a higher  $R_{DS}$  and vice versa. Finding the optimum for all these parameters can be a time-consuming process. Due to the lack of available time, a more straightforward method was implemented, which first assesses which parameters are more critical.

When equalizing the losses in the gate, driver, and on-resistance by doing an approximation for the gate resistances, a ratio could be found between  $R_{DS}$  and  $Q_G$  (equation 5.11) for different input powers. This ratio indicates when the switching losses are equal to the conduction losses. Figure 5.5 shows the ratio as a function of the input power. For this plot are the gate with driver resistances approximated at 10 $\Omega$ , the input voltage is fixed at 12V and the output voltages are fixed at 18.5V.

The figure illustrates the relevance of gate charge and on-resistance for different input power scenarios. When the input power is decreasing (visible for input powers lower than 10W), the gate charge would be more critical. So a lower  $Q_D$  (or higher R) is needed to find an equilibrium between the losses. On the other hand, when the input power increases higher than 10W, the losses in the drain-source resistance is dominating over the switching losses and will be much more critical.

The expected average input power from the solar cells is 32W, so the drain-source resistance will be the most critical parameter to select the MOSFET.

$$P_{MOSFET} = P_{Gate} + P_{DS}(+P_{DRV})$$
(5.5)

$$P_{Gate} = V_{DRV} * Q_G * f_{DRV} \tag{5.6}$$

$$P_{DS} = I_{DS}^2 * R_{DS}$$
(5.7)

$$P_{DRV,ON} = \frac{1}{2} * \frac{R_{HI} * V_{DRV} * Q_G * f_{DRV}}{R_{HI} + R_{Gate} + R_{GI}}$$
(5.8)

$$P_{DRV,OFF} = \frac{1}{2} * \frac{R_{LO} * V_{DRV} * Q_G * f_{DRV}}{R_{LO} + R_{Gate} + R_{GI}}$$
(5.9)

$$P_{DRV} = P_{DRV,ON} + P_{DRV,OFF} \tag{5.10}$$

$$\frac{R}{Q} = \frac{V_{DRV} * f_{DRV} + \frac{(0.5 * R_{HI} * V_{DRV} * f_{DRV})}{(R_{HI} + R_{Gate} + R_{GI})} + \frac{0.5 * R_{LO} * V_{DRV} * f_{DRV}}{(R_{LO} + R_{Gate} + R_{GI})})}{(I_{DS}^2 * DC)}$$
(5.11)



Figure 5.5: RQ ratio for different input powers

To conclude, the following method is used to select the MOSFET: ---

- 1. Filter MOSFETs with a rated  $V_{DS}$  of minimal 40V
- 2. Order MOSFETs with minimal  $R_{DS}$
- 3. Select MOSFET with minimal  $R_{DS}$  and lowest Q

In table 5.2 is the result of the specified selection procedure for three different manufacturers based on the parameters from the respective datasheets. The selected MOSFET for the boost converter is the Nexperia BUK9JOR9-40H; for the datasheet see Appendix F.1. The expected power losses for the selected MOSFET are in a range of 40mW to 75mW, as shown in figure 5.6.



Figure 5.6: Nexperia BUK9JoR9-40H expected power losses

#### 5.2.2. Sensors

For MPPT of the PV system, it's required to know the input voltage (PV), input current (PV) of the converter. Also, the output voltage (battery) is required for the protection of the battery. Furthermore, a current sensor is implemented at the output, in order to have the possibility for a MPPT fallback circuit that is regulated by output power, rather than input power. In this section we discuss how and which sensors are chosen to implement.

Manufacturer	Туре	Max Vds	Rds @Vds = 4.5	<b>Qg @Vds = 4.5</b>
Vishay	SiDR402DP	40	0.00116	53
Texas Instruments	CSD18510Q5B	40	0.00160	118
Nexperia	BUK9JoR9-40H	40	0.00097	50
Nexperia	PSMNR90-40YLH	40	0.00097	54

Table 5.2: Comparison in  $R_{DS}$  and  $Q_G$  for different MOSFETs from three different MOSFET manufacturers

#### Voltage sensor

The sensor will be used by a microcontroller that has a 5 volts, usual 10-bit ADC, input. For this application, to options to consider are the voltage divider or an optocoupler. Because of simplicity, the voltage divider is a more suitable option for this power converter. The resistors will be selected from the RN73 resistor series of KOA Speer Electronics. This resistor series has high precision, high reliability and is very stable for elevated temperatures, which makes it perfectly suitable for measurement applications for a good price.

The disadvantage of the voltage divider is the constant current through the resistors, so to avoid this issue, the sensor resistance must be high. The sensor will also influence the voltage when the sensor resistance is too low. To avoid this issue, the resistor values for the sensor are chosen factor 1000 higher than the resistance of the output load. When the sensor is implemented in the real system, it is essential to use low resistance cables to prevent voltage drop over the cables.

#### Input voltage sensor

The input voltage of the converter is in a range from 5V to 12.5V. A micro-controller will be measuring the voltage with an ADC with a maximum input voltage of 5 volts.

With a margin of 10% over the converter input voltage, the ratio  $R_1/R_2$  must be around 13.2/5. The resistors chosen for the voltage sensor a  $R_1 = 4.9k\Omega$  and  $R_2 = 13.2k\Omega$ . Both with a very low tolerance of 0.1%.

This results in a sensitivity of 269mV/V and a maximum deviation of 0.4mV.

#### Output voltage sensor

The output voltage of the converter is in a range from 12.5*V* to 21*V*. The voltage will be measured by a micro-controller with an ADC with a maximum input voltage of 5 volts.

With a margin of 10% over the converter output voltage, the ratio  $R_1/R_2$  must be around 23.1/5. The resistors chosen for the voltage sensor are  $R_1 = 4.9k\Omega$  and  $R_2 = 24k\Omega$ . Both with a very low tolerance of 0.1%.

This results in a sensitivity of 170mV/V and a maximum deviation of 0.3mV.

#### Current sensor

Current sensing requires a low resistance to be as accurate as possible and avoid power losses. Resistive solutions to current sensing result in a higher power loss than magnetic solutions. For this reason, a (magnetic) hall sensor was chosen as the current sensing method. The current range for the input and output of the converter is from 0 to 7*A*. The current sensor applied in the power converter is the Allegro ACS724LLCTR-10AU-T. This is a digital sensor with a linear output for a current range till 10*A* and an accuracy of 400mV/A. The sensor has a very low conductor resistance of  $1.2m\Omega$  and has an input voltage of 5V and an input current around 10mA. The UBEC will supply this sensor.

#### 5.2.3. Battery protection

Although even while the UAV is cruising, the energy consumption is higher than the maximum energy yield, the battery still needs to be protected in case the PV is still delivering power when the UAV is on stand-by. As such, in order to prevent overcharging of the battery, a method needs to be devised to cut off the PV power. For this, a possibility would be to implement a controllable switch such as a MOSFET. However, it is also possible to either let the PV cells operate in short- or open-circuit mode. This results in the PV operating at a very low efficiency and thus protects the battery from overcharging without

the need of an extra component. In short circuit mode,  $I_{sc}$  will run through the ESR of the inductor and  $R_{on}$  of the MOSFET. This will result in a small induced voltage from this, which will saturate the inductor and continuously put the MOSFET under load. The open-circuit operation mode is, therefore, better suited for this. As the potential in the right-hand side of the circuit is 21*V* when the battery is fully charged, it is much higher than the open-circuit voltage of the PV cells in series and will try to let a current flow through the diode, which will be blocked. Still, a reverse bias current will flow, but as this current is very low ( $\mu A$ ) it is not a problem for the PV system.

#### 5.2.4. Peripherals

#### **MOSFET** driver

The function of the MOSFET driver is to optimize the switching performances. Because the driver is not within the scope of this project, only the requirements will be indicated.

First, the driver must have a switching frequency of 150kHz. When the MOSFET is turned on, in an ideal situation, it will only be used in saturation mode. To achieve this mode, the gate-source voltage of the MOSFET must reach a higher voltage than the Miller plateau voltage of the MOSFET. When the driver voltage does not overcome the Miller plateau voltage, the MOSFET stays in linear or resistive mode what causes high energy losses in the MOSFET. Further increase of the gate-source voltage gives a small decrease in on-resistance  $R_{ds}$  and a linear increase of the gate charge  $Q_G$ . In order to obtain the highest efficiency, the optimal driver voltage must be determined.

Figure 11 of the MOSFET datasheet in Appendix F.1 shows the relationship between the on-resistance as a function of the drain current. The drain current will remain below 10A in the converter. Also illustrated in the figure, the  $R_{DS}$  is constant for every chosen driver voltage in this operating region. After that, the figure illustrates the decreasing on-resistance when increasing the gate voltage. Figure 13 of the datasheet shows the gate capacity in comparison to the gate-source voltage. The Miller plateau is reached after a voltage of around 3V.

Because the resulting switching power losses for the chosen MOSFET are higher than the losses in the on-resistance, it was decided to optimize the driver for a minimum gate capacity. This is why the driver must be optimized to the minimal gate-source voltage after the MOSFET reaches the saturation mode. On the other hand, the power loss will increase dramatically when the gate voltage stays low and does not reach the optimum on-resistance. This could be caused by voltage drops in the source inductor, gate resistances or temperature changes. To be very sure this won't happen, the driver voltage must be 4.5*V*.

The last requirement for the MOSFET driver is the minimum peak current. When the MOSFET turnedon or turned off, the MOSFET driver must be able to supply all the requested power to charging and discharging the gate capacitance. In order to minimize the power losses, the switching period must be minimized when the MOSFET carries a drain-current for a relatively high  $R_{DS}$ . Figure 7 of the MOSFET datasheet shows an example for the on-resistance as a function of gate-source voltage what illustrates the high on-resistance of the MOSFET before it reaches the saturation mode. Equations 5.12 and 5.13 show an approximation for the two most critical time periods when the MOSFET is switching through the linear and Miller-plateau region [4]. These equations confirm that, when the driver has an insufficient output current range, the driver will decrease the switching time in the MOSFET, which results in an increase in power losses.

For simplicity, the peak current is empirically determined by LTspice simulations. These simulations show that the maximum gate peak current can reach around 800mA. Because this current could reach higher ranges by temperature changes and higher drain-source voltages, a safety margin must be included. The driver should be able to deliver a peak current of 1000mA.

$$t_{2} = C_{ISS} \cdot \frac{V_{GS,Miller} - V_{TH}}{I_{G2}}$$
(5.12)  $t_{3} = C_{iss} \cdot \frac{V_{DS,off}}{I_{G3}}$ (5.13)

For resonance reduction in the gate, the combined resistances of the driver with gate must conform equation 5.14 [4]. The source inductance  $L_s$  must be determined after the converter is real implemented on a circuit board. The  $C_{iss}$  and  $R_{gi}$  variables are given in the datasheet of the MOSFET.

$$R_{gate} + R_{driver} = 2 \cdot \sqrt{\frac{L_s}{C_i ss} - R_{g,i}}$$
(5.14)

#### 5.3. LTSpice model

LT spice is preferred to simulate the electrical circuit. LTSpice is a high-performance simulation software from Analog Devices specific for analog circuits. Besides that, for component models, the software is supported by many electrical component manufacturers, which results in a significant advantage over MATLAB or Simulink. The software is also free to use, making the simulation model easily accessible by other people.

The LT spice boost converter model is shown in Appendix 5.7. The used PV model, on the left, is showed in Appendix D.1. The complete LT spice model with settings and parameters can be found in Appendix D.2.



#### 5.4. System integration

For the real implementation, figure 5.8 shows the connections between the power converter and the rest of the systems. The input power has two connections, the positive and negative outputs of the solar panels. The output can be connected to the UBEC or the battery. But because the goal of the project is to increase the flight range, the power converter must be connected to the UBEC, reducing wire resistances. In order to supply the current sensors, the converter needs an input voltage of 5 volts, powered by the UBEC. The power converter has four output signals from the sensors and needs one input signal from the MPPT controller.

Because the results of the solar powered UAV are not tested in real but by simulations, the power converter is implemented in a Simulink model showed in Appendix D.3. The implemented power converter model, showed in Appendix D.4, is simplified to a lookup table to reduce the overall simulation time. These table contains all efficiencies for different situations. These efficiencies can be obtained from three input variables: the PV input power, the switch duty cycle from the MOSFET driver, and the (battery) output voltage. Because the output voltage of the converter is fixed by the battery voltage, is the output of the power converter transposed to a current. This amount of current is added in the system by a variable current source.

The conversion from the LTspice simulation data to the lookup table is done using the Matlab code showed in Appendix E.3 followed by function codes. Because the LTspice simulations will be done in different steps and different computers, the input date files can differ in size or the order of signal sequences. To avoid mistakes being made, the code is automated and adaptive when it loads the data into Matlab. After that, the efficiencies will be calculated. An critical remark, the data from LTspice

are not in constant separated time steps. This issue can be solved by integrating the input and output powers over time, as illustrated in figure 5.9. This results in the approximated in and out flowing energies through the converter over a pre-defined timestep. Dividing the output energy by the input energy results in the converter efficiency. When this job is done, the results will be added to the (existing) lookup table and the data conversion will be finished. Afterwards the lookup table will be saved for the next simulation file.

After converting all data to the efficiency lookup table, the missing data points must be interpolated before it is ready for the Simulink model. The Matlab code to interpolate the lookup table results can be found in Appendix E.4.



Figure 5.8: Schematic for real implementation



Figure 5.9: Energy calculation

# 6 Validation

#### 6.1. Introduction

The simulation results of the power converter can be found in this chapter. The ideal simulations are done to get a feeling with the system and to understand the parasitic effects of the real components in the non-ideal simulations. In the non-ideal simulation, the real components are implemented. The resulting efficiencies of the non-ideal power converter are integrated using a lookup table in the Simulink drone simulation.

#### **6.1.1. Simulation results**

#### LTSpice simulation results

Due to the limited time available, approximately 10000 runs have been done for the steps specified Table 6.1. For the duty cycle, a minimum duty cycle of 0 and maximum duty cycle of 0.60, for 8*V* to 21*V* conversion, with a safety margin have been chosen.

Table 6.1: Stepped parameter values	Table 6.1:	Stepped	parameter valu	es
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Parameter	Values
Output voltage V <sub>bat</sub>	12, 5 - 21V with a step size of $2.125V$
Irradiance (G)	$50 - 1000$ and $25 - 475 W/m^2$ with a step size of 50, additional step with value $1W/m^2$
Duty cycle (D)	0 - 0.60 with a step size of 0.01

**Power losses** The total power loss in the circuit can be expanded to the power losses in the individual components. In Table 6.2, two possible scenarios are considered at its MPP at the minimum, nominal, and maximum output voltage. For the first scenario, an irradiance of  $1000W/m^2$  is considered. From Figure 1.2, it is assumed that the average expected irradiance in June is approximately  $450W/m^2$ . Therefore, this value was chosen for the second scenario.

Table 6.2 shows results corresponding to the expectation of the converter; It is shown that the inductor and diode losses decrease but also that the capacitor and MOSFET power losses increase as the output voltage rises. The higher diode losses at a lower voltage are caused by the lower duty cycle, which leads to a larger time interval in which the diode will conduct. For the MOSFET, this is the exact opposite, as the duty cycle increases, it will conduct for a longer time interval, which leads to increased losses due to the  $R_{on}$ . Furthermore, the inductor losses decrease with a higher duty cycle, as the voltage is divided over the ESR of the inductor and the  $R_{on}$  of the MOSFET, instead of the much lower ESR of the capacitor. Lastly, a higher potential across the capacitor leads to increased losses at a higher voltage. These capacitor losses are minimal compared to other losses in the circuit due to a relatively low ESR, even when the output voltage is increased.

Duty Cycle	$V_{out}$ [V]	Iout [A]	Inductor	Capacitor	Diode	MOSFET	Sensor $(I_{in})$	Sensor (Iout)	Sensor (V <sub>in</sub> )	Sensor (Vout)	Total loss
G = 1000W	$V/m^2, V_{in} = 1$										
0.08	12.5	5.48	1.39W	0.02W	2.23W	0.12W	0.04W	0.04W	0.01W	< 0.01W	3.88W
0.35	18.5	3.72	1.30W	0.10W	1.52W	0.52W	0.04W	0.03W	0.01W	0.01W	3.52W
0.43	21	3.43	1.27W	0.12W	1.32W	0.68W	0.04W	0.02W	0.01W	0.01W	3.47W
G = 450W/2	$m^2, V_{in} = 12$	]									
0.06	12.5	2.53	0.29W	< 0.01W	0.92W	0.10W	0.01W	0.01W	0.01W	< 0.01W	1.34W
0.35	18.5	1.69	0.26W	0.03W	0.62W	0.25W	0.01W	< 0.01W	0.01W	< 0.01W	1.19W
0.42	21	1.46	0.22W	0.04W	0.54W	0.31W	0.01W	< 0.01W	0.01W	0.01W	1.15W

Table 6.2: Power losses per component at MPP at STC irradiance and average expected irradiance in June in the Netherlands

**Input current ripple** The specified input ripple was to be around 30%. This percentage had been chosen, such that the inductor operates in CCM under average operating conditions but does not take long to reach steady-state, since the input power is variable over time, and the converter is required to respond quickly to changes. According to the inductor calculations, the ripple is at its highest when the converter is supplied with maximum power, and the difference in voltage potential is at its maximum as well. In Figure 6.1, the maximum input ripple is shown for these maximized values. It is seen that the input current ripple has a maximum value of 1.6*A*, which corresponds to 25, 6% of the input current. The lower percentage is due to the input capacitor that has been added.



Figure 6.1: Maximum inductor ripple current

**Output voltage ripple** To check whether the output voltage ripple meets the specified value, the point at which the maximum voltage ripple would occur is taken for measuring this. In Figure 5.2a, it is shown that the maximum capacitance needed occurs at 18V, which corresponds to the highest output voltage ripple. At 18V, Figure 6.2a shows the voltage ripple at the highest possible input power from the PV system, which corresponds to a duty cycle of 0.33 at  $V_{MPP} = 12.046V \approx 12V$ . It clear from this figure that a peak-to-peak voltage of 18.05 - 17.91 = 0.14V is present. This leads to a ripple percentage of 0.78%. Unfortunately, this is a little bit higher than the specified value. In Figure 6.2b, the output voltage ripple for an ideal circuit is shown. This figure shows that the theoretical calculations were indeed correct, as it has only a ripple of 0.46%. The higher peak-to-peak voltage in the non-ideal model is a result of the parasitic properties of the capacitor, due to the voltage across the resistor in series with the capacitor, the capacitor voltage is actually reaching for a voltage that is a little lower than the battery, or output, voltage when charging. When the switch connects the input of the diode to the ground, the current running through the capacitor will reverse and cause the voltage polarity of the resistor to change, which leads to a small voltage drop at the output terminal. However, it can also be seen that the slope of the voltage decrease, when discharging, is less steep due to the parasitic resistance.



Figure 6.2: Ideal and non-ideal circuit output voltages

#### Efficiency calculations and interpolation

The code used for importing the data export from LTSpice can be found in Appendix E.1. Figure 6.3 shows the simulated data points. It is shown that the converter's efficiency decreases quickly for an input power below 1W, which is also shown in Figure 6.5b. The Figures within Figure 6.5 are a 2D representation of the simulated datapoints, as seen in Figure 6.4. For higher input powers, an average efficiency of 95.6% is found. This amply satisfies the requirement of a converter efficiency of 85%. It should also be noted that the efficiency decreases after approximately 8W input power, due to the increased current running through the component resistances.



Figure 6.3: Acquired data from the efficiency calculation in Matlab

#### Duty cycle

The duty cycle influences the operating point of the PV system, which influences the yield of the PV system. In Figure 6.5a, this influence is shown. It should also be noted that the operating mode of the converter will transition to DCM for low input power and current (< 1A). However, in Figure 6.5b, it is also shown that no notable difference is observable in terms of efficiency when the converter changes its operating mode. At an efficiency below 90%, five curves for the five different battery voltages are shown with a dip in efficiency. These dips occur at very low input powers, which can be seen from Figure 6.3. The lowest battery voltage shows the best efficiency, which is due to a better matched power point, since at a low input power and duty cycle, the input voltage is also low. In Figure 6.4 is also seen that the respective voltages each have a certain duty cycle at which the PV system it is able to deliver more power. At this point, the maximum power point is reached. At higher and lower duty cycles, a lesser input power is reached. From this, a conclusion can be derived that at this point, the maximum input power is delivered.



Figure 6.4: Input power versus the output power versus the duty cycle with partial interpolation



Figure 6.5: Comparison on the influence of the input power and duty cycle on the efficiency

#### Output voltage

Another factor to be taken into account is the output voltage. The battery voltage range has been separated into 5 values for simulation. The resulting efficiency figures are shown in Figure 6.6. Here, similar efficiency spectra are shown. In Figure 6.6a, the overlay of the Figures 6.6(b-f) is shown. This shows that the interpolation is not always representing an efficient point of operation of the PV. For example, a duty cycle of 0.60 in combination with a battery voltage of 12.5V will result in a low power point. This would correspond to an input voltage of approximately 5V and, according to the PV specifications for an irradiance of  $1000W/m^2$ ,  $V_{MPP} = 12.046$ . The input power at this point would be 31.4W instead of the around 70W, that is shown in the graph. Therefore, the MPPT algorithm will not move the operating point in this region in the general simulations.



Figure 6.6: Interpolated efficiencies at several output voltages versus the duty cycle and input power

Comparing the input and output power shows another interesting detail. In Figure 6.7a, the data points that have been acquired from the simulation are shown without interpolation. From this figure, it is seen that higher output powers at higher duty cycles are acquired at a battery voltage of 21*V*. The interpolated data is shown in Figure 6.7b, where the interpolation shows that the higher output voltages for the most part also result in higher output power. The most apparent reason for this is that at a higher output voltage has a lesser effect on the efficiency, as a higher output voltage results in a lower output current for the same output power. This results in a lower current through the diode and thus also less dissipation as the voltage drop remains relatively constant.



Figure 6.7: Comparison on the performance of the converter input, output power versus the duty cycle

# Discussion

#### 7.1. Limited data points

Due to the limited time available, about 10000 runs have been done for the simulations. The limited amount of runs resulted in a coarse sweep, in which selective ranging of the duty cycle, output voltage, and irradiance had to be applied. I.e. the duty cycle ranges have only been done from 0.00 to 0.60 with a step size of 0.01. A smaller step size would have granted more data points, and from this, it could lead to more accurate results. The same principle goes for the output voltage and irradiances, where also relatively large step sizes have been applied, of 2.125*V* and 25/50*W*/ $m^2$ , respectively. The results between the output voltages have been interpolated in order to grant more data points. However, using this method, unexpected behaviour that occurs in between these points can not be accounted for.

#### 7.2. Sampling method

The sampling method that is shown in Figure 5.9 for determining the efficiency of the converter for some run also has some limitations. As it uses a Riemann sum like approach, the efficiency acquired from this is subject to a small deviation. Comparisons between the Matlab approximation and the LT-Spice integral method have shown that the maximum error in efficiency for this is 0.5%. However, not all samples have been compared, and therefore, it is possible for a larger deviation to exist somewhere.

#### 7.3. Irradiance

In this thesis, it is assumed that the irradiance will never be higher than  $1000W/m^2$ , as is in accordance with Figure 1.2. However, in certain scenarios, the irradiance can be higher than  $1000W/m^2$ . The effect of this higher irradiance on the functioning has not been taken into account. In future research, this could be considered.

#### 7.4. Temperature influence on PV modules

Another potential issue that is not accounted for is the influence of the variation of temperature on the PV cells. In Figure 7.1, the effect of temperature on PV modules is shown for several different temperatures. It shows that as the temperature increases, the short-circuit current increases, and the open-circuit voltage decreases. This may lead to a higher input current, which subsequently can cause a higher output ripple and more losses in components. It should be noted, however, that the increase in current at a higher temperature is tiny. In the figure, it is also seen that the short-circuit currents do not have a great difference. Therefore, the effect might be negligible. Nevertheless, before a definite conclusion on this, this should be researched and taking the temperature into account for future research could be an improvement over the existing model.

#### 7.5. Other non-idealities

For the sake of simplicity, non-idealities such as ESL, ESC, and wiring resistances have been ignored. This may lead to a deviation in results and could be considered for future research.

#### 7.6. Output current sensor

The output current sensor in the model at this point has no particular function within the model. The initial implementation of this sensor was in order to have all possibilities open for an analog fallback circuit. However, since this turned out to not be possible in the given time frame, it has no function. Therefore, however small the losses in the current sensor are, it should have been omitted in the final model, as it does affect the efficiency of the converter.

#### 7.7. Input voltage

Lastly, the assumption has been made that the maximum power point voltage of the PV system will not lie higher than 12.5*V*.It is possible that there exists a better operating point above 12.5*V* in a certain scenario, such as, for example, when the temperature of the PV cell is very low. However unlikely, if in this specific case, the battery voltage is at its lowest, this boost converter will not be able to track the maximum power point properly.



Figure 7.1: Influence of temperature on PV modules (Irradiance:  $1000W/m^2$ ) [9]
## **Conclusions and recommendations**

#### 8.1. Conclusions

In this thesis a boost converter that is able to control the MPP is proposed. This model has integrated current and voltage sensors, at both the input and output in order to provide the necessary measurements for the MPPT controller. It is shown that, according to the simulated model, the converter is able to operate efficiently at an expected efficiency of 95.6%. The peak-to-peak voltage that was simulated turned out to be 0.28% higher that the specified voltage ripple of 0.5% due to the ESR of the capacitor. Although this difference is greater than specified, it did show better smoothing capabilities than the ideal capacitor implementation.

In Appendix B, the results of the final simulation, which has the converter's lookup table integrated, show that at the worst case assumed irradiation the range of the drone is decreased with 4%. In the best case scenario, the range is increased by 83%.

#### 8.2. Recommendations

#### 8.2.1. Diode replacement

Although the efficiency of the topology used in this report is high, it is clear that most of the losses originate from the diode, especially at lower battery voltages with a lower duty cycle. Due to the lack of time, it was not possible to further optimize the circuit by replacing the diode by a MOSFET. However, as can be seen in Figure 8.1, a MOSFET would result in less losses for a output current  $I_{out} < 30A$ . This model does not account for switching losses. However, for future research, a MOSFET should be considered, in order to possibly optimize the efficiency even further.

#### 8.2.2. MOSFET implementation

The selection of the MOSFET is done by ordering the critical values and checking the most significant power loss in the MOSFET. In this design, the MOSFET is optimized for the higher input powers, that's why a low drain source resistance is chosen above a low gate capacity. Another possible improvement over the current model is discussed. More specifically, for choosing a better MOSFET, in order to optimize the power converter efficiency even further.

The resulting power losses of the selected MOSFET could be found in figure 8.3a. This figure illustrates that the constant switching losses are higher than the drain source losses. So finding a better balance between Q and  $R_{DS}$  results in a MOSFET with a higher power efficiency. For illustration, the expected powerlosses at  $V_{ds} = 4.5V$  for the Nexperia PSMN3R3-40YS MOSFET can be found in figure 8.3b. This MOSFET has a better balance between the gate charge and on-resistance what results a decreased total power loss in comparison to the chosen BUK9JOR9-40H MOSFET.

At the end, it is clear from the RQ ratio, described in section 5.2.1.3, when there are low input powers, the gate capacity becomes more dominant. For a better efficiency at low input powers, it can be interesting to implement a second MOSFET with a minimal gate capacitance. Figure 8.2 illustrates the



Figure 8.1: Comparison between a MOSFET (NMOS), PN-Junction diode and



Figure 8.2: Double MOSFET design

design how to implement this second MOSFET. For low input powers, the MOSFET with the optimized gate capacitance is switching the inductor. For higher input powers, the MOSFET with an optimized drain source resistance will operate. This operation could be controlled by the MPPT controller or the MOSFET driver.



(a) Expected powerlosses BUK9J0R9-40E MOSFET @Vds=4.5V



(b) Expected powerlosses PSMN3R3-40YS MOSFET @Vds=4.5V

Figure 8.3: Comparison on the the expected power losses of two different MOSFET types

## A

## Appendix

#### A.1. Terminology

- UAV Unmanned aerial vehicle
- MPP Maximum power point
- MPPT Maximum power point tracking
- ESR Equivalent series resistance
- ESL Equivalent series inductance
- ESC Equivalent series capacitance
- ESC (alt.) Electronic speed controller
- PWM Pulse width modulation
- · MOSFET Metal-oxide-semiconductor field-effect transistor

#### A.2. Symbols

#### A.2.1. General

- $P_{in}$  Input power of the converter [W]
- *r<sub>I</sub>* Current ripple percentage [%]
- *I*<sub>pp</sub> Peak-to-peak current [*A*]
- $r_V$  Voltage ripple percentage [%]
- *V*<sub>pp</sub> Peak-to-peak voltage
- $V_{out}$  Output voltage of the converter [V]
- *V*<sub>bat</sub> Battery voltage [*V*]
- *I*out Output current of the converter [A]
- *I<sub>D</sub>* Diode current [*A*]
- $V_{DF}$  Forward voltage across the diode [V]
- *C*<sub>in</sub> Input capacitance [*F*]
- *C<sub>out</sub>* Output capacitance [*F*]
- E<sub>Store,MAX</sub> Maximum energy capacity to be stored during one cycle [J]
- $E_{L,MAX}$  Maximum energy capacity that can be stored in the inductor [J]

- $E_{C,MAX}$  Maximum energy capacity that can be stored in the input capacitor [J]
- D Duty cycle

#### A.2.2. PV System

- $V_{PV}$  PV system output voltage [V]
- *V*<sub>PV,MAX</sub> PV system maximum output voltage [V]
- *I*<sub>PV</sub> PV system output current [*A*]
- *I*<sub>PV,MAX</sub> PV system maximum output current [A]
- $P_{PV}$  PV system output power [W]
- *P*<sub>PV,MAX</sub> PV system maximum output power [W]
- *V*<sub>MPP</sub> Voltage at the maximum power point voltage [V]
- *I<sub>MPP</sub>* Current at maximum power point [*A*]
- *P*<sub>*MPP*</sub> Power at maximum power point [*W*]
- N<sub>c</sub> Number of PV cells in the PV system
- Voc Open-circuit voltage at STC [V]
- *I<sub>sc</sub>* Short-circuit current at STC [*A*]
- *G* Solar irradiance  $[W/m^2]$

#### A.2.3. MOSFET

- $Q_G$  Gate charge [C]
- $R_{Gate}$  Gate resistance [ $\Omega$ ]
- $R_{G,I}$  Gate input resistance [ $\Omega$ ]
- $R_{on}$  On resistance [ $\Omega$ ] (alt.  $R_{DS}$ )
- $R_{HI}$  Driver 'On' resistance [ $\Omega$ ]
- $R_{LO}$  Driver 'Off' resistance [ $\Omega$ ]
- *V<sub>GS</sub>* Gate-source voltage [*V*]
- V<sub>DS</sub> Drain-source voltage [V]
- *I*<sub>DS</sub> Drain-source current [A]
- *V*<sub>DRV</sub> Driver voltage [V]
- *P*<sub>DRV,ON</sub> Power dissipated by the driver during the "ON" state [*W*]
- *P*<sub>DRV.OFF</sub> Power dissipated by the driver during the "OFF" state [W]
- *P*<sub>DRV</sub> Power dissipated by the driver [*W*]
- $f_{DRV}$  Driver frequency [*Hz*] (alt.  $f_{sw}$ )
- *P<sub>Gate</sub>* Power dissipated by the gate [*W*]
- *P<sub>sw</sub>* Switching power [*W*]
- *f*<sub>sw</sub> Switching frequency [*Hz*] (alt. *f*<sub>DRV</sub>)
- *T<sub>sw</sub>* Switching time [*s*]
- *T*on The time that the MOSFET is switched on [s]

## B

Drone documentation

### **DRONE DOCUMENTATION**

June 19, 2020

– Solar Powered drones – Laura Muntenaar, Martin Geertjes, Rik van der Hoorn, Jasmijn Koning, Jetse Spijkstra, Sjoerd Groot

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### Introduction

With renewables being on the rise, green solutions are being investigated for every market. This thesis revolves around the idea that the flight range and flight time of commercially available drones could possibly be elongated by the addition of solar panels. In order to do this, a drone is proposed which has the ability to accommodate solar panels and thus can provide its on board system with additional power to elongate the flight range.

This thesis is structured as follows. First the UAV type is selected, then the actual drone frame and the additional components are selected. Finally the drone is simulated and final results are elaborated on.

### **UAV** selection

For the UAV selection, it is important to understand which types of UAVs there are. Four types of UAVs are considered and discussed: multi-rotor, single-rotor, fixed-wing and fixed-wing hybrid.

#### 2.1. Multi-rotor

A quadcopter is what is generally thought of when thinking of a UAV, which is a subclass of the multi-rotor class. Typically it is driven by four rotors, where the direct opposite rotors spin in the opposite direction. This type of UAV is the easiest to engineer, since as long as the rotors are spaced in a symmetrical way it will fly without problems. Stability and flexibility are two of the main advantages of a quadcopter; it can suspend itself in the air and take photos from a still position. Furthermore, it can move very flexibly, in any direction at any given time, which may prevent crashes in tightly packed neighbourhoods. Finally it will not need a lot of space for take-off since it can take-off vertically. However, there are also some disadvantages to this design, among which is the fact that it is less-efficient than airplane solutions [6]. Secondly it is difficult to keep the camera angle compared to the ground constant, since the angle of the



Figure 2.1: Quadcopter example [8]

quadcopter changes when moving and hovering. One of the most important advantages is the fact that a quadcopter could have a large area available for the placement of PV panels, which would benefit the purpose of our project. An extensive overview of advantages and disadvantages of a quadcopter are listed in table 2.1.

Table 2.1: Advantages and disadvantages of a quadcopter

Advantages	Disadvantages
Easy to engineer	Less energy efficient than airplane solutions
Can take still photos	Photos while moving changes camera angle
Very flexible movements	Simple square shape is more fragile
Possibly a lot of area for solar panels	Low payload

#### 2.2. Single-rotor

A single-rotor, or helicopter, type of UAV is contrary to the fact that it is called a single-rotor type, two rotors. These rotors consist of a large rotor and a smaller, tail rotor. These types of UAVs are much more efficient than it's multi-rotor counterpart [10], but still less efficient than a fixed-wing solution according to [6]. Besides that, such a solution has little to no suitable room for solar solutions. Therefore, these types will not be further considered since they are not a suitable solution for solar powered UAVs.

#### 2.3. Fixed-wing

Next to this distinction, another type of drone can also be investigated, the fixed-wing. Fixed-wing type UAVs are entirely different in build and using purposes. Where rotor types are



Figure 2.2: Single rotor example [1]

able to suspend themselves in mid-air, fixed-wing UAVs are constantly moving at a certain horizontal speed. They remain at altitude by the lift force generated by the wings, which counteracts the gravitational force. This way, it moves in a more efficient way than a rotor type UAV. This, and the fact that it is always moving, is why it is very suitable for long distance operations, such as mapping. The main disadvantage of a fixed-wing solution is that it is less flexible in moving around mid-air and needs either a runway (a small grass field is usually enough) or some sort of launching base to propel it into the air

Table 2.2: Advantages and disadvantages of a fixed-wing UAV

Advantages	Disadvantages
More energy efficient	No photos from still position / cannot suspend in mid-air
Constant angle while moving (at equal altitude)	Only suitable area for solar panels is on the wings
More robust shape	Needs a runway or launching mechanism
Higher payload	

#### 2.4. Fixed-wing hybrid

The final type researched is a fixed-wing hybrid UAV. This type of UAV has VTOL (Vertical Take-Off and Landing) capabilities and can take-off and land vertically as the name suggests, while in the air flies like a fixedwing UAV. With this design the aim is to combine the benefits of a multi-rotor with the flying efficiency of a fixed-wing UAV. However this leads to a complex design and a bigger chance of complications during flying.



(a) Fixed wing drone [12]

Figure 2.3: Fixed wing vs Fixed wing hybrid

(b) Fixed wing hybrid [7]

#### 2.5. UAV type selection

For the drone selection, a few criteria are important. The table on the next page shows these criteria per section for the four types of drones we distinguished. All the way on the right, the weight per criteria is indicated, with '10' being the most important and '1' being not important at all. From this table it becomes clear that a fixed wing is the best option, especially when the red factors are taken into account. The quadcopter has a low payload capacity and mounting the PV cells would be difficult. For the single-rotor UAV, the placement of the PV cells would also be difficult. The concept of a VTOL UAV is quite new, so the simulation would be difficult to make and the complexity of the design would make it more difficult to make it solar powered. Next to that, it would also be rather expensive.

UAV 1ypes		Multi-rotor	Fixed-wing	Single-rotor	Fixed-wing hybrid	Importance
	Range	short	long	short	long	10
	Speed	limited	variable	variable	variable	4
	Efficiency	low	high	medium	medium	10
General specs	Flight time	20-30 min	couple of hours	higher than multi-rotor	higher than multi-rotor	10
1	Payload capacity	small	medium	high	variable	8
	In air stability/ camera control	high	medium	high	high	8
	Runway	VTOL	yes	VTOL	VTOL	2
Controllability	Skill training	not needed	required	required	required	2
	Controllability	easy	medium	easy	easy	7
	Ability to stay at same height	oke	oke	oke	oke	8
	Maintenance	not often	not often	often	often	2
Robustness	Waterproof	per case	per case	per case	per case	2
	Shockproof	per case	per case	mostly not	per case	4
	Possiblity changing casing	per case	per case	per case	per case	1
	Attachment solar cells	not easy (payload capacity)	easy	low	medium	10
A dinetability	Impact of changes on efficiency	high	low	Low	low	6.5
vujustantiity	Efficiency optimalization	low	high	medium	high	10
	Rotor adjustability	high	Medium/High	high	medium	2
	PV potential/Area	High	Medium/High	None	Medium	8
	Risk of use	possibly	possibly	yes (strong big blades)	possibly	5
	Easily simulatable	medium	yes if drag constants are known	medium	no	8
Othor	Costs	low	medium	high	high	2
OUIGI	Datasheet	per case	Per case	per case	per case	5
	Remarks					
	Link					

#### 2.6. UAV selection

In the last section, the decision was made to design a fixed-wing UAV. To optimize the performance of the drone all the components needed for the drone are chosen manually and described in chapter 3. To select a frame, the payload capacity has to be high enough to carry all the components and a decent camera for albedo mapping. Furthermore, it should be possible to attach PV cells on the frame. There are a lot of frames available, but in the end the frame of the Skywalker X8 was chosen as it was a frame that was commercially available, easily implemented for simulations and has a large wing area. Next to this, it was a relatively cheap UAV. Models of the frame have been found as well to analyse the aerodynamics of the frame needed for modeling of the entire UAV.



Figure 2.4: The Skywalker X8 [15]

The Skywalker X8 is a fixed wing, single rotor UAV. The specifications of the Skywalker X8 are as follows:

Model	Skywalker X8
Wingspan	2122 mm
Length	820 mm
Payload volume	9550 cm <sup>3</sup>
Mean aerodynamic chord	357mm
Max wing curve	8.9 degrees
Typical cruising speed	65 km/hr
Maximum speed	85 km/hr
Body weight	880 g
Maximum take off weight	3200-4200 g

Table 2.3: Frame specifications

The specifications show a large payload volume and high payload capacity for all the components and equipment. Large wings to place the PV cells on, but with a wingspan of 2122mm and the possibility to detach the wings from the body, compact enough for transportation. Finally it is important that there are models available of the frame and aerodynamic specifications can be found or calculated to simulate the characteristics of the frame.

### **Component selection**

In this chapter the different components that were chosen for the implementation of our system are discussed. To complete the skywalker X8, an Auto-pilot, propeller, motor, speed controller, battery and a camera needed to be selected.

#### 3.1. Auto-pilot



Figure 3.1: Chosen Auto-pilot [3]

The autopilot is a mini computer that controls the motor and servos during the flight using sensor and telecommunication hardware. The requirements for our autopilot are the following:

- 1. It is commercially available product.
- 2. Works with other selected components and is easy to implement.
- 3. Can fly stand-alone.
- 4. Easy to set way points/flight route to fly.
- 5. Functionality to implement safety instructions.

Because the the auto-pilot will not be simulated in Simulink and no physical implementation will be made. Little research was done for this component. The above mentioned requirements were taken into account but no comparison was made between all the available auto-pilots on the commercial market. For our application we have chosen the Holybro Pixhawk 4, because this device is affordable and meets our requirements. See the appendix A.2 for the further specifications.

#### 3.2. Propeller

The propeller is able to convert the rotational energy into thrust force. Generally, the greater the propeller area, the higher the thrust force and propeller efficiency, as long as the motor is able to supply the required torque. Also, the propeller needs to fit on the UAV. The recommended maximum propeller size for the frame that was chosen, is  $11" \times 7"$ , so for the propeller this size is taken. To have useful propeller data, a Graupner Nylon  $11" \times 7"$ , which is covered in PropCalc [17], was used. The test data provided by this program is shown in figure 3.3, where efficiency is plotted against the speed.



Figure 3.2: Chosen Propeller [5]



Figure 3.3: Propeller efficiency of the Graupner Nylon 11" x 7"[17]

#### **3.3. Motor**

For the motor, the goal is not to create the most efficient drone but to prolong the flight time as long as possible. The most important consideration that is taken into account is the KV. The KV, or RPM/V, depicts how much the RPM of the motor will increase per volt. The KV is linearly dependent on the torque, as is seen in Equation 3.1, where  $K_t$  is the torque constant. Furthermore, it is beneficial to have the motor operate at a higher voltage, as this results in a higher efficiency [13].

$$K_V = \frac{1}{K_t}$$

(3.1) Figure 3.4: Chosen Motor [2]

Subsequently, higher torque is needed to drive a higher diameter propeller as a higher diameter propeller has more air resistance or, in other words, can propell more air around. This is why the lowest recommended KV motor was chosen, which is the Turnigy Aerodrive 4250 KV500. However, next to this, the motor should also be able to deliver a power of at least 200W, as this is the expected consummated power by keeping the UAV in the air. The chosen model's specifications are shown in table 3.1.

Specification	Unit
Motor model	Turnigy Aerodrive 4250 KV500
KV	500 RPM/V
Maximum power (5S)	1350W
Maximum current	57A
Burst current	70A
Weight	269g

Table 3.1: Motor specifications

#### 3.4. Speed controller

A speed controller is responsible for the three-phase control of the motor. Therefore it must be able to handle the maximum rated current of the motor, which is 60 A. Other requirements regarding the selection of the speed controller is that it should be light weighted and commercially available.

For the selection of the speed controller the following speed controllers were condisered; the Aerostar RVS 60A Electronic Speed Controller w/Reverse Function 5A BEC, YEP 60A (2 6S) SBEC Brushless Speed Controller, Turnigy Plush-32 60A Speed Controller w/BEC and the HobbyKing 60A ESC 4A SBEC. From these options, the Aerostar RVS speed controller was chosen for a number of reasons. First, this controller is able to handle a 60A motor rating that is able to handle the voltage of a five series battery cell configuration. It is also light weight in com-



Figure 3.5: Chosen Speed controller[11]

parison with the Yep en Turnigy speed controllers, namely 20 gram. Furthermore, it has a build-in Battery Eliminator Circuit (BEC) and the efficiency is known.

Specification	Unit
Motor model	Aerostar RVS 60A Electronic Speed Controller w/Reverse Function 5A BEC
Maximum current	60A
Weight	44g
Size	56 x 30 x 14mm
Efficiency	83-87% (Higher efficiency at lower voltages)

The specifications of the chosen speed controller can be found in table 3.2

Table 3.2: ESC specifications

#### 3.5. Battery

When choosing a battery, a trade-off regarding the battery size should be made. A bigger battery capacity will not always extend the flight range because the weight will also impact the payload and so the power consumption. Therefore, a light weighted but high capacity battery is preferred and a high energy to mass ratio is wanted. The battery also needs to be sufficient for the high current demands of the motor, it must be commercial available and the characteristics of the battery need to be specified for the simulations.



Figure 3.6: Chosen battery[14]

When taking these specifications into consideration, it can be found that li-ion batteries offer the highest energy density and have a storage efficiency close to 100%. The lithium nickel cobalt manganese-oxide (NCM) compound meets the requirements for high specific capacity (mAh/g) and this technology is commercial available.

Within the different commercial NCM li-ion cells, the following types are compared; the Sanyo NCR2070C, Molicel INR21700-P42A, the Samsung INR21700-40T, the Samsung INR21700-30T and the Molicel INR20700A. There was found a good energy/mass ratio of 0.224wh per gram for the Molicel INR21700-P42A. This cell is also able to deliver a high maximum output current of 45A.

After comparing different configurations of this battery cell, the configuration with 5 cells in series and 3 of these series configurations in parallel meets the required capacity, input voltage and the continues current.

Also the weight, volume and expected price fits in the range of the drone specifications. Therefore, this configuration is chosen. The specification of this battery pack can be found in table 3.3. For every series string, a safety component is added that prevents high charge and discharge currents flowing trough the battery. The specifications of this safety component can be found in the appendix.

Specification	unit
Battery model	Molicel INR21700-P42A
Voltage	18.5V
Capacity	12000 mAh
Max continues current	135 A
Weight	990 gr
Volume	115.8 cm3

Table 3.3: Battery Pack specs

#### 3.6. Camera

To be able to create Albedo maps, a camera is needed that is able to create high quality images. Since there are flight-height restrictions imposed by the Dutch government that withholds UAV's to fly higher than certain heights, the camera should be able to create quality images around 120m. To be able to do this it should have a maximum ground spatial distance (GSD) of 20cm at a shutter time less or equal to 1/1000s. Another requirement is that the camera should be able to create RAW linear images to make sure the images are easily edited for Albedo map creating. The camera should have stabilisation in order to be less affected by the turbulence of the UAV and lastly, it should be lightweight and as cheap as possible. The options that were considered were the GoPro MAX, HERO8 Black, AKASO V50, Insta360 ONE X, HERO 7 Black and the Sony Alpha-6000 because these camera's are all compact while having considerably good specifications. However, based on above mentioned specifications the The Sony Alpha-6000 camera with SELP1650 lens is chosen.



Figure 3.7: Chosen Camera [4]

Specification	unit
Camera model	Sony Alpha-6000 camera with SELP1650 lens
Weight	460 gr
Pixels	24.3 MP
Frames per second	11 (in burst mode)
Shutter time	1/4000 s
Maximum field of view	83 degree
Aperture	f/2.8
ISO	100-25600
focal point	15-60mm
focal length	35-75mm
Image output	RAW and JPEG

The specifications of the chosen camera can be found in table 3.4

Table 3.4: Camera specifications

### System overview

#### 4.1. Introduction

This chapter describes the system overview. First an overview of the weight and cost of the UAV is given. Then the selected components and the selected drone are 3D modeled to show the fit of the described system. Then the inter-component wiring is described and the total system overview is explained.

#### 4.2. Costs

To ensure that the UAV will fly as efficiently as possible, the system needs to be optimized for weight. Next to this, as a business plan has to be generated, the system also has to be optimized for cost. The total weight and cost of all the components and the UAV are described in table 4.1.

Component	Weight (grams)	Estimation (€)	Costs incl. tax (€)
Body	880	140	239.9
Motor	269	40	38.37
ESC	60	30	30.74
Servo (2x)	40	15	14.22
Propellor	20	15	8.95
Auto pilot/controller	37	180	159.3
Gps for auto pilot	32		
Receiver FrSky R9	5.8	30	29.95
Camera	344	550	650
lens for camera	116		
Battery (18V, 12Ah = 216Wh)	1050	86	123
3x Battery protection	45		45
Wiring and mounting	100		
FrSky Taranis X9 Lite remote ctrl	nvt	120	109
PV panels	616.6	nvt	213.32
Total	3615.4	1206	1661.75

Table 4.1: Total weight and cost of UAV

#### 4.3. Component fit

After the selection of all the components and the drone, a model of the components can be fitted into the drone. Although the physical UAV will not be created, an estimate of the component fit and thus the weight distribution can be made. Table 4.2 shows the components referenced in chapter 3 and their respective dimensions.

Table 4.2: Overview of the component dimensions

Component	Length (cm)	Width (cm)	Height (cm)	Total volume (cm <sup>2</sup> )	<b>Total volume + 2cm marge</b> $(cm^2)$
Battery	13,2	6,6	7	609.84	1176.48
Camera	12.0	6.7	4.5	361.8	791.7
Motor	not relevant (own space)	not relevant(own space)	not relevant(own space)		
Speed controller	5.6	3.0cm	1.4	23.52	129.2
Propellor	not relevant (own space)	not relevant (own space)	not relevant (own space)		
**DC-DC controller(power	4.5	2.5	2.0	22.5	117
management solar panels)	4.5	2.5	2.0	22.5	117
*micro controller - power management	10.16	5.33	2.0	108.3056	356.5312
flight controller(+ auto pilot)	5.5	3.8	1.55	32.395	154.425
**power system	10	5	2	100	336
GPS Module	5.0	5.0	2.5	62.5	220.5
TOTAL				1320.8606	3281.8362

Figure 4.1 shows the 3D fit of the components in the UAV in solid works. Here, the battery is placed more towards the front (the main box visible) to center the weight of the components around the center of mass of the UAV. Thus stabilizing the UAV.





(a) Top view of components in UAV

(b) From Left to right: Camera, Battery, Power system, Autopilot, Speed Controller, DC-DC

Figure 4.1: Top view and closed view of components in the uav

#### 4.4. Inter-Component relationship

Figure 4.2 shows the general overview of the total system. The power supplies are displayed in blue. On the left, the battery cells are connected to the battery management system. This manages the internal cell voltage and ensures enough power is supplied to the secondary systems, such as the ESC and BEC, which is actually integrated inside the ESC but is displayed separately for the completeness of the model. Below, the PV system is interconnected with the power converter. The power converter, subsequently, is supplied with the necessary control data from the (MPPT) micro controller.



Figure 4.2: System overview diagram

### Flight simulation

#### 5.1. Introduction

This chapter describes the modelling of the drone. To accurately represent the drone the aerodynamic forces need to be modelled as they influence the power and energy needed for flight. First the general modelling of an air vehicle is explained, then the implementation of the Aerodynamic model in simulink is elaborated on. Next the integration of the electrical And Aerodynamic model is explained. At last, the effect of the aircraft mass and aircraft angle is investigated with the implementation of a mass sweep and an angle sweep.

#### 5.2. Aerodynamic model

When looking at an aircraft, 4 main forces can be identified. The Lift, Drag, Thrust and gravitation force indicate the way the aircraft will fly. Figure 5.1 shows these four forces and their respective orientation with the UAV.

The Lift force is the result of the airflow above and over the wing lowering the air pressure on the top of the wing, which pulls the wing up-wards and will thus push the aircraft up-wards. It is generally seen as the force which counteracts gravity. Lift force F<sub>L</sub> Thrust force F<sub>Th</sub> Angle of attack α / Flight path angle p Gravitational force F<sub>G</sub>

The drag force is the result of the air resistance which the UAV encounters whilst

Figure 5.1: Forces on the plane

moving through the air. This can be viewed as the amount of force it takes to move the UAV through air. The counteract force to drag is the thrust force. The thrust force indicates the force the motor provides to move the aircraft forward.

Figure 5.1 also shows the angle of attack and the flight path angle. The flight path angle ( $\gamma$ ) is the angle of the aircraft path with respect to the ground. The angle of attack ( $\alpha$ ) is the angle between the incoming air the aircraft experiences with respect to the angle of the aircraft.

The aerodynamic model of the airplane is based on the measurement results and aerodynamic simulations presented in [9] and the electric aircraft Model in Simscape [18].

#### Simulink implementation of the aerodynamics

The main goal of the aerodynamic model is to determine the power that the motor should provide. The architecture of the model is similar to the implementation of [18] however our model does take the angle of attack into account for determining the lift and drag coefficients as seen in Figure 5.2.



Figure 5.2: Aerodynamics overview

#### Aerodynamic forces

Table 5.1: Aerodynamic coefficients used as measured by [9]

$C_{L_0}$	0.0867
$C_{L_{\alpha}}$	4.0203
$C_{D_0}$	0.0197
$C_{D_{\alpha 1}}$	0.0791
$C_{D_{\alpha^2}}$	1.0555

First, the lift and drag coefficients,  $C_L$  and  $C_D$  respectively, are derived from the angle of attack  $\alpha$  according to Equation 5.1 and 5.2.

$$C_L = C_{L_0} + C_{L_\alpha} \alpha \tag{5.1}$$

$$C_D = C_{D_0} + C_{D_{\alpha 1}} \alpha + C_{D_{\alpha 2}} \alpha^2;$$
(5.2)

The lift and drag forces,  $F_L$  and  $F_D$  respectively, are then calculated using the airspeed  $V_a$ , air pressure  $\rho$ , wing area  $S_{wing}$  and the coefficients described above.

$$F_L = \frac{1}{2}\rho V_a^2 S_{wing} C_L \tag{5.3}$$

$$F_D = \frac{1}{2}\rho V_a^2 S_{wing} C_D \tag{5.4}$$

#### Forces reference system

Three reference frames are present in the simulation as seen in Figure 5.3. Firstly the inertial frame (green) is relative to the ground. This is used for the gravity that is always pointing down. The stability frame (blue) is in the orientation that the airplane is flying. This is the orientation that is relevant to calculate if the airplane accelerates/decelerates and if the lift and motor force counteract the gravity. The third reference frame is the body frame (black) pointing in the same direction as the airplane. This differs from the stability frame because of the angle of attack of the airplane.

The forces of lift, drag and the motor are in the body frame reference. These are therefore rotated by the angle  $\alpha$  to go from the body frame to the stability frame.



Figure 5.3: Reference systems of Aerodynamic forces

#### Thrust command and longitudal dynamics

The required thrust  $F_{th}$ , is calculated based on the difference between aerodynamic lift and opposing force of the weight. The propulsion power P, is determined by multiplying the thrust force by the airspeed.

$$F_{th} = \frac{1}{\sin(\alpha)} (F_G^S - F_L^S) \tag{5.5}$$

$$P = F_{th} V_a \tag{5.6}$$

Where  $F_G^S F_L^S$  are the gravitational force with respect to the stability frame visible in 5.3. From this thrust, the drag and gravitational forces in the stability reference frame,  $F_{Gx}^S$  and  $F_D^S$  respectively and the total airplane mass *m*, the acceleration  $\dot{v}$  is determined. The airspeed is determined by integrating this acceleration.

$$\dot{\nu} = -F_{Gx}^{S} - \frac{F_{D}^{S}}{m} + \frac{F_{th_{x}}^{S}}{m}$$
(5.7)

By integrating the airspeed and taking the flightpath angle into account the height of the plane is tracked and the corresponding air density is determined.

If the airplane didn't had enough lift to counteract the gravity the motor thrust will become larger, this will increase the airplane velocity and this results in more lift generated. This feedback loop ensures that the aircraft will acquire a steady state, ascending or descending according to the flight path angle.

#### Mass and angle of attack sweep

Figure 5.4 was obtained by simulating the airplane model. On the vertical axis the Maximum flight range, the maximum flight duration the cruising speed and the cruising power draw is shown. In each simulation the airplane starts off thrown at a height of 2 meter and an airspeed of 2 m/s. The plane is instructed to climb to a height of 120m and than to maintain level flight until the battery is empty.

This simulation is performed for different airplane weights and different alpha values to obtain the maximum range for all weight configurations.





Figure 5.4: Simulation results under varying weight and angle of attack (alpha in degrees)

### **Final simulation**

#### **6.1. Introduction**

This chapter elaborates on the final simulation done. In this simulation, all the sub-systems were integrated into one big simulation. Fist the simulink is explained after which the results are discussed.

#### 6.2. Simulink Implementation

The overview of the final simulation can be viewed in 6.1. Here, on the left, the battery and the PV power are inputted which provide power to the system. The PV power can be turned on or off to simulate a cloudy day or a sunny day. The on the top right, the flight controller and the aerodynamic model can be found. These simulate the take-off, level flight and landing by adjusting the flight path angle in the aerodynamic model.



Figure 6.1: Final electrical simulation of the aircraft

The middle block shows the propeller and the motor. The aerodynamic model output the power needed to stay in the air. The propeller and motor then adjust accordingly to provide the necessary torque. Lastly, the bottom block, represents all the secondary systems which need power like the auto-pilot or the speed controller.

#### 6.3. Results of final simulation

Figure 6.2 shows the additional flight range that the PV panels provide. On the left, the worst case scenario is displayed in which the solar irradiance is  $129 W/m^2$  and the temp equals 21.1 degrees Celsius. Here the flight range is decreased with 4% by the addition of PV as the PV panels add more weight and dus more power is needed than the PV panels deliver. On the Right, the best case scenario is displayed. In this case, the the solar irradiance is 782  $W/m^2$  and the temp equals 18.5 degrees Celsius. Here the flight range is increased by 83%.



Figure 6.2: Addition to flight range from PV under different weather scenario's

Figure 6.3 shows the overall results of he total simulation, this figure can be found as a larger figure in appendix B.2. In the figure, the purple line represents the simulation in which PV panels are used. The green line represents the simulation without the PV panels. The right On he top left, the increased flight time becomes visible. We can see that the drone will fly for a long period of time before it lands. On the bottom left, it becomes visible that the discharging of the battery goes slower when PV panels are added.



Figure 6.3: Comparison of flight path with and without PV

On the left, the difference in range becomes clear. In this particular situation, as mentioned in 6.2, the solar panels added 83% increase in range.

#### 6.4. Flight path calculation

The flight path of the UAV is dependent on UAV and camera specifications based on the program of requirements, its path should be able generate high quality images while being time efficient. The image will have a good quality when the ground sampling distance (GSD) is lower than 20 cm [16]. The GSD can be calculated using formula 6.1.

$$GSD = \frac{p * H * 1000}{f * cosd(0.5 * FOV)}$$
(6.1)

Here p is the pixel size (micron), H is the flight height (m), f is the focal length (mm) and FOV is the field of view (degrees). The camera specifications can be found in section 3.6. The pixel size can be calculated using formula 6.2

$$p = \frac{s_w}{p_w} + \frac{\frac{s_h}{p_h}}{2} \tag{6.2}$$

Here  $s_w$  and  $s_h$  are the sensor width and sensor height respectively and  $p_w$  and  $p_h$  are the pixel width and pixel height respectively. Based on these calculations, the GSD is 17.93cm when flying at a height of 120m, which is sufficient. Now, another specification that is needed to guarantee the quality of the generated albedo map is image overlapping. There is a forward overlap of images needed of 65% and a sideward overlap of 40% [16]. The following formula's are used.

$$d_f max = \frac{v}{f \, ps * 3600} \tag{6.3}$$

$$d_{s}need = \frac{\frac{(1-o_{s}ide)*H*s_{w}}{f}}{1000}$$
(6.4)

In formula 6.3 the forward distance between every picture  $d_{fmax}$  (m) is calculated based on the speed v of the UAV (m/s) and the frames per second fps (1/s), and is 1.6 meter. The needed side overlap  $d_{sneed}$  is calculated in formula 6.4 to be 48.5 meters. Lastly, in order to create a flight path, the turning radius of the UAV needs to be calculated. This is done via the following formula's.

$$R_f = \frac{(0.54 * v)^2}{11.26 * tand(30)}$$
(6.5)

$$R = \frac{\frac{R_f}{3.28}}{1000} \tag{6.6}$$

$$l_c = 0.5 * \pi * R \tag{6.7}$$

Here  $R_f$  is the possible radius in foot, which is dependent of the velocity v of the UAV. In formula 6.6, the radius is transformed from foot to kilometer. In formula 6.7 the minimum length of the turn is calculated. This is 363 meter. Based on previously mentioned outcomes, a flight path is chosen. This can be seen in figure 6.4.

The total flight range is calculated via formula 6.8. Here  $l_{fl}$  is the length of the flight lines, which is based on the length of the TU Delft and is 2.1km.  $l_{ctop}$  and  $l_{cdown}$  are the lengths of the turning circles for the top part of the figure and bottom part of the figure respectively, which are 686m and 609m .  $n_{rtop}$  and  $n_{rdown}$  are the number of turns for the top and bottom part respectively, which are 9 and 8.  $l_{to}$  and  $l_l$  are the take-off length and the length for landing respectively. They are both chosen to be 1 km to be on the safe side.

$$d_{tot} = (n_{fl} * l_{fl}) + (l_{ctop} * n_{rtop}) + (l_{cdown} * n_{rdown}) + l_t o + l_l$$
(6.8)

This gives a total flight path length of 48.75 km. The corresponding flight time, given that the speed of the UAV is 65km/h is 00:45 hours.



Figure 6.4: Flight path of the area of the TU Delft

#### 6.5. Power consumption in flight

Figure 6.5 shows the power consumption by the system and the power generated from the battery and PV in the best case scenario. The Solar irradiation equals 887  $W/m^2$  and the temperature equals 21.2 degrees. From this graph, it becomes visible that the solar panels generate about 50W, the battery generates about 60W and the motor + secondary system consume about 110W. Figure 6.6 shows the power consumption by the system



Figure 6.5: Power consumption of the system in the best case scenario

and the power generated from the battery and PV in the worst case scenario. The Solar irradiation equals 0  $W/m^2$  and the temperature equals 4.4 degrees. From this graph, it becomes visible that the solar panels generate 0W, the battery generates about 100W and the motor + secondary system consume about 110W.



Figure 6.6: Power consumption of the system in the worst case scenario

Figure 6.6 shows the power consumption by the system and the power generated from the battery when no PV panels are added. From this graph, the battery generates about 82W and the motor + secondary system consume about 89W.



Figure 6.7: Power consumption of the system when no PV panels are added

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# A

## Components

#### Turnigy Aerodrive SK3 - 4250-500kv Brushless Outrunner Motor





SKU: SK3-4250-500 425 g

#### Config Table

Kv	500 RPM/V
Weight	269 g
Max Current	57 A
Max Voltage	19 V
Power	1350 W
Internal resistance	18 mohm
Shaft (A)	5 mm
Length (B)	58 mm
Diameter (C)	42 mm
Can Length (D)	34 mm
Total Length (E)	80 mm

Figure A.1: Motor - Turnigy Aerodrive SK3 - 4250-500kv specifications



#### The power of Pixhawk®4 in a compact form

#### Product Features

- Half the footprint of the *Pixhawk*<sup>®</sup> 4
- The same FMU processor and memory resources as the *Pixhawk 4*
- Aluminum casing for great thermal performance
- Easy to connect to commercial ESCs
- The latest sensor technology from Bosch<sup>®</sup> and InvenSense<sup>®</sup>
- Redundant IMUs for reliable
   performance
- NuttX real-time operating system
- Pre-installed with the most recent PX4 firmware



The *Pixhawk*<sup>®</sup> 4 Mini autopilot is designed for engineers and hobbyists who are looking to tap into the power of *Pixhawk* 4 but are working with smaller drones. *Pixhawk* 4 *Mini* takes the FMU processor and memory resources from the *Pixhawk* 4 while eliminating normally unused interfaces. This allows the *Pixhawk* 4 *Mini* to be small enough to fit in a 250mm racer drone. The *Pixhawk* 4 *Mini* is easy to install; the 2.54mm (0.1in) pitch connector makes it easier to connect the 8 PWM outputs to commercially available ESCs.

*Pixhawk 4 Mini* was designed and developed in collaboration with Holybro<sup>®</sup> and Auterion<sup>®</sup>. It is based on the Pixhawk FMUv5 design standard and is optimized to run PX4 flight control software.

Pixhawk® 4 Mini Technical Data Sheet (August 2018)

Page 1 of 3

Figure A.2: Auto pilot - Pixhawk 4 mini specifications

**TURNIGY Manual for Brushless Motor Speed Controller** 

TURNIGY Manual for Brushless Motor Speed Controller

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have

C). High power systems for RC model can be very dange and control over the connect use, installation, applic accepted for any damages, bases or costs resulting or or malfunctioning etc. will be denied. We assume no its

el can be very dangerous; we installation, application, or r costs resulting from the use d. We assume no liability for manship. As far as is legally





rake Settings: Brake Enabled / Brake Disabled, default is Brake Disabled atteny Type: Li- $\infty$ (Li-ion or Li-poly) / Ni- $\infty$ (NiMh or Nicd), default is Li- $\infty$ .

- Battery Type: Li-xx(Li-ion or Li-poly) / / Low Voltage Protection Mode(Cutoff I Reduce / Cutoff Output Power, default is Reduce the output power gradually

- Low Yolge Protection Twoel-(utum receipt): Acquire rows, two with this defail is Medium.
   When NOT using balance discharge monitoring and protecting function (a. Do Not Upin the balance charge concerprint): The second structure of the second structure structure of second structure of the seco
- volum) and opened again (throttle slick is moved upwards) within 3 seconds after the first startup, the *i* changed to normal mode to get rid of the chances of crash caused by slow throttle response in aerobatic fly Timing. Low / Medium / High, default is Low.

s, low timing can be used for most motors. But for high efficiency, we recommend the Leo 1 for 6 poiles and allows. For higher speed, High timing can be chosen. four your ethinging the tighing setting, please test your RC model on ground firstly! , we recommend the Low timing for 2 poles motor and

Some high VV out-unner motors have very special configuration, the space between each anino is very large, and loss of ESCs can't drive hese motors. After updating the program our ESCs have very good compatibility with them. But some RC fans still have several questions about the programmable value for some special motors. So we just give some suggestions as follows:

Motor Programmable Value Suggestion	Timing	Startup Mode
General in-runner motor	Low	Usually, aircraft uses "normal" startup mode
General out-runner motor	Low or Medium	helicopter uses "super-soft" startup mode
Align 420LF (Made in TAIWAN, out-runffier)	High (MUST)	
450TH (Made in TAIWAN, out-runner)	Low	Soft (MUST)

Figure A.3: Speed controller - Turnigy Plush 60

## 

e the throttle : ž ollowing sequence

- pack and all the connection

**PRODUCT DATA SHEET** MODEL INR-21700-P42A

4.2A charge 8.4A charge

1

4000

1.0

0.84A 4.2A 10A 20A 30A

2800 3200 3600

Charge: CC-CV Charge to 4.29, 50mA cut at 23°C.

0.5

Discharge Rate Characteristics

CC-CV, 4.2A, 4.2V e: Cut off at 2.5V. ture: 23°C.

800 1200 1600 2000 2400

Charge Characteristics

2.0

8.0 6. ε 3.0 Voltage Ĕ 40

> 2.0 1.0

• • 0.0

4 S 3.0

Voltage 2.0 1.

0.0

400



#### **CELL CHARACTERISTICS**

	Typical	4200 mAh
Capacity		15.5 Wh
	Minimum	4000 mAh
		14.7 Wh
Cell Voltage	Nominal	3.6 V
	Charge	4.2 V
	Discharge	2.5 V
Charge Current	Standard	4.2 A
Charge Time	Standard	1.5 hr
Discharge Current	Continuous	45 A
Typical	AC (1 KHz)	10 mΩ
Impedance	DC (10A/1s)	16 mΩ
Temperature	Charge	0°C to 60°C
	Discharge	-40°C to 60°C
Energy Density	Volumetric	615 Wh/l
	Gravimetric	230 Wh/kg

#### PHYSICAL CHARACTERISTICS



#### Figure A.4: Battery cel - Molicel P42A specifications
		Model: LIM-5S1045LI606	
No.		Test item	Criterion
я	Voltage	Charging voltage	DC:21V CC/CV
31	voltage	Balance voltage for single cell	4.180V±50 mV
		Balance current for single cell	80mA±10mA
9	Current	Current consumption for single cell	<u>≤</u> 6µА
2	Curren	Maximal continuous Charging current	10A
		Maximal continuous Discharging current	45A
3		Over charge detection voltage	4.25V≲ <u>+</u> 0.025V
3	Over charge Protection	Over charge delay time	15
	and designed of the	Over charge recovery voltage	4.15V≲ <u>+</u> 0.025V
		Over discharge detection voltage	2.8V≪ <u>+</u> 0.1V
4	Over discharge protection	Over discharge delay time	128ms
		Over discharge release voltage	3.0V≪ <u>+</u> 0.1V
		Over current testing voltage	150mv ≤ <u>+</u> 15mv
		Over current detection current	90A <u>+</u> 10A
5	Over current protection	Over current delay time	12MS
		Release condition	Automatic Recovery
		Detection condition	Exterior short circuit
6	Short protection	Detection delay time	≤300us
		Release condition	Automatic Recovery
7	Resistance	Protection circuitry (MOSFET)	≤20mΩ
ø	Tomperature	Operating Temperature Range	-40~+65°C
9	remperature.	Storage Temperature Range	-40-++ 125°C
9	Size		L60*W60*T9mm
10	Weight		30g

Figure A.5: Specifications battery protection component

# B

## Results

			PV power, Battery	Power, Pe_extra, Pe_mot	ior	
140						PV power Battery Power Pe_sotra Pe_motor
80						
60						
-40						
	20	00 40	00 60	00 80	100 9	0000 12000

Figure B.1: Power distribution during flight





## C

## Steady state analysis

A steady state analysis on the boost converter has been done. These calculations are a similar method to the one used by library. For the simplicity of calculation, it is assumed that the battery is detached. MOSFET ON:

$$V_{PV} = L\frac{di}{dt} + i(R_L + R_{on}) \tag{C.1}$$

$$C\frac{dv_C}{dt} = -\frac{V_{out}}{R_{load}}$$
(C.2)

$$V_{out} = v_C \frac{R_{load}}{R_{load} + R_C}$$
(C.3)

$$\begin{bmatrix} \frac{di}{dt} \\ \frac{dv_C}{dt} \end{bmatrix} = \begin{bmatrix} \frac{-(R_L + R_{on})}{L} & 0 \\ 0 & -\frac{1}{(R_{load} + R_C)} \end{bmatrix} \cdot \begin{bmatrix} i \\ v_C \end{bmatrix} + \begin{bmatrix} \frac{1}{L} \\ 0 \end{bmatrix} V_{PV} = A_1 x + B_1 V_{PV}$$
(C.4)

$$V_{out} = q_1 x = \begin{bmatrix} 0 & \frac{R_{load}}{R_{load} + R_C} \end{bmatrix} x$$
(C.5)

MOSFET OFF:

$$V_{PV} = L\frac{di}{dt} + iR_L + V_{out} + V_D \tag{C.6}$$

$$i = C \frac{dv_C}{dt} + \frac{V_{out}}{R_{load}}$$
(C.7)

$$V_{out} = v_C \frac{R_{load}}{R_{load} + R_C} + i \frac{R_{load} R_C}{R_{load} + R_C}$$
(C.8)

$$\begin{bmatrix} \frac{di}{dt} \\ \frac{dv_C}{dt} \end{bmatrix} = \begin{bmatrix} \frac{-(R_L + \frac{R_{load}R_C}{R_{load} + R_C})}{L} & -\frac{R_{load}}{(R_{load} + R_C)L} \\ \frac{R_{load}}{(R_{load} + R_C)C} & -\frac{1}{(R_{load} + R_C)} \end{bmatrix} \cdot \begin{bmatrix} i \\ v_C \end{bmatrix} + \begin{bmatrix} \frac{1}{L} \\ 0 \end{bmatrix} (V_{PV} - V_D) = A_2 x + B_2 (V_{PV} - V_D)$$
(C.9)

$$V_{out} = q_2 x = \begin{bmatrix} \frac{R_{load}R_C}{R_{load}+R_C} & \frac{R_{load}}{R_{load}+R_C} \end{bmatrix} x$$
(C.10)

$$V_{out} = Ax + BV_{PV},\tag{C.11}$$

where 
$$A = \frac{A_1 + A_2}{2} = A_1 D + (1 - D)A_2$$
 and  $B = \frac{B_1 + B_2}{2} = B_1 D + (1 - D)B_2$  (C.12)

In steady state:

$$A = \begin{bmatrix} -\frac{D(R_L + R_{on}) + (1 - D)[R_L + \frac{R_{load} R_C}{R_{load} + R_C}]}{L} & -\frac{(1 - D)R_{load}}{(R_{load} + R_C)L} \\ \frac{(1 - D)R_{load}}{(R_{load} + R_C)C} & -\frac{1}{(R_{load} + R_C)C} \end{bmatrix}$$
(C.13)

$$\dot{x} = \begin{bmatrix} \frac{di}{dt} \\ \frac{dv_c}{dt} \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \end{bmatrix} = A \begin{bmatrix} I \\ V_c \end{bmatrix} + BV_{PV}$$
(C.14)

$$X = \begin{bmatrix} I \\ V_C \end{bmatrix} = -A^{-1}BV_{PV}$$
(C.15)

$$A^{-1} = \frac{1}{det(A)} \cdot \begin{bmatrix} -\frac{1}{(R_{load} + R_C)C} & \frac{(1-D)R_{load}}{(R_{load} + R_C)L} \\ -\frac{(1-D)R_{load}}{(R_{load} + R_C)C} & -\frac{D(R_L + R_{on}) + (1-D)[R_L + \frac{R_{load}R_C}{R_{load} + R_C}]}{L} \end{bmatrix}$$
(C.16)

where 
$$\frac{1}{det(A)} = \frac{(R_{load} + R_{c})^{2}CL}{(R_{L} + R_{on}D)(R_{load} + R_{c}) + (1 - D)R_{load}(R_{c} + (1 - D)R_{load})}$$
(C.17)

Similarly, the input/output voltage relationship can calculated as in Equation C.18. For the simplification of the inverse A matrix, matrix E is found as in Equation C.20. This is possible due to the fact that the bottom row in matrix B is equal to zero.

$$\frac{V_{out}}{V_{PV}} = -CEB,\tag{C.18}$$

where  $C = \begin{bmatrix} 0 & 1 \end{bmatrix}$  and *E* is  $A^{-1}$  with the second column is set to 0's (C.19)

$$E = \begin{bmatrix} -\frac{(R_{load} + R_C)L}{(R_L + R_{on}D)(R_{load} + R_C) + (1 - D)R_{load}(R_C + (1 - D)R_{load})} & 0\\ -\frac{(1 - D)R_{load}(R_{load} + R_C)L}{(R_L + R_{on}D)(R_{load} + R_C) + (1 - D)R_{load}(R_C + (1 - D)R_{load})} & 0 \end{bmatrix}$$
(C.20)

The input/output voltage relation is then found as in Equation C.21.

$$\frac{V_{out}}{V_{pv}} = \frac{(1-D)R_{load}(R_{load}+R_C)}{(R_L + R_{on}D)(R_{load}+R_C) + (1-D)R_{load}(R_C + (1-D)R_{load})}$$
(C.21)

## D

### Simulation models

#### D.1. PV model

#### **D.1.1.** Parameters

Parameters of the PV system under STC (G = 1000W/m2,  $T = 25^{\circ}C$ )

- $N_c = 19$
- $R_s = 0.001386 \cdot N_c[\Omega]$
- $R_p = 4.7091 \cdot N_c[\Omega]$
- A = 0.96737
- $I_s = 1.402 \cdot 10^{-12} [A]$
- $V_{oc} = 0.724 \cdot N_c[V]$
- $I_{sc} = 6.43[A]$
- $V_{mpp} = 0.634 \cdot N_c[V]$
- $I_{mpp} = 6.06[A]$
- $P_{mpp} = 3.84 \cdot N_c[W]$



Figure D.1: The spice PV model used for the simulations





Figure D.2: LTSpice simulation model

#### **D.3. Simulink General Simulation Model**



Figure D.3: Simulink general model for the UAV

### **D.4. Simulink Power Converter Model**



Figure D.4: Power converter simulink implementation

## E



#### E.1. Data import

```
function [header, step_info, cycle_data] = import_data(file_name)
1
  %
       Loads simulation data from LTSpice simulation
2
  %
       IN:
3
  %
                                  [String]
           – File name
4
  %
       OUT:
5
           - Header info
  %
                                  Γ
                                    String matrix]
6
  %
           - Step info
7
  %
                                   Integer cell,
8
  %
                                      – Duty cycles
                                                             [ /1000 ]
9
  %
                                      - Irradiances
                                                            [ W/m^2 ]
10
  %
                                      - Battery voltages [ Integer cell, V ]
11
  %
                                  ]
12
  %
           - Cycle data
                                  [ Double cell, simulation results]
13
14
       %% Open file and check if successfull
15
       [fid ,msg] = fopen(file_name, 'rt');
16
       assert(fid >=3,msg)
17
18
       %% Define file format
19
       % Get simulation variables from header
20
       header = fgetl(fid);
21
       header = regexp(header, '\S+', 'match');
22
23
       % Data format
24
       format = repmat('%f',1,numel(header));
25
       options = { 'CollectOutput ', true };
26
27
       %% Process
28
       subheader = \{\};
29
       cycle_data = {};
30
31
       while ~feof(fid)
32
           subheader{end+1} = fgetl(fid);
33
           cycle_data(end+1) = textscan(fid, format, options{:});
34
       end
35
36
       fclose(fid);
37
```

```
38
       %% Extract duty cycle
39
       subheader = regexp(subheader, (\d+)*, 'match');
40
41
       step_info = [];
42
       for i = 1:length(subheader)
43
           step_info(1, i) = str2double(subheader{1, i}(1,1));
44
           step_info(2, i) = str2double(subheader{1, i}(1,2));
45
           if size(subheader\{1, i\}, 2) == 6
46
                step_info(3, i) = str2num(string(char(subheader{1, i}(1,3)))
47
                   +"."+ string (char(subheader\{1, i\}(1,4)));
           else
48
                step_info(3, i) = str2num(string(char(subheader{1, i}(1,3)));
49
           end
50
        end
51
52
  end
53
```

#### **E.2. Efficiency calculation**

```
function [outputdata] = eff_power_avg(inputdata, header)
  % Average efficiency by integrating the energie
2
  %
3
  %
                      -> input data matrix with dimension: datatracks X
        inputdata
4
      samples
  %
       timestep
                          -> sample steps for new data
5
  %
        outputdata
                       -> output data matrix with dimension: datatracks X
6
      samples/ratio
7
  time = find(header == "time");
8
  Vout = find(header == "V(vout)");
  Vin = find(header == "V(vin)");
10
  Iout = find(header == "I(R1)");
11
  Iin = find(header == "Ix(x1:VP)");
12
13
  steps = 1;
14
15
  Ts = inputdata(end, time);
16
  timestep = Ts/(steps+1);
17
18
  %% calculate energy per time
19
  % calculate P
20
  Pin = -1*inputdata(:,Vin).*inputdata(:,Iin);
21
  Pout = -1*inputdata(:,Vout).*inputdata(:,Iout);
22
23
  %% calculate dt
24
  time_temp(:,1) = [0; inputdata(:,time)];
25
  time_temp(:,2) = [inputdata(:,time);0];
26
  time\_temp(:,3) = [inputdata(2:1:end,time);0;0];
27
28
  time_mid = time_temp(:,1:1:2) + diff(time_temp,1,2)/2;
29
30
  dt = diff(time_mid, 1, 2);
31
32
  Ttimes = table(time_temp, time_mid,
                                          dt);
33
34
  %% calculate energy per time difference
35
```

```
Ein = 1;
36
  Eout = 2;
37
38
  E(:, Ein) = Pin.*dt(1:end-1);
39
  E(:, Eout) = Pout.*dt(1:end-1);
40
41
  %% sum energie
42
43
  %cumtime = cumsum(inputdata(:,time));
44
  %avgcumtime = cumtime./[1:1:length(cumtime)]';
45
46
  E_sum = zeros(1, size(E, 2));
47
  time_sum = [];
48
  points = [1];
49
50
  for n = 1:steps
51
       points(n+1) = find(inputdata(:,time) > n*timestep ,1) -1 ;
52
53
       E_sum(n,:) = sum(E(points(n):points(n+1),:),1);
54
       time_sum(n) = inputdata(points(n+1),time);
55
  end
56
57
  %% calculate efficiency
58
  Eff = E_sum(:,Eout)./E_sum(:,Ein);
59
  Pin_avg = round(2*E_sum(:,Ein)/Ts,1);
60
61
  outputdata = [Pin_avg, Eff];
62
```

#### E.3. Data points to lookup conversion

```
%% Main Data Run
  %
  % By Martin & Jetserrr
3
  clear all
4
  close all
5
  load lookupPE.mat
  file_name = 'G_75_125_175.txt'; % give data filename to import
9
10
                                    % skip this runs
  deletes = [];
11
  stepvalues = [];
12
13
  %% import data from file %%
14
  [header, stepvalues ,data] = import_data(file_name);
15
16
  %% column modus Jetse
17
 time = find(header == "time");
18
  Vout = find(header == "V(vout)");
19
  Vin = find(header == "V(vin)");
20
  Iout = find(header == "I(R1)");
21
  Iin = find(header == "Ix(x1:VP)");
22
23
  runs = size(data,2);
24
  %runs = 10;
25
26
  %% Calculate, downsample efficiencies -> save to matrix
27
```

```
results = \{\};
28
29
  Pin = 1;
30
  Eff = 2;
31
32
   for i = 1:runs
33
       if ismember(i, deletes)
34
            results \{1, i\} = zeros(1, 2);
35
36
       else
37
            results {1, i} = eff_power_avg(data {1, i}, header);
38
      %
             Results \{1, i\}(1, Eff)
39
       end
40
41
  end
42
43
  %% Build lookup table
44
  DC_lt = [0:0.01:0.60];
45
  Vbat_lt = [12.5:2.125:21];
                                                      %just to know the indexes
46
  Pin_lt = [0:0.1:75];
47
48
   for i =1:runs
49
       index_DC = (stepvalues(1, i)+10)/10;
50
       index_Vbat = (stepvalues(3,i)-12.5)/2.125+1;
51
       index_Pin = results \{1, i\}(1, Pin)*10+1;
52
53
       lookup(index_DC, index_Vbat, index_Pin) = results {1, i }(1, Eff);
54
  end
55
56
  save('lookupPE.mat', 'lookup')
57
58
  status = 'finished'
59
60
  statussound (1)
61
```

#### E.4. Interpolation of the lookup matrix

```
load lookupPE.mat
1
  % Counting the number of zeros
2
  idx=lookup==0;out=sum(idx(:));
  disp([num2str(100*out/numel(lookup)), '% of the points are zeros!'])
4
5
  lookup_new = lookup;
6
  % Step 1: substitute zeros with NaN
7
  for i = 1:61
8
       for j = 1:5
9
           for k=1:729
10
                if lookup(i,j,k)==0
11
                    lookup_new(i, j, k) = NaN;
12
                end
13
           end
14
       end
15
  end
16
17
  % to visualize results, create 2-dimensional matrices.
18
  % here, every matrix correspond to a battery voltage
19
  lookup_1_buffer = reshape(lookup_new(:,1,:),61,729);
20
```

```
lookup_2_buffer = reshape(lookup_new(:,2,:),61,729);
21
  lookup_3_buffer = reshape(lookup_new(:,3,:),61,729);
22
  lookup_4_buffer = reshape(lookup_new(:,4,:),61,729);
23
  lookup_5_buffer = reshape(lookup_new(:,5,:),61,729);
24
25
  figure
26
  s=surf(lookup_1_buffer); s. EdgeColor = 'r'; s. FaceColor = 'r'; s. FaceAlpha=1;
27
  hold on
28
  s=surf(lookup_2_buffer);s.EdgeColor = 'g';s.FaceColor = 'g';s.FaceAlpha=1;
29
  s=surf(lookup_3_buffer); s.EdgeColor = 'b'; s.FaceColor = 'b'; s.FaceAlpha=1;
30
  s=surf(lookup_4_buffer); s. EdgeColor = 'm'; s. FaceColor = 'm'; s. FaceAlpha=1;
31
  s=surf(lookup_5_buffer);s.EdgeColor = 'y';s.FaceColor = 'y';s.FaceAlpha=1;
32
   ylabel('Duty cycle index')
33
  xlabel('Input Power index')
legend('V_b_a_t #1','V_b_a_t #2','V_b_a_t #3','V_b_a_t #4','V_b_a_t #5','
34
35
      location ', 'southeast ')
   title('Very few points are not zero!!!'')
36
37
  figure
38
  subplot(321)
39
  s=surf(lookup 1 buffer); s. EdgeColor = 'r'; s. FaceColor = 'r'; s. FaceAlpha=1;
40
  hold on
41
  s=surf(lookup_2_buffer);s.EdgeColor = 'g';s.FaceColor = 'g';s.FaceAlpha=1;
42
  s=surf(lookup_3_buffer);s.EdgeColor = 'b';s.FaceColor = 'b';s.FaceAlpha=1;
43
  s=surf(lookup_4_buffer); s.EdgeColor = 'm'; s.FaceColor = 'm'; s.FaceAlpha=1;
44
  s=surf(lookup_5_buffer); s. EdgeColor = 'y'; s. FaceColor = 'y'; s. FaceAlpha=1;
45
  ylabel('Duty cycle index')
46
  xlabel('Input Power index')
47
  legend('V_b_a_t #1', 'V_b_a_t #2', 'V_b_a_t #3', 'V_b_a_t #4', 'V_b_a_t #5', '
48
      location ', 'southeast')
49
  % Here, I try to fill all missing values (the zeros in your original
50
  % matrix, now substituted with NaN) using a moving median filter along
51
      a11
  %
     the dimensions of your matrix.
52
      I do the moving median over 35 points in all dimensions because it is
  %
53
      the
  %
      lowest value that removes all the NaN. You can play with this value,
54
      and
      it can be different in the different dimensions. Perhaps, you do not
  %
55
     need to remove all the NaN, since Simulink should not consider them
  %
56
      when
  % using the look up table.
57
  lookup_new = fillmissing(lookup_new, 'movmedian',35,1);
58
  lookup_new = fillmissing(lookup_new, 'movmedian',35,2);
lookup_new = fillmissing(lookup_new, 'movmedian',35,3);
59
60
61
  disp([num2str(sum(sum(isnan(lookup_new))))), 'NaN remaining'])
62
63
  % to visualize results, create 2-dimensional matrices.
64
  % here, every matrix correspond to a battery voltage
65
  lookup_1 = reshape(lookup_new(:,1,:),61,729);
66
  lookup_2 = reshape(lookup_new(:,2,:),61,729);
67
  lookup_3 = reshape(lookup_new(:,3,:),61,729);
68
  lookup_4 = reshape(lookup_new(:,4,:),61,729);
69
  lookup_5 = reshape(lookup_new(:,5,:),61,729);
70
```

```
71
  i=2;
72
<sub>73</sub> subplot(3,2,i)
<sub>74</sub> i=i+1;
75 s=surf(lookup_1);s.EdgeColor = 'none';s.FaceColor = 'r';s.FaceAlpha=1;
   vlabel('Duty cycle index')
76
   xlabel('Input Power index')
77
  hold on
78
  subplot(3,2,i)
79
  i=i+1;
80
  s=surf(lookup 2):s.EdgeColor = 'none':s.FaceColor = 'g':s.FaceAlpha=1;
81
   ylabel('Duty cycle index')
82
   xlabel('Input Power index')
83
   subplot(3,2,i)
84
  i=i+1;
85
  s=surf(lookup_3);s.EdgeColor = 'none';s.FaceColor = 'b';s.FaceAlpha=1;
86
  ylabel('Duty cycle index')
87
  xlabel('Input Power index')
88
  subplot(3,2,i)
89
  i=i+1;
90
  s=surf(lookup_4);s.EdgeColor = 'none';s.FaceColor = 'm';s.FaceAlpha=1;
91
   ylabel('Duty cycle index')
92
   xlabel('Input Power index')
93
  subplot(3,2,i)
94
  i=i+1;
95
  s=surf(lookup_5);s.EdgeColor = 'none';s.FaceColor = 'y';s.FaceAlpha=1;
96
   vlabel('Duty cycle index')
97
   xlabel('Input Power index')
98
99
   figure
100
   subplot(121)
101
   s=surf(lookup_1_buffer); s. EdgeColor = 'r'; s. FaceColor = 'r'; s. FaceAlpha=1;
102
  hold on
103
  s=surf(lookup_2_buffer);s.EdgeColor = 'g';s.FaceColor = 'g';s.FaceAlpha=1;
104
  s=surf(lookup_3_buffer);s.EdgeColor = 'b';s.FaceColor = 'b';s.FaceAlpha=1;
105
   s=surf(lookup_4_buffer);s.EdgeColor = 'm';s.FaceColor = 'm';s.FaceAlpha=1;
106
   s=surf(lookup_5_buffer);s.EdgeColor = 'y';s.FaceColor = 'y';s.FaceAlpha=1;
107
   ylabel('Duty cycle index')
108
   xlabel('Input Power index')
109
   legend('V_b_a_t #1', 'V_b_a_t #2', 'V_b_a_t #3', 'V_b_a_t #4', 'V_b_a_t #5', '
110
      location ', 'southeast ')
111
112
   subplot(1,2,2)
113
   s=surf(lookup 1); s.EdgeColor = 'none'; s.FaceColor = 'r'; s.FaceAlpha=1;
114
   hold on
115
   s=surf(lookup_2);s.EdgeColor = 'none';s.FaceColor = 'g';s.FaceAlpha=0.8;
116
   s=surf(lookup_3);s.EdgeColor = 'none';s.FaceColor = 'b';s.FaceAlpha=0.6;
117
  s=surf(lookup_4); s. EdgeColor = 'none'; s. FaceColor = 'm'; s. FaceAlpha=0.4;
118
  s=surf(lookup_5);s.EdgeColor = 'none';s.FaceColor = 'y';s.FaceAlpha=0.2;
119
   ylabel('Duty cycle index')
120
   xlabel('Input Power index')
```

# F

## Datasheets

#### F.1. MOSFET datasheet



### **BUK9J0R9-40H**

N-channel 40 V, 0.9 mΩ logic level MOSFET in LFPAK56E

7 October 2019

**Product data sheet** 

#### 1. General description

Automotive qualified N-channel MOSFET using the latest Trench 9 low ohmic superjunction technology, housed in an enhanced LFPAK56E package. This product has been fully designed and qualified to meet AEC-Q101 requirements delivering high performance and endurance.

#### 2. Features and benefits

- Fully automotive qualified to AEC-Q101:
  - 175 °C rating suitable for thermally demanding environments
- Trench 9 Superjunction technology:
  - Reduced cell pitch enables enhanced power density and efficiency with lower R<sub>DSon</sub> in same footprint
  - Improved SOA and avalanche capability compared to standard TrenchMOS
  - Tight V<sub>GS(th)</sub> limits enable easy paralleling of MOSFETs
- LFPAK Gull Wing leads:
  - High Board Level Reliability absorbing mechanical stress during thermal cycling, unlike traditional QFN packages
  - Visual (AOI) soldering inspection, no need for expensive x-ray equipment
  - Easy solder wetting for good mechanical solder joint
- LFPAK copper clip technology:
  - Improved reliability, with reduced R<sub>th</sub> and R<sub>DSon</sub>
  - Increases maximum current capability and improved current spreading

#### 3. Applications

- 12 V automotive systems
- Motors, lamps and solenoid control
- Start-Stop micro-hybrid applications
- Transmission control
- Ultra high performance power switching

#### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit			
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C		-	-	40	V			
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C; <u>Fig. 2</u>	[1]	-	-	220	А			
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>		-	-	500	W			
Static characte	eristics									
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>		0.53	0.82	0.94	mΩ			

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#### N-channel 40 V, 0.9 mQ logic level MOSFET in LFPAK56E

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Dynamic chara	Dynamic characteristics							
Q <sub>GD</sub>	gate-drain charge	$I_D = 25 \text{ A}; V_{DS} = 20 \text{ V}; V_{GS} = 4.5 \text{ V};$ Fig. 13; Fig. 14		-	12.7	25.3	nC	
Source-drain d	iode	· · · · · ·			-	-		
Q <sub>r</sub>	recovered charge	$\label{eq:IS} \begin{array}{l} {\sf I}_{\rm S} = 25 \; {\sf A}; \; {\sf dI}_{\rm S}/{\sf dt} = -100 \; {\sf A}/\mu{\sf s}; \; {\sf V}_{\rm GS} = 0 \; {\sf V}; \\ {\sf V}_{\rm DS} = 20 \; {\sf V}; \; {\sf T}_{\rm j} = 25 \; {\rm ^{\circ}C} \end{array}$	[2]	-	52.6	-	nC	
S	softness factor	$I_{S} = 25 \text{ A}; \text{ dI}_{S}/\text{dt} = -100 \text{ A}/\mu\text{s}; \text{ V}_{GS} = 0 \text{ V}; \\ \text{V}_{DS} = 20 \text{ V}; \text{ T}_{j} = 25 ^{\circ}\text{C}; \text{ Fig. 17}$		-	0.77	-		

[1] 220A continuous current has been successfully demonstrated during application tests. Practically the current will be limited by PCB, thermal design and operating temperature.

[2] includes capacitive recovery

#### 5. Pinning information

#### Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S	source		D
2	S	source		
3	S	source		G-UFA
4	G	gate		mbb076 S
mb	D	mounting base; connected to drain	LFPAK56E; Power- SO8 (SOT1023)	

#### 6. Ordering information

Table 3. Ordering information								
Type number	Package							
	Name	Description	Version					
BUK9J0R9-40H	LFPAK56E; Power-SO8	plastic, single-ended surface-mounted package (LFPAK56); 4 leads; 1.27 mm pitch	SOT1023					

#### 7. Marking

Table 4. Marking codes					
Type number	Marking code				
BUK9J0R9-40H	90H940E				

#### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	25 °C ≤ T <sub>j</sub> ≤ 175 °C	-	40	V
V <sub>GS</sub>	gate-source voltage	DC; T <sub>j</sub> ≤ 175 °C	-10	16	V

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#### N-channel 40 V, 0.9 mΩ logic level MOSFET in LFPAK56E

Symbol	Parameter	Conditions		Min	Мах	Unit
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; <u>Fig. 1</u>		-	500	W
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 25 °C; <u>Fig. 2</u>	[1]	-	220	A
		V <sub>GS</sub> = 10 V; T <sub>mb</sub> = 100 °C; <u>Fig. 2</u>	[1]	-	220	А
I <sub>DM</sub>	peak drain current	pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^{\circ}C$ ; Fig. 3		-	600	А
T <sub>stg</sub>	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
Source-drain die	ode					
I <sub>S</sub>	source current	T <sub>mb</sub> = 25 °C	[2]	-	165	А
I <sub>SM</sub>	peak source current	pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^{\circ}C$		-	600	А
Avalanche rugg	edness					
E <sub>DS(AL)S</sub>	non-repetitive drain- source avalanche energy	$\label{eq:ld} \begin{array}{l} I_D = 160 \text{ A}; \ V_{sup} \leq \ 40 \text{ V}; \ R_{GS} = 50 \ \Omega; \\ V_{GS} = 10 \text{ V}; \ T_{j(init)} = 25 \ ^\circ\text{C}; \ unclamped; \\ \hline Fig. \ 4 \end{array}$	[3] [4]	-	290	mJ

[1] 220A continuous current has been successfully demonstrated during application tests. Practically the current will be limited by PCB, thermal design and operating temperature.

[2] 165A continuous current has been successfully demonstrated during application tests. Practically the current will be limited by PCB, thermal design and operating temperature.

[3] Single-pulse avalanche rating limited by maximum junction temperature of 175 °C.

[4] Refer to application note AN10273 for further information.





(1) 220A continuous current has been successfully demonstrated during application tests. Practically the current will be limited by PCB, thermal design and operating temperature.

Fig. 2. Continuous drain current as a function of mounting base temperature

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#### N-channel 40 V, 0.9 mΩ logic level MOSFET in LFPAK56E



#### 9. Thermal characteristics

#### Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	<u>Fig. 5</u>	-	0.21	0.3	K/W

#### N-channel 40 V, 0.9 mQ logic level MOSFET in LFPAK56E



#### 10. Characteristics

Table 7. Cha	racteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics			·		
V <sub>(BR)DSS</sub>	drain-source	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	40	43	-	V
	breakdown voltage	I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = -40 °C	-	40.5	-	V
		I <sub>D</sub> = 250 μA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = -55 °C	36	40	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	$I_D = 1 \text{ mA}; V_{DS}=V_{GS}; T_j = 25 \text{ °C}; Fig. 9;$ Fig. 10	1.35	1.66	2.05	V
		I <sub>D</sub> = 1 mA; V <sub>DS</sub> =V <sub>GS</sub> ; T <sub>j</sub> = 175 °C; Fig. 10	0.6	-	-	V
		I <sub>D</sub> = 1 mA; V <sub>DS</sub> =V <sub>GS</sub> ; T <sub>j</sub> = -55 °C; <u>Fig. 10</u>	-	-	2.5	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = 40 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	0.4	5	μA
		V <sub>DS</sub> = 16 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 125 °C	-	3.2	25	μA
		V <sub>DS</sub> = 40 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C	-	405	1000	μA
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 16 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	2	100	nA
		V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	2	100	nA

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#### N-channel 40 V, 0.9 m $\Omega$ logic level MOSFET in LFPAK56E

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>DSon</sub>	drain-source on-state resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>		0.53	0.82	0.94	mΩ
		V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 105 °C; Fig. 12		0.79	1.17	1.48	mΩ
		V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 125 °C; Fig. 12		0.87	1.29	1.64	mΩ
		V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 175 °C; <u>Fig. 12</u>		1.1	1.63	2.05	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C; <u>Fig. 11</u>		0.68	0.97	1.2	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 105 °C; Fig. 12		1	1.47	1.9	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 125 °C; Fig. 12		1.1	1.62	2.1	mΩ
		V <sub>GS</sub> = 4.5 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 175 °C; Fig. 12		1.4	2.03	2.6	mΩ
R <sub>G</sub>	gate resistance	f = 1 MHz; T <sub>j</sub> = 25 °C		0.42	1.04	2.6	mΩ
Dynamic chara	cteristics				-		
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 20 \text{ V}; V_{GS} = 10 \text{ V};$ Fig. 13; Fig. 14		-	120	168	nC
		$I_D = 25 \text{ A}; V_{DS} = 20 \text{ V}; V_{GS} = 4.5 \text{ V};$		-	54.2	76	nC
Q <sub>GS</sub>	gate-source charge	Fig. 13; Fig. 14		-	20.2	30.2	nC
Q <sub>GD</sub>	gate-drain charge			-	12.7	25.3	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 25 V; V <sub>GS</sub> = 0 V; f = 1 MHz;		-	8977	12568	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C; <u>Fig. 15</u>		-	1549	2168	pF
C <sub>rss</sub>	reverse transfer capacitance			-	346	760	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 20 V; $R_{L}$ = 0.8 $\Omega$ ; $V_{GS}$ = 4.5 V;		-	45.4	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 5 \Omega$		-	46.2	-	ns
t <sub>d(off)</sub>	turn-off delay time			-	59.2	-	ns
t <sub>f</sub>	fall time	-		-	32.6	-	ns
Source-drain d	iode	·			1		
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 25 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C; <u>Fig. 16</u>		-	0.76	1.2	V
t <sub>rr</sub>	reverse recovery time	$I_{S}$ = 25 A; dI <sub>S</sub> /dt = -100 A/µs; V <sub>GS</sub> = 0 V;		-	44.6	-	ns
Q <sub>r</sub>	recovered charge	V <sub>DS</sub> = 20 V; T <sub>j</sub> = 25 °C	[1]	-	52.6	-	nC
S sc	softness factor	$I_{S} = 25 \text{ A}; \text{ dI}_{S}/\text{dt} = -100 \text{ A}/\mu\text{s}; \text{ V}_{GS} = 0 \text{ V};$ $V_{DS} = 20 \text{ V}; \text{ T}_{j} = 25 ^{\circ}\text{C}; \text{ Fig. 17}$		-	0.77	-	
		$I_{S} = 25 \text{ A}; \text{ d}_{S}/\text{dt} = -500 \text{ A}/\mu\text{s}; \text{ V}_{GS} = 0 \text{ V}; \\ \text{V}_{DS} = 20 \text{ V}; \text{ T}_{j} = 25 ^{\circ}\text{C}; \text{ Fig. 17}$		-	0.67	-	

[1] includes capacitive recovery

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#### N-channel 40 V, 0.9 mΩ logic level MOSFET in LFPAK56E



#### BUK9J0R9-40H

#### N-channel 40 V, 0.9 mΩ logic level MOSFET in LFPAK56E



#### N-channel 40 V, 0.9 m logic level MOSFET in LFPAK56E

#### **11. Package outline**



#### N-channel 40 V, 0.9 m logic level MOSFET in LFPAK56E

#### 12. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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#### N-channel 40 V, 0.9 mΩ logic level MOSFET in LFPAK56E

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