

Temporal noise analysis of charge-domain sampling readout circuits for cmos image sensors

Ge, Xiaoliang; Theuwissen, Albert J.P.

DOI

[10.3390/s18030707](https://doi.org/10.3390/s18030707)

Publication date

2018

Document Version

Final published version

Published in

Sensors

Citation (APA)

Ge, X., & Theuwissen, A. J. P. (2018). Temporal noise analysis of charge-domain sampling readout circuits for cmos image sensors. *Sensors*, *18*(3), 1-16. Article 707. <https://doi.org/10.3390/s18030707>

Important note

To cite this publication, please use the final published version (if applicable). Please check the document version above.

Copyright

Other than for strictly personal use, it is not permitted to download, forward or distribute the text or part of it, without the consent of the author(s) and/or copyright holder(s), unless the work is under an open content license such as Creative Commons.

Takedown policy

Please contact us and provide details if you believe this document breaches copyrights. We will remove access to the work immediately and investigate your claim.

Article

Temporal Noise Analysis of Charge-Domain Sampling Readout Circuits for CMOS Image Sensors [†]

Xiaoliang Ge ^{1,*}  and Albert J. P. Theuwissen ^{1,2}

¹ Electronic Instrumentation Laboratory, Delft University of Technology, 2628 CD Delft, The Netherlands; albert@harvestimaging.com

² Harvest Imaging, 3960 Bree, Belgium

* Correspondence: X.Ge-1@tudelft.nl; Tel.: +31-15-27-86518

[†] This paper is an expanded version of our paper published in Ge, X.; Theuwissen, A. A $0.5 e^-_{\text{rms}}$ Temporal-Noise CMOS Image Sensor with Charge-Domain CDS and Period-Controlled Variable Conversion-Gain. In Proceedings of the 2017 International Image Sensor Workshop, Hiroshima, Japan, 30 May–2 June 2017.

Received: 21 November 2017; Accepted: 13 February 2018; Published: 27 February 2018

Abstract: This paper presents a temporal noise analysis of charge-domain sampling readout circuits for Complementary Metal-Oxide Semiconductor (CMOS) image sensors. In order to address the trade-off between the low input-referred noise and high dynamic range, a Gm-cell-based pixel together with a charge-domain correlated-double sampling (CDS) technique has been proposed to provide a way to efficiently embed a tunable conversion gain along the read-out path. Such readout topology, however, operates in a non-stationery large-signal behavior, and the statistical properties of its temporal noise are a function of time. Conventional noise analysis methods for CMOS image sensors are based on steady-state signal models, and therefore cannot be readily applied for Gm-cell-based pixels. In this paper, we develop analysis models for both thermal noise and flicker noise in Gm-cell-based pixels by employing the time-domain linear analysis approach and the non-stationary noise analysis theory, which help to quantitatively evaluate the temporal noise characteristic of Gm-cell-based pixels. Both models were numerically computed in MATLAB using design parameters of a prototype chip, and compared with both simulation and experimental results. The good agreement between the theoretical and measurement results verifies the effectiveness of the proposed noise analysis models.

Keywords: charge-domain sampling; CMOS image sensor; low noise; non-steady-state signal analysis; dynamic range; pixel-level amplification

1. Introduction

Advanced imaging systems for high-end applications, such as scientific and medical imaging, demand high-sensitivity CMOS image sensors (CIS). The noise performance of such CIS usually determines the ultimate detection sensitivity of the overall imaging system. However, CIS generally suffer from high temporal noise, which is typically measured by the minimum number of detectable electrons (e^-_{rms}) at the input of the pixel.

To improve the temporal noise performance of the CIS, a variety of solutions have been proposed. An effective approach is to implement a high-gain stage at the column-level, as well as to minimize the capacitance associated with the floating diffusion (FD) node at the pixel-level. By virtue of the high conversion gain (CG) along the signal path, such an approach is capable of effectively reducing the input-referred noise of each pixel. Image sensors based on this architecture have been

demonstrated in several prior designs [1–11], which exhibited excellent image capturing performance along with a very impressive photon-counting capability in respect of the superior noise performance. Nevertheless, the use of a fixed high-gain amplification in the pixel, either in the charge domain or the voltage domain, inevitably leads to degradation of the dynamic range (DR), thus resulting in undesired contrast loss in the final image.

To solve this problem, a charge-domain sampling pixel readout circuit based on trans-conductance (Gm)-cells has been proposed in [12,13] as an alternative to the conventional voltage-domain implementation based on source followers (SF). The linear-charging characteristic of Gm-cells enables the implementation of a variable voltage gain at the pixel level, by means of controlling the length of the charging period. As such, the CG of the overall read-out path can be programmed according to the specific application of the CIS, without the need for reconstructing or replacing the hardware. Therefore, the proposed pixel structure is able to overcome the trade-off between the low input-referred noise and high DR.

While the operational principle and implementation details of the Gm-cell-based pixel have been described in [12,13], this paper focuses on its noise characteristic. Compared to its counterpart based on source followers, a Gm-cell-based pixel operates in a non-stationary large-signal [14] manner, i.e., its bias condition changes as a function of the operating time. Therefore, the traditional temporal noise analysis method based on steady-state small-signal models does not readily apply to a Gm-cell-based pixel. In view of the above issues, we propose an exact temporal noise model to guide the analysis and design of a Gm-cell-based pixel. The effectiveness of this model has been successfully verified with both simulation and prototype measurement results.

This paper is organized as follows. In Section 2, the operation principles of a Gm-cell-based pixel are briefly reviewed. Section 3 introduces the theoretical fundamentals used for the noise analysis and presents the details of the noise analysis model. Section 4 compares the analysis model with experimental results derived from simulations and measurements. Finally, a conclusion is given in Section 5.

2. Operating Principle and Implementation of a Gm-Cell-Based Pixel

2.1. Concept of Gm-Cell-Based Pixel

A simplified schematic of the Gm-cell-based pixel is shown in Figure 1a. Its basic architecture consists of a pinned-photodiode (PPD), a Gm-cell and a sample-and-hold (S/H) capacitor. Similar to [1], a single-ended cascode common-source topology has been chosen as the proposed pixel-level Gm-cell in consideration of the fill factor limitation. In addition, the column-parallel readout path has been implemented with the correlated-double sampling (CDS) S/H switches and capacitor banks for the sake of the simplicity of this proof-of-concept design.

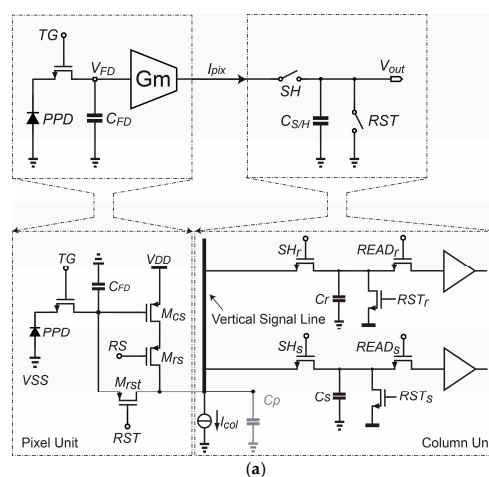


Figure 1. Cont.

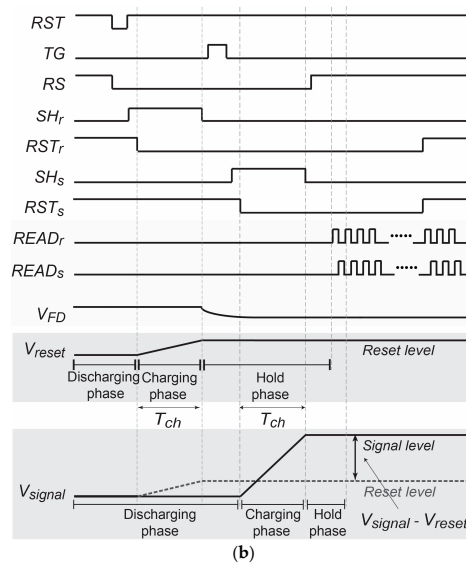


Figure 1. Basic schematic (a) and timing diagram (b) of the Gm-cell-based pixel with charge-domain sampling.

Figure 1b illustrates the associated basic operation timing diagram. In contrast to conventional voltage-sampling pixels, the voltage on the FD node, V_{FD} , is first converted to a signal-dependent current (I_{pix}) by the Gm-cell, rather than buffered by a unity-gain source follower. I_{pix} is then integrated onto the S/H capacitor ($C_{S/H}$) with a programmable duration T_{ch} , at the end of which the voltage stored on $C_{S/H}$ is readout as a representative of V_{FD} . After that, $C_{S/H}$ is reset by the switch RST to clear the charge before the next sampling phase. Such sampling process essentially operates in the charge-domain, while the pixel output signal is still in the voltage domain. Its transfer function is:

$$V_{out}(nT_s) = \frac{g_m}{C_{S/H}} \int_{nT_s}^{nT_s+T_{ch}} V_{FD}(t) dt \quad (1)$$

where g_m is the trans-conductance of M_{CS} , T_s is the sampling period and n is an integer.

Equation (1) describes a boxcar sampling process [15], which can be interpreted as the convolution integral of V_{FD} and a rectangular time window with a height of $g_m/C_{S/H}$ and a width of T_{ch} . Its transfer function can be written as [16]:

$$H(f) = \frac{g_m T_{ch}}{C_{S/H}} \cdot \frac{1 - e^{-j2\pi f T_{ch}}}{j2\pi f T_{ch}} \quad (2)$$

whose ideal magnitude transfer function can be expressed as a *sinc*-type low-pass filter with a defined DC gain:

$$|H_{CD1}(f)| = |H_W(f)| = \frac{g_m T_{ch}}{C_{S/H}} \cdot |\text{sinc}(\pi f T_{ch})| \quad (3)$$

Notice that the sampled voltage has been hold with zero-order hold (ZOH) process [17] after the charging phase, the ZOH model has been included as one term of the overall charge-domain sampling transfer function, which is shown below:

$$|H_{CD1}(f)| = |H_W(f)| \cdot |H_{ZOH}(f)| = \frac{g_m T_{ch}}{C_{S/H}} \cdot \underbrace{|\text{sinc}(\pi f T_{ch})|}_{\text{charge-domain sampling}} \cdot \underbrace{|\text{sinc}(\pi f T_{ZOH})|}_{\text{zero-order hold}} \quad (4)$$

where T_{ZOH} is the sampling period, which is equal to T_{ch} in the case of charge-domain sampling.

Intuitively, in comparison to a voltage-domain sampling readout path, a charge-domain sampling circuit introduces an additional first-order *sinc*-type low-pass filter prior to the discrete-time sampler,

resulting in a twice steeper roll-off in the overall transfer function, as shown in Figure 2. The extra *sinc*-type filter features frequency notches locating at integer multiples of $1/T_{ch}$.

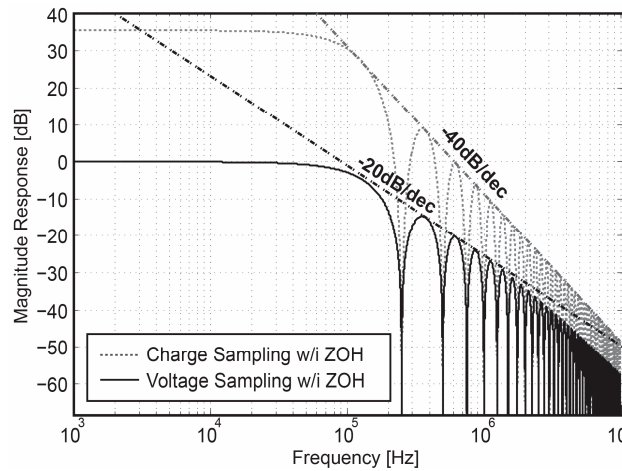


Figure 2. Transfer functions of the charge-domain sampling vs. voltage-domain sampling. The overall roll-off of the transfer function of charge-domain is -40 dB, one -20 dB introduced by the zero-order hold (ZOH) effect by the discrete-time sampler, the additional -20 dB introduced by the charge-domain sampler.

The above analysis reveals some important advantages of Gm-cell-based pixels for low-noise design: (1) A programmable voltage gain and -3 dB bandwidth can be realized by tuning the length of the charging window. Increasing T_{ch} not only boosts the voltage gain, but also narrows down the readout bandwidth, both of which are beneficial for suppressing the pixel-level input-referred noise; (2) The charge-sampling process provides an additional anti-aliasing filtering, leading to further compression of the high-frequency noise components. Both features are taken into account in the following noise analysis (Section 3).

2.2. Periodic Filtering Model of the Charge-Domain CDS

CDS is widely used in CIS for low-frequency noise reduction. By subtracting the reset level sampled at T_{rst} from the signal level sampled T_{sig} , both the offset and the flicker noise could be effectively suppressed. The effect of the CDS noise canceller can be characterized as a discrete-time (DT) high-pass filtering operation, as analysed in [18]. The transfer function of $H_{CDS}(f)$ is given by

$$|H_{CDS}(f)| = |2\sin(\pi f T_0)| \quad (5)$$

where T_0 is the sampling interval between T_{rst} and T_{sig} . A behavioral model of the Gm-cell-based pixel with charge-domain CDS is depicted in Figure 3. As two distinct filtering functions, namely, a continuous-time (CT) sinc low-pass filter $H_{WI}(f)$ [19] and a DT high-pass filter $H_{CDS}(f)$ are realized simultaneously, the overall transfer function of the charge-domain CDS can be written as

$$|H_{CD2}(f)| = |H_{CD1}(f)| \cdot |H_{CDS}(f)| = \left| 2 \frac{g_m T_{ch}}{C_{S/H}} \text{sinc}(\pi f T_{ch}) \cdot \text{sinc}(\pi f T_{ZOH}) \cdot \sin(\pi f T_0) \right| \quad (6)$$

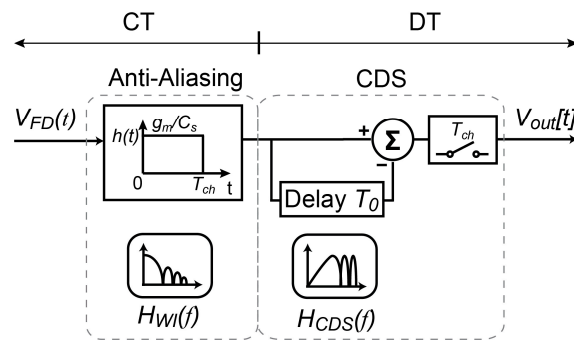


Figure 3. Block diagram model of the charge-domain correlated-double sampling (CDS).

Compared to a corresponding voltage-domain CDS transfer function, which has an equal -3 dB bandwidth, the charge-domain CDS introduces one additional group of notches. As shown by simulations in Figure 4, one group of notch frequencies is located at k/T_{ch} , owing to the charge-sampling sinc-type filter ($\text{sinc}(\pi f T_{ch})$), while the other group is placed at k/T_0 , owing to the sinc function effect ($\text{sinc}(\pi f T_0)$) of the CDS operation [20]. The joint effect of $\text{sinc}(\pi f T_{ch})$ and $\text{sinc}(\pi f T_0)$ increases the depth of the notches thus further improving the attenuation in the stop band. As such, the charge-domain CDS not only helps in reducing the low-band noise, but also provides a greater extent of attenuation on high-frequency noise components, in comparison with the voltage-domain CDS which only features first-order low-pass filtering.

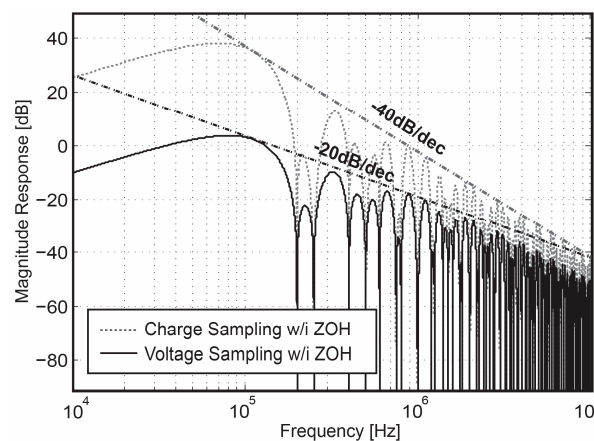


Figure 4. Transfer function of the charge-domain CDS vs. voltage-domain CDS.

3. Noise Analysis of a Gm-Cell-Based Pixel

3.1. Nonstationary Noise Theory Analysis

Temporal noise analysis on conventional CIS readout circuits is established based on the fact that the pixel-level SF operates in the steady-state [21]. As shown in Figure 5a, in the process of the voltage-domain sampling with an exponential settling behavior, the statistics properties of the temporal noise do not vary as a function of time and can be well represented by its time-averaged root-mean-square (RMS) value. However, this prerequisite is not valid for Gm-cell-based pixels. A time-domain plot of the voltage on the S/H capacitor with superimposed random noise is conceptually shown in Figure 5b. As explained in Section II, the final output signal on the S/H capacitor is obtained through a charging process. Given the fact that the proposed read-out topology works with a large signal behavior throughout its operation, the standard deviation of the voltage distribution and hence the RMS value of the noise is no longer static with time. Therefore, the conventional noise analysis method based on steady-state models is not appropriate for Gm-cell-based pixels along with

its read-out path. To quantitatively analyze the nonstationary noise, a time-domain linear analysis approach, based on the autocorrelation of nonstationary random process, has been described in [14,22]. Here, we apply a similar approach to evaluate the temporal noise characteristic of Gm-cell-based pixels.

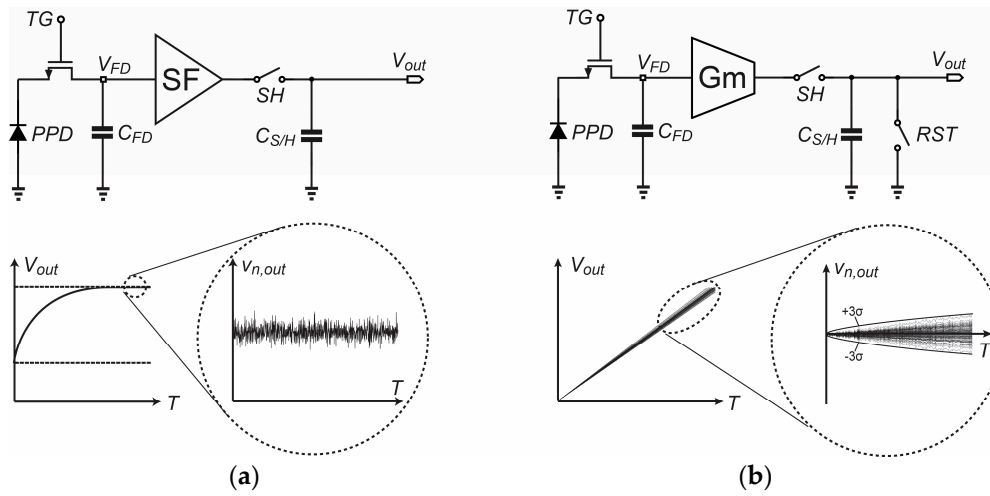


Figure 5. (a) Steady-state noise waveform for source followers (SF)-based pixel (b) Non-steady-state noise waveform for Gm-cell-based pixel.

Noise in the time-domain represents the variance of a random process, which can be derived from its autocorrelation as a function of time [23]. Suppose that the time-domain representatives of the input and output noise are $X(t)$ and $Y(t)$ respectively, the autocorrelation of the input noise between two time points (t_1 and t_2) is $R_{xx}(t_1, t_2)$ and the time-domain impulse response of the pixel readout circuit is $h_p(t)$. Thus, the autocorrelation of the output noise can be derived from time-domain convolutions [14]:

$$R_{YY}(t_1, t_2) = h_p(t_1) * R_{XX}(t_1, t_2) * h_p(t_2) \quad (7)$$

The variance of $Y(t)$ as a function of the autocorrelation is:

$$\sigma_Y^2(t) = E[Y(t)Y(t)] = R_{YY}(t_1, t_2)|_{t_1=t_2=t} \quad (8)$$

Equations (7) and (8) serve as the fundamental for the time-domain analysis of Gm-cell-based pixels. As can be seen, in order to investigate the output noise in the time-domain, all that is required is the input noise autocorrelation functions of different noise sources, as well as the impulse response from the pixel input voltage (V_{FD}) to its output. During the charging phase of a Gm-cell-based pixel, both the thermal noise and flicker noise of the Gm-cell contribute to the overall output noise. In addition, the kTC noise caused by the column-level switch also affords a part of noise in the reset phase. Therefore, in the following discussions their contributions will be investigated separately.

3.2. Equivalent Small Signal Model and Noise Gain

Although a Gm-C integrator works in a large-signal behavior throughout its charging phase, its small-signal model at the completion moment of the sampling response can still be utilized for a first-order noise analysis, due to the fact that only the noise power at that point has impacts on the final decision. Figure 6 shows the noise model and the equivalent small signal model of the readout path of a Gm-cell-based pixel.

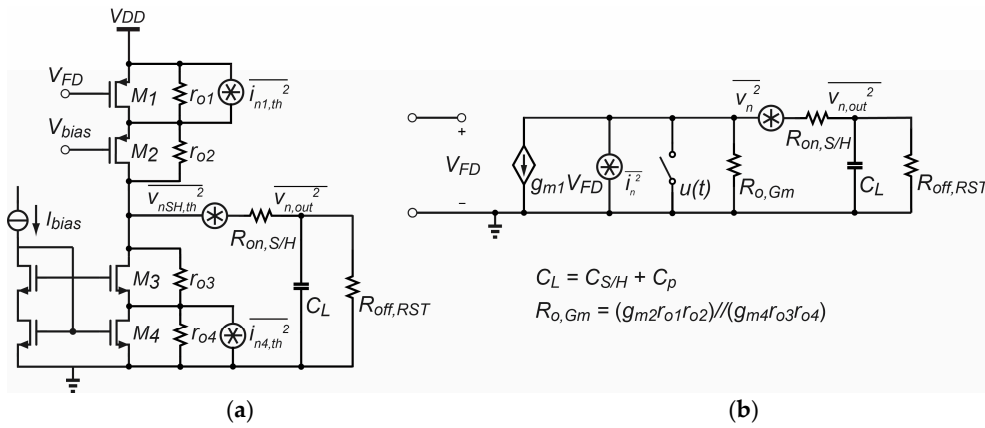


Figure 6. (a) Noise model (b) Small signal model of Gm-cell-based pixel.

In order to facilitate the noise optimization, the mentioned output noise power needs to be referred to the FD node. For this purpose, the noise gain of the Gm-cell-based pixel must be first calculated.

According to [14], the noise gain of an integrator-like Gm-cell can be determined by the ratio of voltage slopes at the output and input ports:

$$|A_N| = \left(\frac{dV_{out}(t)}{dt} \Big|_{t=T_{ch}} \right) / \left(\frac{dV_{FD}(t)}{dt} \Big|_{t=T_{ch}} \right) \quad (9)$$

The input signal V_{FD} of the Gm-cell during the charging phase can be assumed as a slow ramp and is given as:

$$V_{FD}(t) = Ku(t) \quad (10)$$

where K is the input voltage magnitude. The time-domain response of a Gm-cell to a step ramp input is given by:

$$V_{out}(t) = A_0 K (1 - e^{-t/\tau}) u(t) \quad (11)$$

$$\text{where } A_0 = g_{m1} R_{OUT} = g_{m1} (R_{o,Gm} || (R_{off,RST} + R_{on,S/H})) \text{ and } \tau = R_{OUT} \cdot (C_{S/H} + C_p)$$

where $A_0 = g_{m1} R_{OUT}$ is the DC gain of the Gm-cell at the steady-state, τ is the time constant of the Gm-C integrator, $R_{o,Gm}$ is the output impedance of the Gm-cell, $R_{on,S/H}$ is the on-resistance of switch SH , the value of which is much smaller than $R_{o,G}$, $R_{off,RST}$ is the off-resistance of switch RST ($RSTr$ and $RSTs$). Thus, the final noise gain of the Gm-cell can be described by the following expression:

$$|A_N|_{t=T_{ch}} = A_0 (1 - e^{-T_{ch}/\tau}) \quad (12)$$

Note that this result can be simplified into two special cases:

$$|A_N| = \begin{cases} g_{m1} R_{OUT}, & \text{for } T_{ch} \gg \tau \\ g_{m1} T_{ch} / C_L, & \text{for } T_{ch} \ll \tau \end{cases} \quad (13a)$$

$$(13b)$$

To be more precise, a plot showing the variation of the noise gain factor as a function of charging period T_{ch} with a wide range of time constant τ . As revealed in Figure 7a and Equation (13a), with a constant T_{ch} and the time-boundary $T_{ch} \gg \tau$, the noise gain increases as τ and R_{OUT} increasing, which showing the steady-state noise gain characteristic of a broadband amplifier. Figure 7b and Equation (13b) shows an integrator-like noise gain with the time-boundary $T_{ch} \ll \tau$ resulting from the charge-sampling process as explained in [2], which is inversely proportional to τ and C_L with a constant T_{ch} .

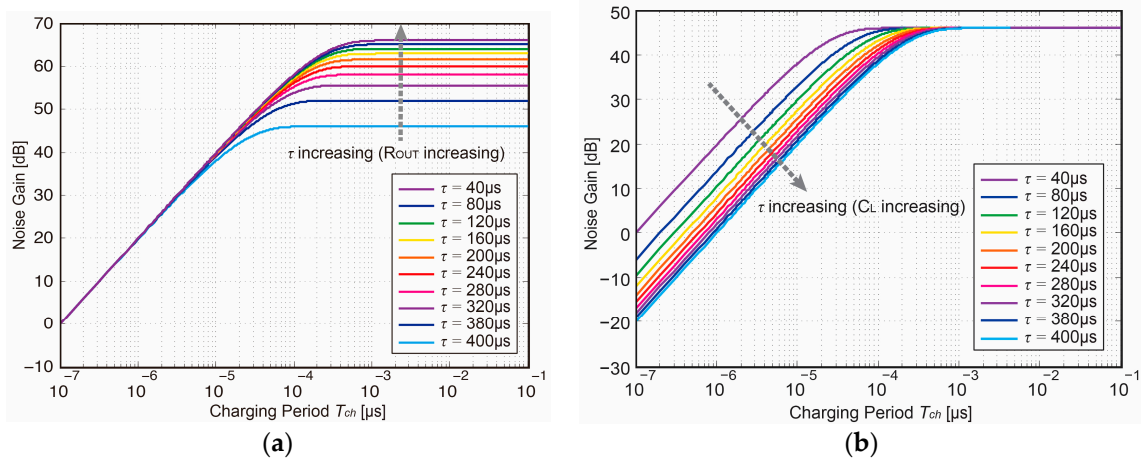


Figure 7. Noise gain factor as a function of charging period T_{ch} (a) with τ and R_{OUT} increasing (b) with τ and C_L increasing.

3.3. Noise Model of Charging Phase

3.3.1. Thermal Noise

In a Gm-cell small signal model, the impulse response from the noise current source to the output voltage V_{out} , which is given by [14]:

$$h_p(t) = \frac{1}{C_L} e^{-t/\tau} u(t) = \frac{1}{C_{S/H} + C_p} e^{-t/\tau} u(t) \quad (14)$$

where $C_{S/H}$ is the loading S/H capacitance, C_p is the parasitic capacitance of the column net, $R_{o,Gm}$ is the output impedance of the Gm-cell, τ is the time constant of this Gm-C integrator and $u(t)$ is the noise current unit step input.

Consider a white noise unit step input $u_n(t)$, the autocorrelation function of the thermal noise source is a Dirac delta function with an amplitude equal to its double-sided power spectral density (PSD) [23]:

$$R_{XX,th}(t_1, t_2) = \frac{S_{th,n}}{2} \delta(t_2 - t_1) \quad (15)$$

where $S_{th,n}$ is the equivalent single-sided temporal noise PSD. According to Figure 6, the noise sources include the equivalent current noise source i_n from the pixel-level Gm-cell and the equivalent voltage noise source v_n from the column-level sample-and-hold switch SH (SHr and SHs). Thus, $S_{th,n}$ can be modelled as:

$$S_{th,n} = 4kTg_n + \frac{4kTR_{on,S/H}}{R_{OUT}^2} \approx \frac{8}{3}kT(g_{m1} + g_{m4}) \quad (16)$$

where $g_n = 2(g_{m1} + g_{m4})/3$ is the equivalent noise trans-conductance of i_n , $k = 1.3807 \times 10^{-23}$ J/K is the Boltzmann constant and T is absolute temperature in Kelvin.

By substituting Equations (14) and (15) into Equations (7) and (8), we obtain the variance of the output voltage due to the time-variant thermal noise, as given by:

$$\sigma_{Y,th}^2(t) = \frac{S_{th,n}}{2} \int_0^t |h(\alpha)|^2 d\alpha \quad (17)$$

with the aid of Equation (16), the above integral can be solved as:

$$\sigma_{Y,th}^2(t) = \frac{2}{3} \cdot \frac{kT}{C_{S/H} + C_p} (g_{m1} + g_{m4}) R_{OUT} (1 - e^{-2t/\tau}) u(t) \quad (18)$$

In a charge sampling circuit, only the noise at the instant of the sampling completion (T_{ch}) has impact on the final readout noise. Accordingly, the concerned output thermal noise power of a Gm-cell-based pixel can be evaluated as:

$$\overline{v_{n,th}^2} = \sigma_{Y,th}^2(t) \Big|_{t=T_{ch}} = \frac{2}{3} \cdot \frac{kT}{C_{S/H} + C_p} (g_{m1} + g_{m4}) R_{OUT} \left(1 - e^{-2T_{ch}/\tau}\right) \quad (19)$$

On the basis of Equations (12) and (19), the input thermal noise power can be derived by:

$$\overline{v_{in,th}^2} = \sigma_{Y,th}^2(t) \Big|_{t=T_{ch}} / |A_N| \Big|_{t=T_{ch}}^2 = \frac{2}{3} \cdot \frac{kT}{\tau} \cdot \left(\frac{1}{g_{m1}} + \frac{g_{mz}}{g_{m1}^2} \right) \cdot \coth\left(\frac{T_{ch}}{2\tau}\right) \quad (20)$$

$$= \begin{cases} 4kT \cdot \frac{2}{3} \left(\frac{1}{g_{m1}} + \frac{g_{mz}}{g_{m1}^2} \right) \cdot \frac{1}{4\tau}, & \text{for } T_{ch} \gg 2\tau \quad (21a) \\ 4kT \cdot \frac{2}{3} \left(\frac{1}{g_{m1}} + \frac{g_{mz}}{g_{m1}^2} \right) \cdot \frac{1}{2T_{ch}}, & \text{for } T_{ch} \ll 2\tau \quad (21b) \end{cases}$$

As Equation (20) contains a hyperbolic function of the ratio of the charging time T_{ch} and time constant τ , the time limits $T_{ch} \gg \tau$ and $T_{ch} \ll \tau$ are thus of interest. Figure 8 showing the variation of the input thermal noise power with a wide range of time constant τ . With a fixed T_{ch} , the input-referred thermal noise decreases as τ increases at the time-boundary of $T_{ch} \ll \tau$. Its noise behavior is identical with the input-referred thermal noise power in common single-pole steady-state systems. On the other hand, if $T_{ch} \gg \tau$, the input-referred thermal noise linearly reduces as T_{ch} gets longer for a given τ . Within this region, the thermal noise becomes T_{ch} -dependent and behaves as an integrator-like noise. As such, this interesting characteristic offers an orientation to the thermal noise estimation in the specific design of Gm-cell-based pixels.

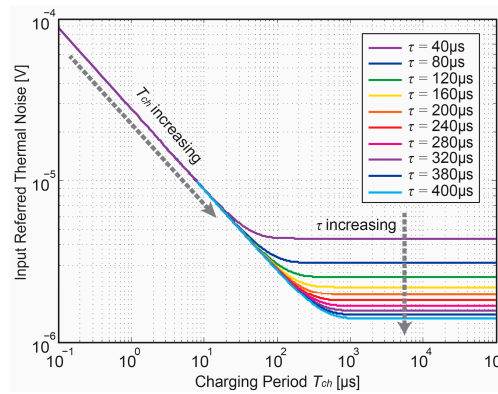


Figure 8. Input referred thermal noise as a function of charging period T_{ch} .

3.3.2. Flicker Noise

Flicker noise in CIS refers to those noise sources whose PSD is inversely proportional to the frequency. The flicker noise PSD sourced from the input MOS transistor of the Gm-cell can be modeled as:

$$S_{1/f,n} = \frac{K}{C_{ox} \cdot A} \cdot \frac{1}{f} \cdot (g_{m1} + g_{m4})^2 \quad (22)$$

where K is a process-dependent constant, C_{ox} is the unit oxide capacitance of the MOS gate, and A is the channel area. In contrast to thermal noise, the time-domain response of flicker noise is a nonstationary process [22].

$$R_{XX,1/f}(t_1, t_2) = h_{1/f}(t_1) * R_{XX,th}(t_1, t_2) * h_{1/f}(t_2) \quad (23)$$

where $h_{1/f}(t)$ is the impulse response of an ideal $1/f$ noise-shaping filter:

$$h_{1/f}(t) = \left(\frac{2f_c}{t} \right)^{1/2} u(t) \quad (24)$$

Here, f_c is the corner frequency of the flicker noise, which is relevant to the process and transistor parameter:

$$f_c = \frac{K}{C_{ox} \cdot A \cdot S_{th,n}} \cdot (g_{m1} + g_{m4})^2 \quad (25)$$

Based on Equations (23)–(25), the autocorrelation of flicker noise can be expressed as:

$$R_{XX,1/f}(t_1, t_2) = \frac{K(g_{m1} + g_{m4})^2}{C_{ox}A} \int_0^\infty \frac{1}{[\mu \cdot (t_2 - t_1 + \mu)]^{1/2}} u(\mu) d\mu \quad (26)$$

Equation (26) appears as a divergent integral function of time [24–27] and does not have a finite limit. To address this issue, characterization of the flicker noise is often reasonably limited to a finite length of observation time window [24] (or a limited bandwidth in the frequency domain [23]). The minimum of this time window (t_{min}) is defined by the reciprocal of the upper limit of the concerned frequency range, i.e., the flicker corner frequency (f_c), while the total operation time of the readout circuit (t_{op}) determines the maximum. Based on this approximation, the autocorrelation of flicker noise can be written as [23]:

$$R_{XX,1/f}(t_{op}, t_1, t_2) \cong \frac{K(g_{m1} + g_{m4})^2}{C_{ox}A} \ln \frac{4t_{op}}{|t_2 - t_1|}, \text{ where } \frac{1}{f_c} \ll |t_2 - t_1| \ll t_{op} \quad (27)$$

By substituting Equations (14) and (27) into Equations (6) and (7), the variance of the pixel output voltage owing to flicker noise can be expressed as:

$$\sigma_{Y,1/f}^2(t) = \frac{(g_{m1} + g_{m4})^2 R_{OUT} K}{2(C_{S/H} + C_p) C_{ox} A} \int_0^t \left(\ln \frac{4t_{op}}{\alpha} \right) \cdot [1 - e^{-2(t+\alpha)/\tau}] u(\alpha) d\alpha \quad (28)$$

Similarly, the output flicker noise at the sampling instant is evaluated at T_{ch} :

$$\overline{v_{n,1/f}^2} = \sigma_{Y,1/f}^2(t) \Big|_{t=T_{ch}} \quad (29)$$

However, the integral in Equation (28) does not have an analytic solution. Therefore, Equations (28) and (29) must be numerically computed in MATLAB to get a quantitative evaluation of the flicker noise power. Note that t_{op} should be assigned with a sufficiently large value to ensure the accuracy of approximation (typically around one hour [22]). Take the CDS effect into consideration, the impulse response of the ideal $1/f$ noise-shaping filter are assumed as $h_{1/f}(T_{ch})$ and $h_{1/f}(T_0 + 2T_{ch})$. Therefore, the autocorrelation of the flicker noise with CDS is given as:

$$R_{XX,1/f}(T_{ch}, T_0 + 2T_{ch}) = h_{1/f}(T_{ch}) * R_{XX,th}(T_{ch}, T_0 + 2T_{ch}) * h_{1/f}(T_0 + 2T_{ch}) \quad (30)$$

where T_0 is the interval period between two samples (reset level and signal level) which is assumed as $T_0 = T_{ch} + 1 \mu s$. Consequently, the output flicker noise power after CDS can be defined by:

$$\sigma_{Y,1/f}^2(t) = \frac{(g_{m1} + g_{m4})^2 R_{OUT} K}{2(C_{S/H} + C_p) C_{ox} A} \int_{T_{ch}}^{2T_{ch}+T_0} \left(\ln \frac{4t_{op}}{\alpha} \right) \cdot [1 - e^{-2(2T_{ch}+T_0+\alpha)/\tau}] u(\alpha) d\alpha \quad (31)$$

As a brief proof, Figure 9 numerically plot the flicker noise output power as a function of charging time T_{ch} . In contrast to thermal noise output power whose value reaches steady-state until $T_{ch} \approx 2\tau$,

flicker noise is continuously accumulated with an increasing T_{ch} , which agrees with the theoretical analysis of the flicker noise in frequency domain.

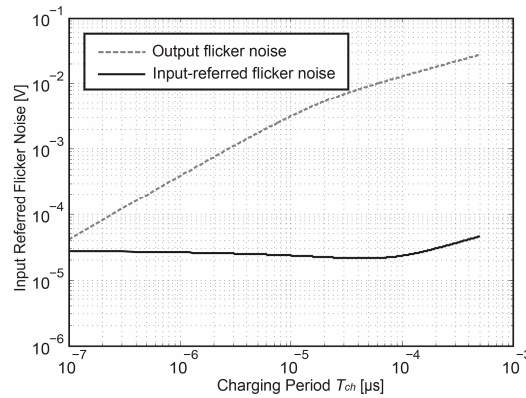


Figure 9. Input referred flicker noise as a function of charging period T_{ch} .

According to our circuit level simulations, the corner frequency f_c is around 500 kHz, which is higher than the equivalent noise bandwidth of the proposed circuit, and thus the flicker noise obviously appears even beyond the noise bandwidth. As a result, the input-referred flicker noise is highly dependent on T_{ch} and it is effectively reduced through increasing T_{ch} . On the contrary, as the Gm-cell enters into the steady-state region when T_{ch} gets longer. The input-referred flicker begins to increase due to a constant noise gain and noise bandwidth.

3.4. Noise Model of Discharging Phase

In order to segregate the sampling operations between two adjacent frames, the S/H capacitor is discharged by switching on RST ($RSTr$ and $RSTs$) before the next new frame. As the switch operation during this process is considered to have reached stationary levels with an exponential settling behavior, the noise is therefore exhibit as the steady-state. By using the first-order low-pass filter transfer function, the thermal noise power caused by switch RST is calculated as:

$$\overline{v_{n1,kTC}^2} = \int_0^\infty 4kTR_{on,RST} \frac{1}{1 + (2\pi f R_{on,RST} C_L)^2} = \frac{kT}{C_L} \quad (32)$$

where $R_{on,RST}$ is the on-resistance of switch RST . Different from the voltage-domain sampling circuit, the charging phase follows the switch off of RST . As a consequence, part of the noise charge on C_L is discharged in the charging phase with a non-stationary random process and thus the resulting noise power from RST is given as:

$$\overline{v_{n,kTC}^2} = \sigma_{Y,kTC}^2(t) \Big|_{t=T_{ch}} = \frac{kT}{C_L} - \sigma_{n1,kTC}^2(t) \Big|_{t=T_{ch}} = \frac{kT}{C_L} - \frac{kT}{C_L} \int_0^t |h(\alpha)|^2 d\alpha = \frac{kT}{C_L} e^{-2T_{ch}/\tau} \quad (33)$$

where the term $e^{-2T_{ch}/\tau}$ represent the amplitude degrading during the charging phase. The value of the kTC noise from the discharging phase are also numerically investigated, with results presented in Figure 10a,b.

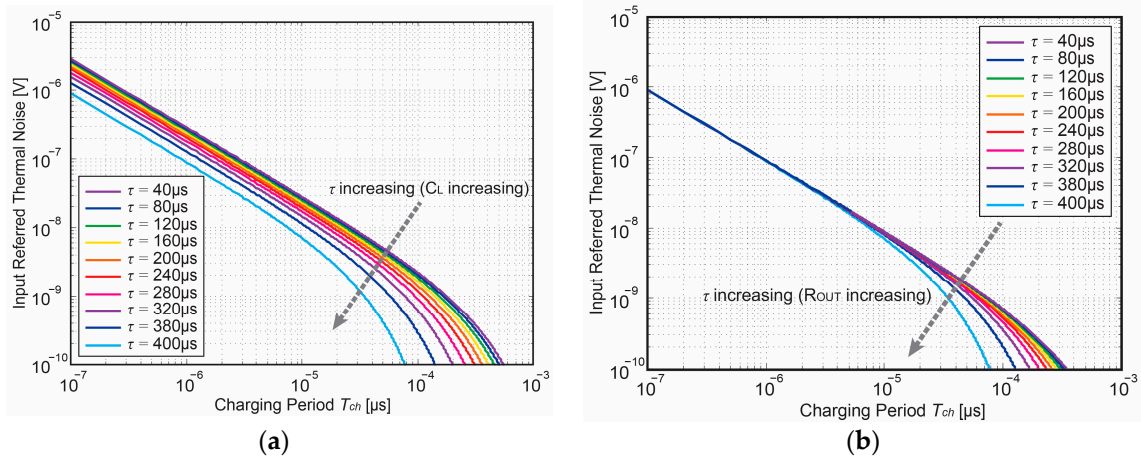


Figure 10. Input referred kTC noise as a function of charging period T_{ch} during discharging phase (a) with τ and C_L increasing (b) with τ and R_{OUT} increasing.

3.5. Overall Input-Referred Noise

Consequently, the overall input-referred temporal noise power of a Gm-cell-based pixel can be calculated by:

$$\begin{aligned} \overline{v_{n,in}^2} &= \frac{2\sigma_{Y,th}^2(t)|_{t=T_{ch}} + \sigma_{Y,1/f}^2(t)|_{t=T_{ch}} + 2\sigma_{Y,kTC}^2(t)|_{t=T_{ch}}}{|A_N|^2} \\ &= \frac{2\sigma_{Y,th}^2(t)|_{t=T_{ch}} + \sigma_{Y,1/f}^2(t)|_{t=T_{ch}} + 2\sigma_{Y,kTC}^2(t)|_{t=T_{ch}}}{[g_{m1}T_{ch}/(C_{S/H} + C_p)]^2}, \text{ where } T_{ch} \ll \tau \end{aligned} \quad (34)$$

The combination of formulas (20), (31) and (33) provides an effective way to predict and calculate the temporal noise power of Gm-cell-based pixels in the time domain. Given the fact that the proposed circuit operates as a Gm-C integrator, T_{ch} should be settled with the range of $T_{ch} \ll \tau$. Applying the device parameters used for the design of the CIS chip as listed in Table 1, the noise components of the readout circuits and the resulting total noise are calculated in MATLAB, which is shown in Section 4 as a comparison of measurement result.

Table 1. Device parameter used for the noise estimation.

Parameter	Value	Parameter	Value
g_{m1}	30 μ S	A	3 μ m (W) \times 0.5 μ m (L)
C_p	2 pF	K	1×10^{-25}
$R_{o,Gm}$	20 M Ω	Cox	4.3 fF/ μ m ²
k	1.38×10^{-23}	fc	500 kHz
T	300 K	top	~1 h

4. Implementation and Experimental Results

The test sensor with the proposed pixel architecture has been fabricated in a 0.18 μ m 1P4M standard CIS process technology. The test pixels have been divided into six sub-groups, each of which includes 20 (H) \times 32 (V) pixels and features the same pixel pitch of 11 μ m. For flexibility, the digital logic, which implements the charging clocks T_{ch} and other operating clocks are realized off-chip. By performing these double charging processes, the resulting voltage level V_{reset} and V_{signal} are held on C_r and C_s respectively and are sequentially readout from the CIS chip via multiplexers and output buffers. An off-chip 16-bit ADC with an LSB of 30 μ V has been implemented on the PCB to convert the analog output voltage levels into digital signal. The voltage subtraction of the reset level and the signal

level ($V_{reset} - V_{signal}$) is then performed in the digital domain with the aid of an NI-IMAQ (National Instruments–Vision Acquisition Software) 16.2. In this way, we realize the CDS in digital domain and obtain the period-controlled amplified video signal $V_{signal} - V_{reset}$ with the charge-domain CDS.

A critical parameter for the evaluation of the temporal noise is the conversion gain. The pixel-level conversion gain CG_{tot} associated with the period-controlled function has been measured by using the photon transfer curve (PTC) measurement technique. Figure 11 shows the measured conversion gain $CG_{tot} = CG_{FD} \times A_{pix}$ of the fabricated Gm-cell-based pixel, where CG_{FD} is the conversion gain at the FD node. To separately investigate the gain factor A_{pix} of the charge-sampling pixel, we also measured the CG_{FD} of a unity-gain pMOS SF-based 4T-pixel [28] as a reference for comparison, whose the FD node is laid out with the same area as the proposed pixel. Note that the CG_{FD} of the SF-based pixel is measured as $55 \mu\text{V}/e^-$, which indicates that the nominal value A_{pix} of the charge-sampling pixel is around $\times 30$. The measurement results show that CG_{tot} can be programmable from $50 \mu\text{V}/e^-$ to $1.6 \text{ mV}/e^-$ when a charging period from 100 ns to $4 \mu\text{s}$ is applied.

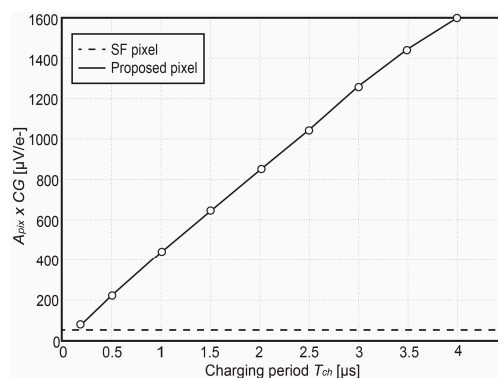


Figure 11. Measured conversion gain ($CG_{FD} \times A_{pix}$) as a function of the charging period T_{ch} [12].

The temporal noise characterization has been done in the dark and implemented by keeping the transfer gate TG off during the measurement period. Figure 12 shows the measured input-referred noise of the proposed pixel as a function of T_{ch} . The noise-reduction tendency initially is proportional to $1/T_{ch}$ and later becomes proportional to $1/\sqrt{T_{ch}}$. This result indicates that the Gm-cell-based pixel not only reduces the noise originating from the exceeding circuits connected at the back of the pixel as a result of the signal amplification of the charge-sampling technique, but also suppresses the thermal noise generated by the pixel level circuit as a result of noise-bandwidth reduction. At $T_{ch} = 4 \mu\text{s}$, the pixel achieves an input-referred noise of $0.51 e^-_{\text{rms}}$. In addition, when referred the noise back to the input of the signal chain in the voltage domain by dividing its corresponding gain factor A_{pix} , the lowest measured input-referred noise level is found around $27 \mu\text{V}$, which is shown and compared with other state-of-the-art low-noise CIS in Figure 13. This figure presents that an improvement in figure-of-merit regarding the read-out noise reduction was successfully obtained by using the proposed Gm-cell-based pixel and charge-domain CDS technique.

For verification of the time-domain noise analysis model, Figure 14 shows the measured input-referred noise with a comparison to the simulation results in voltage domain. In the calculation results described above, noise due to the clock jitter effect and sample and hold process, as well as the noise generated from the board-level succeeding readout circuits are ignored. As a result, there is a noise value deviation between the calculation and measurement results. Moreover, because of the trans-conductance is V_{FD} -dependent and the Gm-cell is open loop, the g_m variation degrades the pixel output linearity, leading to a noise reduction factor deviation between two results. As Figure 14 indicates, the noise reduction tendency obtained from the calculation model shows a steeper slope than the measurement results. However, the measured and calculated results show a reasonable agreement

on the noise reduction tendency, demonstrating the validity of the noise calculation by using the time-domain noise analysis model.

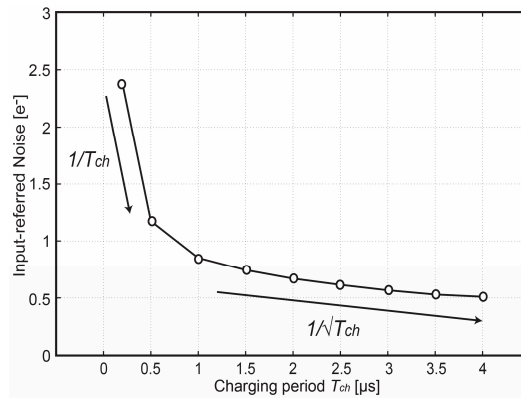


Figure 12. Measured input-referred noise as a function of the charging period T_{ch} [12].

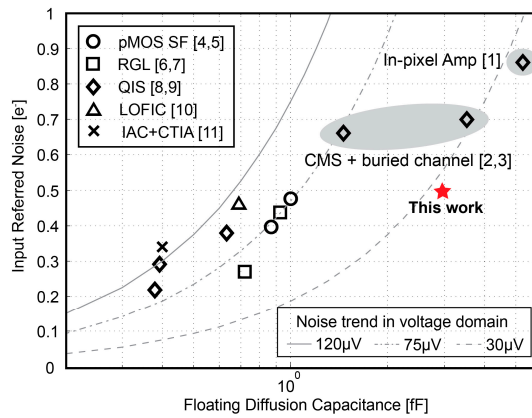


Figure 13. Comparison of input-referred noise in the electron domain vs. FD capacitance, and noise trend in the voltage domain with reported image sensors. The values are based on the best guess with the known values of CG_{FD} in reported publications [13].

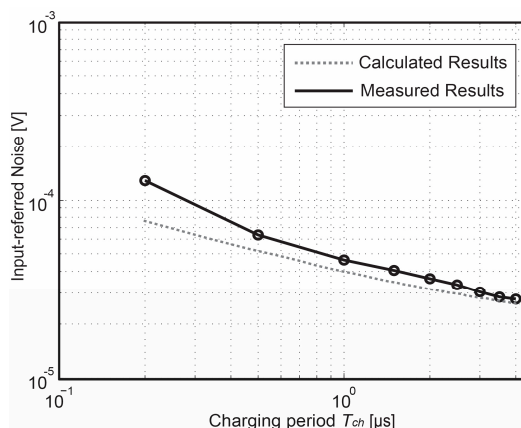


Figure 14. Input-referred noise in voltage domain as a function of T_{ch} for measured and simulated results.

5. Conclusions

A Gm-cell-based CMOS image sensor pixel structure that realizes a tunable conversion gain with a charge-domain CDS scheme was proposed for applications in low-noise high-DR image sensors.

In contrast to conventional CIS pixel architectures, a Gm-cell-based pixel operates in a large-signal manner, and its noise behaves as a function of time. To allow a precise and predictive noise performance optimization for such type of pixels and their readout circuits, a non-stationary thermal and flicker noise analysis model based on a time-domain approach is presented and discussed in this paper. By comparing the numerical results derived from the proposed models with both simulation and experimental results, which showed a reasonable agreement, the effectiveness of the theoretical analysis model was verified.

Acknowledgments: The authors would like to thank TowerJazz for their support in silicon fabrication and the Dutch technology foundation STW for sponsoring the project.

Author Contributions: X.G. developed and designed the prototype CIS test chip; performed the characterization work and contributed to write this paper. A.T. contributed to the paper redaction, project conception, management and academic supervision.

Conflicts of Interest: The authors declare no conflict of interest.

References

1. Lotto, C.; Seitz, P.; Baechler, T. A Sub-Electron Readout Noise CMOS Image Sensor with Pixel-Level Open-Loop Voltage Amplification. *IEEE Int. Solid-State Circuits Conf. Dig. Tech. Papers* **2011**, 402–403. [[CrossRef](#)]
2. Chen, Y.; Xu, Y.; Chae, Y.; Mierop, A.; Wang, X.; Theuwissen, A. A $0.7 e^-_{\text{rms}}$ Temporal-Readout-Noise CMOS Image Sensor for Low-Light-Level Imaging. In Proceedings of the 2012 IEEE International Solid-State Circuits Conference, San Francisco, CA, USA, 19–23 February 2012; pp. 384–385. [[CrossRef](#)]
3. Yeh, S.-F.; Chou, K.-Y.; Tu, H.-Y.; Chao, C.Y.-P.; Hsueh, F.-L. A $0.66 e^-_{\text{rms}}$ Temporal-Readout-Noise 3D-Stacked CMOS Image Sensor with Conditional Correlated Multiple Sampling (CCMS) Technique. *Proc. Symp. VLSI Circuits* **2015**, *53*, 527–537. [[CrossRef](#)]
4. Boukhayma, A.; Peizerat, A.; Enz, C. A Sub-0.5 Electron Read Noise VGA Image Sensor in a Standard CMOS Process. *IEEE J. Solid-State Circuits* **2016**, *51*, 2180–2191. [[CrossRef](#)]
5. Boukhayma, A.; Peizerat, A.; Enz, C. Temporal readout noise analysis and reduction techniques for low-light CMOS image sensors. *IEEE Trans. Electron Devices* **2016**, *63*, 72–78. [[CrossRef](#)]
6. Seo, M.-W.; Kawahito, S.; Kagawa, K.; Yasutomi, K. A $0.27 e^-_{\text{rms}}$ read noise $220 \mu\text{V}/e^-$ conversion gain reset-gate-less CMOS image sensor with $0.11 \mu\text{m}$ CIS process. *IEEE Electron Device Lett.* **2015**, *36*, 1344–1347.
7. Seo, M.-W.; Wang, T.; Jun, S.-W.; Akahori, T.; Kawahito, S. A $0.44 e^-_{\text{rms}}$ Read-Noise 32 fps 0.5 Mpixel High-Sensitivity RG-Less-Pixel CMOS Image Sensor Using Bootstrapping Reset. In Proceedings of the 2017 IEEE International Solid-State Circuits Conference (ISSCC), San Francisco, CA, USA, 5–9 February 2017; pp. 80–81. [[CrossRef](#)]
8. Ma, J.; Fossum, E. Quanta Image Sensor Jot with Sub $0.3 e^-_{\text{rms}}$ Read Noise and Photon Counting Capability. *IEEE Electron Device Lett* **2015**, *36*, 926–928. [[CrossRef](#)]
9. Ma, J.; Starkey, K.; Rao, A.; Odame, K.; Fossum, E. Characterization of Quanta Image Sensor Pump-Gate Jots with Deep Sub-electron Read Noise. *IEEE J. Electron Devices Soc.* **2015**, *3*, 472–480. [[CrossRef](#)]
10. Wakashima, S.; Kusuhara, F.; Kuroda, R.; Sugawa, S. A linear response single exposure CMOS image sensor with $0.5 e^-$ readout noise and $76 ke^-$ full well capacity. In Proceedings of the 2015 Symposium on VLSI Circuits (VLSI Circuits), Kyoto, Japan, 17–19 June 2015; pp. C88–C89. [[CrossRef](#)]
11. Yao, Q.; Dierickx, B.; Dupont, B.; Ruttens, G. CMOS image sensor reaching $0.34 e^-_{\text{rms}}$ read noise by inversion-accumulation cycling. In Proceedings of the International Image Sensor Workshop (IISW), Vaals, The Netherlands, 8–11 June 2015.
12. Ge, X.; Theuwissen, A. A $0.5 e^-_{\text{rms}}$ Temporal-Noise CMOS Image Sensor with Charge-Domain CDS and Period-Controlled Variable Conversion Gain. In Proceedings of the International Image Sensor Society Workshop, Hiroshima, Japan, 30 May–2 June 2017; pp. 290–293.
13. Ge, X.; Theuwissen, A. A $0.5 e^-_{\text{rms}}$ Temporal-Noise CMOS Image Sensor with Gm-Cell-Based Pixel and Period-Controlled Variable Conversion Gain. *IEEE Trans. Electron Devices* **2017**, *64*, 5019–5024. [[CrossRef](#)]
14. Sepke, T.; Holloway, P.; Sodini, C.G.; Lee, H.-S. Noise analysis for comparator-based circuits. *IEEE Trans. Circuits Syst.* **2004**, *56*, 541–553. [[CrossRef](#)]

15. Karvonen, S.; Riley, T.; Kurtti, S.; Kostamovaara, J. A quadrature chargedomain sampler with embedded FIR and IIR filtering functions. *IEEE J. Solid-State Circuits* **2006**, *41*, 507–515. [[CrossRef](#)]
16. Mirzaei, A.; Chehrazi, S.; Bagheri, R.; Abidi, A. Analysis of first-order antialiasing integration sampler. *IEEE Trans. Circuits Syst. I Regular Papers* **2008**, *55*, 2994–3005. [[CrossRef](#)]
17. Unser, M. Sampling-50 Years after Shannon. *Proc. IEEE* **2000**, *88*, 569–587. [[CrossRef](#)]
18. Wey, H.; Guggenbuhl, W. Noise transfer characteristics of a correlated double sampling circuit. *IEEE Trans. Circuits Syst.* **1986**, *29*, 1028–1030. [[CrossRef](#)]
19. Tohidian, M.; Madadi, I.; Staszewski, R.B. Analysis and design of a high-order discrete-time passive IIR low-pass filter. *IEEE J. Solid-State Circuits* **2014**, *49*, 2575–2587. [[CrossRef](#)]
20. Martin-Gonthier, P.; Magnan, P. CMOS image sensor noise analysis through noise power spectral density including undersampling effect due to readout sequence. *IEEE Trans. Electron Devices* **2014**, *61*, 2834–2842. [[CrossRef](#)]
21. Kawai, N.; Kawahito, S. Noise analysis of high-gain, low-noise column readout circuits for CMOS image sensors. *IEEE Trans. Electron Devices* **2004**, *51*, 185–194. [[CrossRef](#)]
22. Papoulis, A. *Probability, Random Variables, and Stochastic Process*; McGraw-Hill, Inc.: New York, NY, USA, 1991; pp. 310–311. ISBN 0-07-048477-5.
23. Terry, S.C.; Blalock, B.J.; Rochelle, J.M.; Ericson, M.N.; Caylor, S.D. Time-domain noise analysis of linear time-invariant and linear time-variant systems using MATLAB and HSPICE. *IEEE Trans. Nuclear Sci.* **2005**, *52*. [[CrossRef](#)]
24. Chow, A.; Lee, H. Transient noise analysis for comparator-based switched-capacitor circuits. In Proceedings of the 2007 IEEE International Symposium on Circuits and Systems, New Orleans, LA, USA, 27–30 May 2007; pp. 953–956.
25. Tian, H.; Gamal, A.E. Analysis of $1/f$ noise in CMOS APS. In Proceedings of the 2000 Electronic Imaging, San Jose, CA, USA, 15 May 2000; pp. 421–430. [[CrossRef](#)]
26. Redeka, V. $1/f$ noise in physical measurements. *IEEE Trans. Nuclear Sci.* **1969**, *16*, 17–35. [[CrossRef](#)]
27. Guo, W. Flicker noise process analysis. In Proceedings of the 1993 IEEE International Frequency Control Symposium, Salt Lake City, UT, USA, USA, 2–4 June 1993. [[CrossRef](#)]
28. Ge, X.; Theuwissen, A. A CMOS image sensor with nearly unity-gain source follower and optimized column amplifier. In Proceedings of the 2016 IEEE SENSORS, Orlando, FL, USA, 30 October–3 November 2016. [[CrossRef](#)]



© 2018 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (<http://creativecommons.org/licenses/by/4.0/>).