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High-Accuracy Generalized Average Model of Dual Active Bridge Converters

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ABSTRACT This article presents a detailed procedure for deriving the generalized average model (GAM) of a dual active bridge converter. The proposed model incorporates higher orders of harmonic components to increase accuracy. Moreover, the turn ratio of the high-frequency transformer (N_t) is considered for realistic modeling, which removes the conventional assumption of unity turn ratio. A detailed model of the DAB will ensure an accurate control design. Required mathematical expressions are derived and explained thoroughly, with an example showcasing a GAM model of the DAB converter up to the ninth harmonics. Several GAM models using different harmonic orders (first, third, fifth, seventh, and ninth harmonics) are derived and compared to the PLECS simulation model and a real-time simulation of the DAB converter on the PLECS RT-Box-2. Results show that including up to the ninth harmonics in the proposed model of the DAB converter leads to achieving accurate voltage and current amplitudes that are almost identical to the simulation outputs and even better than the experimental results.

INDEX TERMS Average model, generalized average model, dual active bridge.

I. INTRODUCTION

Power electronics converters are pivotal in the effective management of energy transfer in renewable energy systems (RES). The application spectrum of power electronic converters is extensive, encompassing electric machine motion control, switched-mode power supplies (SMPS), lighting drive systems, energy storage solutions, distributed power generation, active power filters, flexible AC transmission systems (FACTS), as well as in vehicular and embedded technologies. Control objectives should be defined based on the application [1] to operate a power converter. The foundation for designing these control objectives and for the overall control strategy of a power electronics converter lies in understanding its mathematical modeling. These models are crucial for comprehending, analyzing, and designing power electronics systems. This helps to anticipate performance, refine designs, and devise effective control strategies, ensuring that the converters function optimally within their intended applications. Among the various DC-DC converters, the Dual Active Bridge (DAB) converter has been widely researched in the literature thanks to providing galvanic isolation. Ever since its invention in the early 1990s [2], it has been used in

a plethora of applications such as in DC grids [3], [4], [5], [6], [7], [8], [9], Solid State Transformers (SSTs) [10], [11], [12], automotive applications [13], [14], [15], energy storage systems [16], [17], [18], and aerospace [19], [20]. Moreover, this converter topology also exhibits potential within the application of high-power electrolysis [21] and their real-time digital twins [22]. Several mathematical models have been proposed for the DAB converter in the literature. Authors in [23] investigate a 2 kW automotive bidirectional DAB converter to achieve power transfer between a low-voltage port (11V–16 V) and a high-voltage port (240V–450 V). An exact discrete-time small signal model is derived for designing digital control of the converter for different modulation schemes. In [24], a novel average modeling approach for a phase-shift controlled bidirectional DAB converter in the continuous time domain was proposed. The method utilizes switching frequency terms in the Fourier series of the state variables. However, this modeling approach only considers the contribution of the fundamental component for modeling the converter dynamics and transformer current. Three terms ($N=0$ and $N=\pm 1$) are considered for the

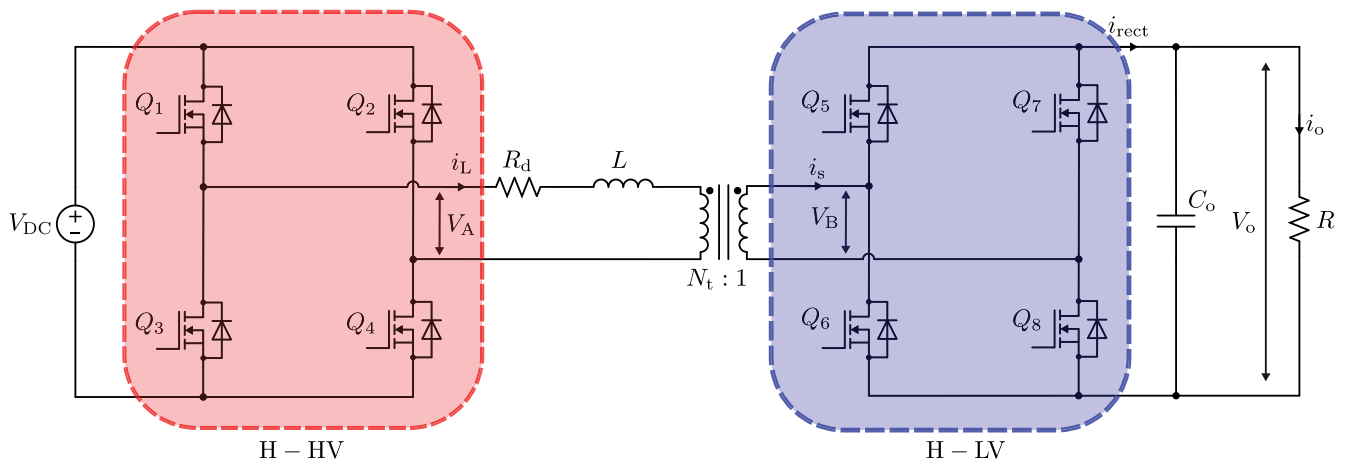


FIGURE 1. Dual Active Bridge Converter.

modeling approach. However, with the contribution of these three terms, a sinusoidal transformer current is obtained, which does not represent the actual current within a DAB converter. Furthermore, only the frequency response of the proposed model has been compared with other modeling approaches. In [25], the average modeling method was applied to general modulation strategies such as dual phase shift (DPS), extended phase-shift (EPS), and triple phase shift (TPS) using the technique proposed in [24]. A method was proposed to minimize the error due to truncating the Fourier series at the fundamental harmonics. Moreover, an additional correction factor was introduced that considers the copper losses in the transformer. A continuous time full-order model retaining high-frequency information of the system was proposed in [26]. A single equation using the Toeplitz operator obtained state equations for every harmonic of a given state variable. Authors in [27] present a method of constructing accurate and scalable models of DC distribution systems containing multiple DAB converters. The reconstruction of DAB transformer currents has been discussed in this work. However, this approach utilizes Fourier and inverse Laplace transformations, making the reconstruction steps complex and challenging to understand.

This work emphasizes DAB converters' Generalized Average Modelling approach, offering additional contributions. Firstly, in the literature, the generalized average modeling of DAB converters does not explain crucial steps within the modeling process. This often creates ambiguity among researchers endeavoring to comprehend this modeling paradigm. To address this gap, the present study delineates an exhaustive, step-by-step methodology to derive the generalized average model of a DAB converter, extending beyond the conventional scope. Moreover, while the GAM methodology frequently presupposes a unity transformer turns ratio for simplification purposes, this assumption does not universally apply to all DAB converter designs. Additionally, the conventional GAM is typically restricted to the fundamental frequency component. Contrasting this, the modeling procedure in this study encompasses the integration of higher-order harmonic elements employing only Fourier transformations and the

turns ratio to enhance the model's fidelity and applicability. Furthermore, it provides generalized expressions for more straightforward computation when additional harmonics must be considered. The structure of the paper is as follows: Section II provides an overview of the DAB converter and the concept of generalized average modeling. Section III is the crux of this paper and, therefore, describes the step-by-step proposed modeling procedure for the DAB converter. Results have been illustrated and discussed in Section IV. Finally, conclusions have been provided in Section V.

II. THE DAB CONVERTER AND GAM

A. THE DAB CONVERTER

The DAB converter has been shown in Fig. 1. It consists of two H-bridges, H-HV and H-LV, representing the high-voltage side bridge (leading H-bridge) and the low-voltage side bridge (lagging H-bridge). Depending on the application, each bridge is isolated using a high-frequency (HF) transformer. For this work, the single-phase shift (SPS) strategy has been considered a standard controller in the industry. The expression for the output power of the DAB with the SPS controller is given by,

$$P_o = \frac{(d \cdot (1 - d)) \cdot V_{DC} \cdot V_o \cdot N_t}{2 \cdot f_s \cdot L} \quad (1)$$

$$N_t = \frac{V_{DC}}{V_o} \quad (2)$$

$$d = \frac{\phi}{\pi} \quad (3)$$

where P_o is the output power, W. V_o is the output voltage, V. V_{DC} is the input DC voltage, V. f_s is the switching frequency, Hz. N_t is the primary to the secondary turns ratio of the HF transformer. L is the series inductance, H. d is the phase shift duty ratio and, ϕ is the phase angle, rad.

B. CONCEPT OF GENERALIZED AVERAGE MODELLING (GAM)

Most DC-DC converters have only one conversion state, containing only DC variables. The small ripple approximation holds in modeling such converters, wherein the higher-order

TABLE 1. Switching States of the DAB Converter Based on the Switching Functions $S_1(t)$ and $S_2(t)$

Value	Switching States ($S_1(t)$)	Switching States ($S_2(t)$)
+1	Q_1, Q_4 ON	Q_5, Q_8 ON
-1	Q_2, Q_3 ON	Q_6, Q_7 ON

terms are neglected. However, in the case of special DC-DC converters such as the DAB converter, there is an intermediate AC stage, i.e., the intermediate current between the two H-Bridge is purely AC, and therefore, the previously stated small ripple approximation no longer can be applied to such a converter.

To model power electronics converters with more than one conversion stage, the generalized average modeling (GAM) technique can be utilized. This technique captures the contribution of higher-order harmonics using averaging [1]. The GAM is based on the waveform representation using the complex Fourier series [28]. A signal $x(t)$ can be expressed as

$$x(t) = \sum_{-\infty}^{\infty} x_k \cdot e^{jk\omega_s t} \quad (4)$$

where ω_s is the angular switching frequency, rad s^{-1} . x_k is the k^{th} coefficient of variable x which can be defined as,

$$x_k(t) = \frac{1}{T} \int_{t-T}^T x(\tau) \cdot e^{-jk\omega_s \tau} d\tau \quad (5)$$

Applying the principle of averaging,

$$x(t) = \langle x \rangle_k(t) \quad (6)$$

GAM technique utilizes two fundamental properties necessary for simplifying the calculations [28]. The first property relates to the derivative of the k^{th} coefficient of variable x .

$$\frac{d\langle x \rangle_k(t)}{dt} = \left\langle \frac{dx}{dt} \right\rangle_k(t) - jk\omega_s \langle x \rangle_k(t) \quad (7)$$

The second property relates to the k^{th} product of two variables x and y ,

$$\langle x \cdot y \rangle_k(t) = \sum_i \langle x \rangle_{k-i}(t) \cdot \langle y \rangle_i(t) \quad (8)$$

III. PROPOSED MODELING OF THE DAB CONVERTER

A. STEP 1: DEFINING THE SWITCHING FUNCTIONS

The operation of a DAB converter is mainly governed by two piece-wise switching functions defined as follows:

$$S_1(t) = \begin{cases} +1, & 0 \leq t < \frac{T}{2}, \\ -1, & \frac{T}{2} \leq t < T \end{cases} \quad (9)$$

$$S_2(t) = \begin{cases} -1, & 0 \leq t < \frac{dT}{2}, \\ +1, & \frac{dT}{2} \leq t < \frac{dT}{2} + \frac{T}{2} \\ -1, & \frac{dT}{2} + \frac{T}{2} \leq t < T \end{cases} \quad (10)$$

Table 1 showcases the switching states of the DAB converter based on the defined piece-wise switching functions where

$S_1(t)$ represents the switching function associated with H-HV, and $S_2(t)$ represents the switching function associated with H-LV. For this work, d represents the phase shift duty ratio of H-LV with respect to H-HV.

B. STEP 2: IDENTIFYING THE FOURIER COEFFICIENTS

Any signal $x(t)$ can be defined using a series of sinusoids with the help of Fourier Series [29],

$$x(t) = A_0 + \sum_{n=1}^N A_n \cos(n\omega_s t) + \sum_{n=1}^N B_n \sin(n\omega_s t) \quad (11)$$

where

$$A_0 = \frac{1}{T} \int_0^T x(t) dt$$

$$A_n = \frac{2}{T} \int_0^T x(t) \cos(n\omega_s t) dt$$

$$B_n = \frac{2}{T} \int_0^T x(t) \sin(n\omega_s t) dt$$

Before analyzing the switching functions, it is evident from preliminary inspection of their definitions that the DC component for both switching functions, $S_1(t)$ and $S_2(t)$ is zero.

1) ANALYSIS OF $S_1(t)$

$$A_n = \frac{2}{T} \left(\int_0^{\frac{T}{2}} \cos(n\omega_s t) dt + \int_{\frac{T}{2}}^T -\cos(n\omega_s t) dt \right)$$

$$B_n = \frac{2}{T} \left(\int_0^{\frac{T}{2}} \sin(n\omega_s t) dt + \int_{\frac{T}{2}}^T -\sin(n\omega_s t) dt \right)$$

$$A_0 = 0 \quad (12)$$

$$A_n = 0, \quad \forall n \quad (13)$$

$$B_n = \begin{cases} \frac{4}{n\pi}, & n \text{ is odd} \\ 0, & n \text{ is even} \end{cases} \quad (14)$$

2) ANALYSIS OF $S_2(t)$

$$A'_n = \frac{2}{T} \left(\int_0^{\frac{dT}{2}} -\cos(n\omega_s t) dt + \int_{\frac{dT}{2}}^{\frac{dT}{2} + \frac{T}{2}} \cos(n\omega_s t) dt \right.$$

$$\left. + \int_{\frac{dT}{2} + \frac{T}{2}}^T -\cos(n\omega_s t) dt \right)$$

$$B'_n = \frac{2}{T} \left(\int_0^{\frac{dT}{2}} -\sin(n\omega_s t) dt + \int_{\frac{dT}{2}}^{\frac{dT}{2} + \frac{T}{2}} \sin(n\omega_s t) dt \right.$$

$$\left. + \int_{\frac{dT}{2} + \frac{T}{2}}^T -\sin(n\omega_s t) dt \right)$$

$$A'_0 = 0$$

$$(15)$$

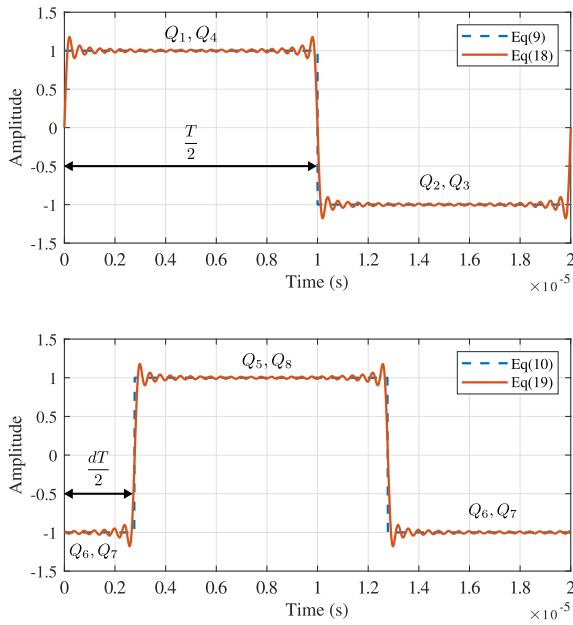


FIGURE 2. Verification of Fourier coefficients, $d = 0.2764$ ($\phi = 0.873$ rad).

$$A'_n = \begin{cases} \frac{-4 \sin(n\phi)}{n\pi}, & n \text{ is odd} \\ 0, & n \text{ is even} \end{cases} \quad (16)$$

$$B'_n = \begin{cases} \frac{4 \cos(n\phi)}{n\pi}, & n \text{ is odd} \\ 0, & n \text{ is even} \end{cases} \quad (17)$$

where ϕ is the phase shift angle of switching function $S_2(t)$ with respect to $S_1(t)$. The switching functions can, therefore, be represented using (11),

$$S_1(t) = A_0 + \sum_{n=1}^N A_n \cos(n\omega_s t) + \sum_{n=1}^N B_n \sin(n\omega_s t) \quad (18)$$

$$S_2(t) = A'_0 + \sum_{n=1}^N A'_n \cos(n\omega_s t) + \sum_{n=1}^N B'_n \sin(n\omega_s t) \quad (19)$$

C. STEP 3: VALIDATION OF FOURIER COEFFICIENTS

Fig. 2 shows the comparison between the piece-wise switching functions, $S_1(t)$ and $S_2(t)$, obtained using (9) and (10) with Fourier switching functions obtained using (18) and (19) for $N = 50$. The Fourier coefficients have been computed correctly.

D. STEP 4: DEFINING THE DAB CONVERTER DYNAMICS

Once the switching functions have been defined and represented correctly, the next step is to define the converter dynamics with the help of state-space equations. The following assumptions have been made:

- 1) The switching is considered to be instantaneous, and therefore, the effect of dead time has not been considered.
- 2) The transformer is considered ideal; therefore, the magnetizing inductance is sufficiently large to prevent saturation.

- 3) All quantities have been referred toward the primary side of the transformer.
- 4) The input voltage and load dynamics are much slower than the converter's.

The primary side inductor current, $i_L(t)$ and the output capacitor voltage, $v_c(t)$ have been considered as state variables for this model. The primary side inductor current, considering the turn ratio of the transformer N_t , can be expressed as,

$$\frac{di_L(t)}{dt} = \frac{1}{L} \left(V_A(t) - N_t V_B(t) - R_d \cdot i_L(t) \right)$$

where R_d is the lumped resistance that takes into account the equivalent series resistance of the inductor (L) and the on-state resistance of the semiconductor switches.

Similarly, the output capacitor voltage can be expressed as,

$$\frac{dv_c(t)}{dt} = \frac{1}{C_o} \left(i_{\text{rect}}(t) - \frac{v_c(t)}{N_t R} \right)$$

where $V_A(t)$ and $V_B(t)$ are the output voltage and input voltage of H-HV and H-LV, respectively, and $i_{\text{rect}}(t)$ is the rectified secondary current of the H-LV.

The voltage of the H-bridges, H-HV, and H-LV, as well as the rectified current of H-LV, can now be expressed in terms of the switching functions by using (9) and (10).

$$\frac{di_L(t)}{dt} = \frac{1}{L} \left(S_1(t) \cdot V_{\text{DC}} - N_t \cdot S_2(t) \cdot v_c(t) - R_d \cdot i_L(t) \right) \quad (20)$$

Similarly, the output capacitor voltage can be expressed as,

$$\frac{dv_c(t)}{dt} = \frac{1}{C_o} \left(S_2(t) \cdot i_s(t) - \frac{v_c(t)}{N_t R} \right) \quad (21)$$

where $i_s(t)$ is the secondary side input current to H-LV.

E. STEP 5: TRANSITIONING TOWARDS GENERALIZED STATE SPACE AVERAGING

To apply the complex Fourier series, the following changes need to be carried out:

- 1) The Fourier coefficients computed for the switching functions in Step 2 need to be expressed in terms of equivalent complex Fourier coefficients [29]. Note that the DC component remains independent of this change.

$$\langle S_1 \rangle_0 = A_0 \quad (22)$$

$$\langle S_1 \rangle_{nR} = \frac{A_n}{2} \quad (23)$$

$$\langle S_1 \rangle_{nI} = -\frac{B_n}{2} \quad (24)$$

$$\langle S_2 \rangle_0 = A'_0 \quad (25)$$

$$\langle S_2 \rangle_{nR} = \frac{A'_n}{2} \quad (26)$$

$$\langle S_2 \rangle_{nI} = -\frac{B'_n}{2} \quad (27)$$

where the subscripts R and I denote the real and imaginary components.

2) Equations (20) and (21) need to be decomposed into real and imaginary components.

Therefore, applying (6) and (7) on (20) and (21),

$$\frac{d\langle i_L \rangle_0}{dt} = \frac{1}{L} \left(\langle S_1 \cdot V_{DC} \rangle_0 - N_t \cdot \langle S_2 \cdot v_c \rangle_0 - R_d \cdot \langle i_L \rangle_0 \right) \quad (28)$$

$$\begin{aligned} \frac{d\langle i_L \rangle_{1R}}{dt} = \frac{1}{L} \left(\langle S_1 \cdot V_{DC} \rangle_{1R} - N_t \cdot \langle S_2 \cdot v_c \rangle_{1R} \right. \\ \left. - R_d \cdot \langle i_L \rangle_{1R} \right) + \omega_s \langle i_L \rangle_{1I} \end{aligned} \quad (29)$$

$$\frac{d\langle i_L \rangle_{1I}}{dt} = \frac{1}{L} \left(\langle S_1 \cdot V_{DC} \rangle_{1I} - N_t \cdot \langle S_2 \cdot v_c \rangle_{1I} \right. \\ \left. - R_d \cdot \langle i_L \rangle_{1I} \right) - \omega_s \langle i_L \rangle_{1R} \quad (30)$$

$$\frac{d\langle v_c \rangle_0}{dt} = \frac{1}{C_o} \left(\langle S_2 \cdot i_s \rangle_0 - \frac{\langle v_c \rangle_0}{N_t R} \right) \quad (31)$$

$$\frac{d\langle v_c \rangle_{1R}}{dt} = \frac{1}{C_o} \left(\langle S_2 \cdot i_s \rangle_{1R} - \frac{\langle v_c \rangle_{1R}}{N_t R} \right) + \omega_s \cdot \langle v_c \rangle_{1I} \quad (32)$$

$$\frac{d\langle v_c \rangle_{1I}}{dt} = \frac{1}{C_o} \left(\langle S_2 \cdot i_s \rangle_{1I} - \frac{\langle v_c \rangle_{1I}}{N_t R} \right) - \omega_s \cdot \langle v_c \rangle_{1R} \quad (33)$$

Equations (28), (32), and (33) can be truncated based on the fourth assumption. (29)–(31) contains the product of the fundamental harmonics as well as the product of the DC component. Therefore, (8) is necessary to further simplify (29)–(31).

Considering that the $-N^{\text{th}}$ coefficient is a complex conjugate of the N^{th} coefficient, we have for $N = 1$,

$$\langle xy \rangle_0 = \langle x \rangle_1 \cdot \langle y \rangle_{-1} + \langle x \rangle_0 \cdot \langle y \rangle_0 + \langle x \rangle_{-1} \cdot \langle y \rangle_1 \quad (34)$$

$$\langle xy \rangle_1 = \langle x \rangle_1 \cdot \langle y \rangle_0 + \langle x \rangle_0 \cdot \langle y \rangle_1 \quad (35)$$

In general, for the first M odd harmonics,

$$\begin{aligned} \langle xy \rangle_0 = \langle x \rangle_0 \cdot \langle y \rangle_0 + 2 \cdot \left(\sum_{k=1}^M \left(\langle x \rangle_{(2k-1)R} \cdot \langle y \rangle_{(2k-1)R} \right. \right. \\ \left. \left. + \langle x \rangle_{(2k-1)I} \cdot \langle y \rangle_{(2k-1)I} \right) \right) \end{aligned} \quad (36)$$

Similarly,

$$\langle xy \rangle_{(2k-1)R} = \langle x \rangle_{(2k-1)R} \cdot \langle y \rangle_0 + \langle x \rangle_0 \cdot \langle y \rangle_{(2k-1)R} \quad (37)$$

$$\langle xy \rangle_{(2k-1)I} = \langle x \rangle_{(2k-1)I} \cdot \langle y \rangle_0 + \langle x \rangle_0 \cdot \langle y \rangle_{(2k-1)I} \quad (38)$$

where $k = 1, 2, 3 \dots M$.

Applying (36)–(38) to (29)–(31), we have,

$$\begin{aligned} \frac{d\langle i_L \rangle_{1R}}{dt} = \frac{1}{L} \left(\langle S_1 \rangle_{1R} \cdot \langle V_{DC} \rangle_0 + \langle S_1 \rangle_0 \cdot \langle V_{DC} \rangle_{1R} \right. \\ \left. - N_t \cdot (\langle S_2 \rangle_{1R} \cdot \langle v_c \rangle_0 + \langle S_2 \rangle_0 \cdot \langle v_c \rangle_{1R}) \right. \\ \left. - R_d \cdot \langle i_L \rangle_{1R} \right) + \omega_s \langle i_L \rangle_{1I} \end{aligned}$$

$$\begin{aligned} \frac{d\langle i_L \rangle_{1I}}{dt} = \frac{1}{L} \left(\langle S_1 \rangle_{1I} \cdot \langle V_{DC} \rangle_0 + \langle S_1 \rangle_0 \cdot \langle V_{DC} \rangle_{1I} \right. \\ \left. - N_t \cdot (\langle S_2 \rangle_{1I} \cdot \langle v_c \rangle_0 + \langle S_2 \rangle_0 \cdot \langle v_c \rangle_{1I}) \right. \\ \left. - R_d \cdot \langle i_L \rangle_{1I} \right) - \omega_s \langle i_L \rangle_{1R} \end{aligned}$$

$$\begin{aligned} \frac{d\langle v_c \rangle_0}{dt} = \frac{1}{C_o} \left(\langle S_2 \rangle_0 \cdot \langle i_L \rangle_0 + 2 \cdot (\langle S_2 \rangle_{1R} \cdot \langle i_L \rangle_{1R} \right. \\ \left. + \langle S_2 \rangle_{1I} \cdot \langle i_L \rangle_{1I}) - \frac{\langle v_c \rangle_0}{N_t R} \right) \end{aligned}$$

Applying the fourth assumption and substituting the values of $\langle S_1 \rangle_0$, $\langle S_1 \rangle_{nR}$, $\langle S_1 \rangle_{nI}$, $\langle S_2 \rangle_0$, $\langle S_2 \rangle_{nR}$, and $\langle S_2 \rangle_{nI}$ from (22)–(27), we have,

$$\frac{d\langle i_L \rangle_{1R}}{dt} = \frac{1}{L} \left(\frac{2N_t \sin(\phi) \langle v_c \rangle_0}{\pi} - R_d \cdot \langle i_L \rangle_{1R} \right) + \omega_s \langle i_L \rangle_{1I} \quad (39)$$

$$\begin{aligned} \frac{d\langle i_L \rangle_{1I}}{dt} = \frac{1}{L} \left(\frac{-2V_{DC}}{\pi} + \frac{2N_t \cos(\phi) \langle v_c \rangle_0}{\pi} - R_d \cdot \langle i_L \rangle_{1I} \right) \\ - \omega_s \langle i_L \rangle_{1R} \end{aligned} \quad (40)$$

$$\frac{d\langle v_c \rangle_0}{dt} = \frac{1}{C_o} \left(\frac{-4 \sin(\phi) \langle i_L \rangle_{1R}}{\pi} - \frac{4 \cos(\phi) \langle i_L \rangle_{1I}}{\pi} - \frac{\langle v_c \rangle_0}{N_t R} \right) \quad (41)$$

F. STEP 6: MATRIX REPRESENTATION OF THE SYSTEM

Equations (39), (40), and (41) define the dynamics of the DAB converter by only considering the fundamental component. (42) provides the matrix representation defining the dynamics of the DAB converter, considering only the fundamental component.

$$\begin{aligned} \frac{d}{dt} \begin{bmatrix} i_{L1R} \\ i_{L1I} \\ v_{c0} \end{bmatrix} = \begin{bmatrix} -\frac{R_d}{L} & \omega_s & \frac{2N_t \sin(\phi)}{\pi L} \\ -\omega_s & -\frac{R_d}{L} & \frac{2N_t \cos(\phi)}{\pi L} \\ \frac{-4 \sin(\phi)}{\pi C_o} & \frac{-4 \cos(\phi)}{\pi C_o} & \frac{-1}{N_t R C_o} \end{bmatrix} \begin{bmatrix} i_{L1R} \\ i_{L1I} \\ v_{c0} \end{bmatrix} \\ + \begin{bmatrix} 0 & \frac{-2}{L\pi} & 0 \end{bmatrix}^T V_{DC} \end{aligned} \quad (42)$$

The system defined by (42) can be easily recomputed to include additional harmonic components using (6)–(8), and (36)–(38) to model the transformer current of the DAB converter accurately. Considering the contribution until the third harmonics, the DAB converter model is described below as an example.

$$\frac{d\langle i_L \rangle_{1R}}{dt} = \frac{1}{L} \left(\frac{2N_t \sin(\phi) \langle v_c \rangle_0}{\pi} - R_d \cdot \langle i_L \rangle_{1R} \right) + \omega_s \langle i_L \rangle_{1I}$$

$$\begin{aligned} \frac{d\langle i_L \rangle_{1I}}{dt} = \frac{1}{L} \left(\frac{-2V_{DC}}{\pi} + \frac{2N_t \cos(\phi) \langle v_c \rangle_0}{\pi} - R_d \cdot \langle i_L \rangle_{1I} \right) \\ - \omega_s \langle i_L \rangle_{1R} \end{aligned}$$

$$\begin{aligned} \frac{d\langle i_L \rangle_{3R}}{dt} = \frac{1}{L} \left(\frac{2N_t \sin(3\phi) \langle v_c \rangle_0}{3\pi} - R_d \cdot \langle i_L \rangle_{3R} \right) \\ + 3\omega_s \langle i_L \rangle_{3I} \end{aligned}$$

TABLE 2. Test Converter Specifications

Parameter	Symbol	Value
Rated Power	P_o	2.5 kW
Input DC Voltage	V_{DC}	500 V
Output DC Voltage	V_o	50 V
Turns Ratio	N_t	10:1
Series Inductance	L	200 μ H
Phase Shift Angle	ϕ	0.873 rad
Switching Frequency	f_s	50 kHz
Lumped Resistance	R_d	0.1 Ω
Output Capacitance	C_o	200 μ F
Load Resistance	R	1 Ω

$$\frac{d\langle i_L \rangle_{3I}}{dt} = \frac{1}{L} \left(\frac{-2V_{DC}}{3\pi} + \frac{2N_t \cos(3\phi) \langle v_c \rangle_0}{3\pi} - R_d \cdot \langle i_L \rangle_{3I} \right) - 3\omega_s \langle i_L \rangle_{3R}$$

$$\frac{d\langle v_c \rangle_0}{dt} = \frac{1}{C_o} \left(\frac{-4 \sin(\phi) \langle i_L \rangle_{1R}}{\pi} - \frac{4 \cos(\phi) \langle i_L \rangle_{1I}}{\pi} - \frac{4 \sin(3\phi) \langle i_L \rangle_{3R}}{3\pi} - \frac{4 \cos(3\phi) \langle i_L \rangle_{3I}}{3\pi} - \frac{\langle v_c \rangle_0}{N_t R} \right)$$

Equation (43) shown at the bottom of this page, showcases the GAM model of the DAB converter considering the first five odd harmonics.

IV. RESULTS AND DISCUSSION

A test case was considered to validate the GAM model. Table 2 showcases the specifications of the DAB converter utilized for the validation purpose. Fig. 3 showcases the experimental setup consisting of a PLECS RT-Box-2 connected to a PC. Table 3 provides the specifications of the PLECS RT-Box-2. A discretization time step of $T_{disc} = 1.5 \mu s$ was selected for the real-time simulation model.



FIGURE 3. Experimental Setup.

TABLE 3. PLECS RT-Box-2 Specifications [30]

Processor	Xilinx Zynq Ultrascale+	ZU9EG
Number of CPU cores	4	ARM Cortex-A53, 1.5 GHz
Analog inputs	Channels	16
	Resolution	16 bit
	Input type	Differential
	Max. sample rate	5 Msps
Analog outputs	Channels	16
	Resolution	16 bit
	Max. update rate	5 Msps
Digital inputs	Channels	32
	Logic levels	3.3 V (5 V tolerant)
Digital outputs	Channels	32
	Logic levels	3.3 V, 5 V

Fig. 4(a) showcases the steady state output voltage response obtained from GAM ($M = 1$ and $M = 5$), PLECS simulation model, and the RT-Box-2 for the considered test case. While the PLECS model response for the output voltage

$$\frac{d}{dt} \begin{bmatrix} i_{L1R} \\ i_{L1I} \\ i_{L3R} \\ i_{L3I} \\ i_{L5R} \\ i_{L5I} \\ i_{L7R} \\ i_{L7I} \\ i_{L9R} \\ i_{L9I} \\ v_{c0} \end{bmatrix} = \begin{bmatrix} -\frac{R_d}{L} & \omega_s & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & -\frac{N_t \langle S_2 \rangle_{1R}}{L} \\ -\omega_s & -\frac{R_d}{L} & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & -\frac{N_t \langle S_2 \rangle_{1I}}{L} \\ 0 & 0 & -\frac{R_d}{L} & 3\omega_s & 0 & 0 & 0 & 0 & 0 & 0 & 0 & -\frac{N_t \langle S_2 \rangle_{3R}}{L} \\ 0 & 0 & -3\omega_s & -\frac{R_d}{L} & 0 & 0 & 0 & 0 & 0 & 0 & 0 & -\frac{N_t \langle S_2 \rangle_{3I}}{L} \\ 0 & 0 & 0 & 0 & -\frac{R_d}{L} & 5\omega_s & 0 & 0 & 0 & 0 & 0 & -\frac{N_t \langle S_2 \rangle_{5R}}{L} \\ 0 & 0 & 0 & 0 & -5\omega_s & -\frac{R_d}{L} & 0 & 0 & 0 & 0 & 0 & -\frac{N_t \langle S_2 \rangle_{5I}}{L} \\ 0 & 0 & 0 & 0 & 0 & 0 & -\frac{R_d}{L} & 7\omega_s & 0 & 0 & 0 & -\frac{N_t \langle S_2 \rangle_{7R}}{L} \\ 0 & 0 & 0 & 0 & 0 & 0 & -7\omega_s & -\frac{R_d}{L} & 0 & 0 & 0 & -\frac{N_t \langle S_2 \rangle_{7I}}{L} \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & -\frac{R_d}{L} & 9\omega_s & 0 & -\frac{N_t \langle S_2 \rangle_{9R}}{L} \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & -9\omega_s & -\frac{R_d}{L} & 0 & -\frac{N_t \langle S_2 \rangle_{9I}}{L} \\ \frac{2\langle S_2 \rangle_{1R}}{C_o} & \frac{2\langle S_2 \rangle_{1I}}{C_o} & \frac{2\langle S_2 \rangle_{3R}}{C_o} & \frac{2\langle S_2 \rangle_{3I}}{C_o} & \frac{2\langle S_2 \rangle_{5R}}{C_o} & \frac{2\langle S_2 \rangle_{5I}}{C_o} & \frac{2\langle S_2 \rangle_{7R}}{C_o} & \frac{2\langle S_2 \rangle_{7I}}{C_o} & \frac{2\langle S_2 \rangle_{9R}}{C_o} & \frac{2\langle S_2 \rangle_{9I}}{C_o} & \frac{-1}{N_t R C_o} \end{bmatrix} \begin{bmatrix} i_{L1R} \\ i_{L1I} \\ i_{L3R} \\ i_{L3I} \\ i_{L5R} \\ i_{L5I} \\ i_{L7R} \\ i_{L7I} \\ i_{L9R} \\ i_{L9I} \\ v_{c0} \end{bmatrix} + \begin{bmatrix} 0 & \frac{\langle S_1 \rangle_{1I}}{L} & 0 & \frac{\langle S_1 \rangle_{3I}}{L} & 0 & \frac{\langle S_1 \rangle_{5I}}{L} & 0 & \frac{\langle S_1 \rangle_{7I}}{L} & 0 & \frac{\langle S_1 \rangle_{9I}}{L} & 0 \end{bmatrix}^T V_{DC} \quad (43)$$

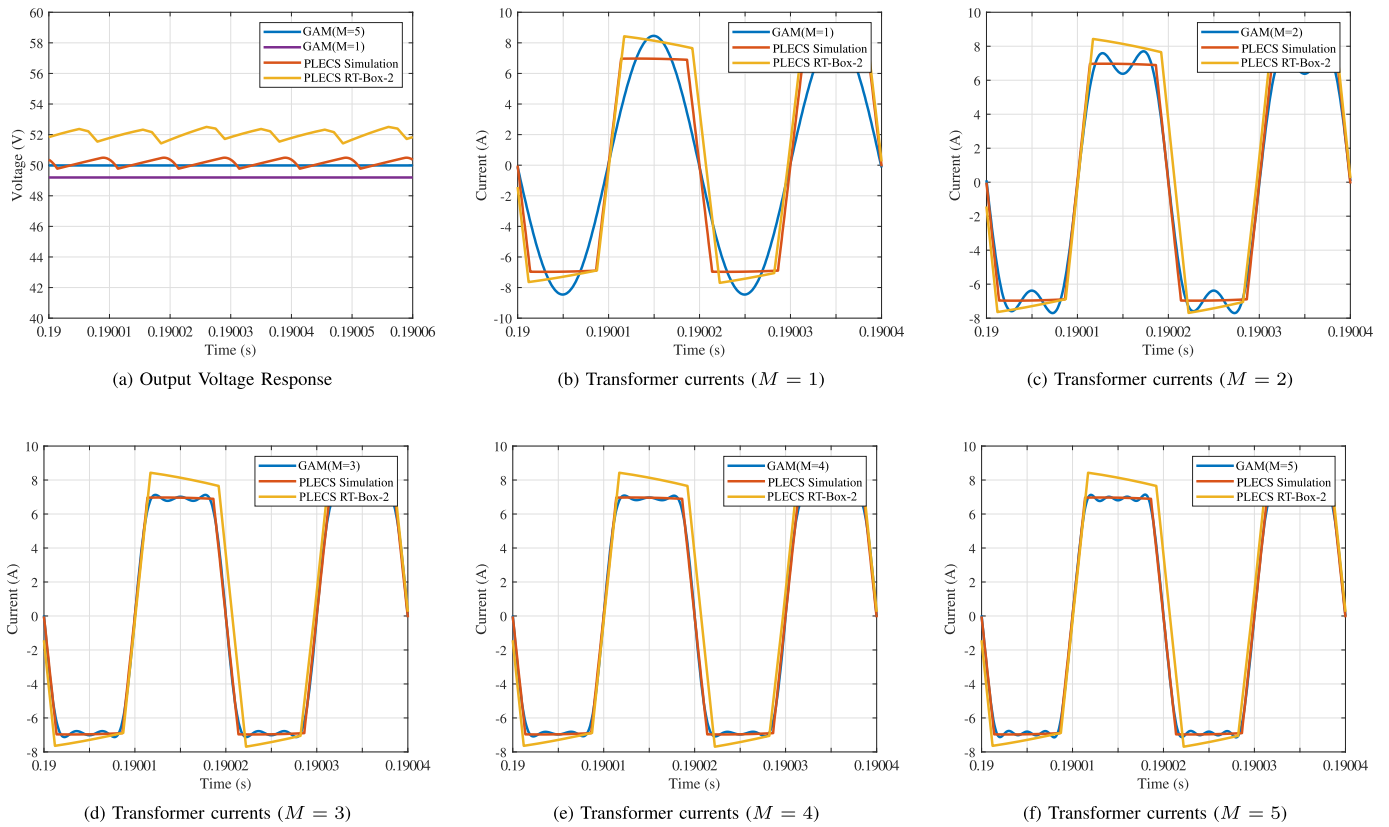


FIGURE 4. Comparison of output voltage response and steady-state transformer currents of GAM, PLECS simulation model, and PLECS RT-Box-2 for the first M odd harmonics.

contains higher ripple content, the GAM response for the output voltage contains little or no ripple. It is evident from the response that the output voltage of the converter for the case of GAM ($M = 1$) has a steady-state error. On the other hand, the output voltage of GAM ($M = 5$) converges towards the PLECS simulation response such that it is nearly the average of the PLECS simulation response. The real-time simulation response contains a steady-state error but has a similar ripple content to the PLECS simulation model. The transformer current with contributions up to the first M odd harmonics using the GAM can be extracted using the following expression:

$$i_L(t) = 2 \sum_{k=1}^M \left(i_{L(2k-1)R} \cos \left((2k-1) \cdot \left(\omega_s t - \frac{\phi}{2} \right) \right) + i_{L(2k-1)I} \sin \left((2k-1) \cdot \left(\omega_s t - \frac{\phi}{2} \right) \right) \right) \quad (44)$$

With the increase in M , the accuracy of the GAM increases as shown in Fig. 4(b)–(f) that showcases the comparison of the reconstructed transformer currents of the GAM with the response of the PLECS simulation model and the real-time simulation model.

To quantitatively evaluate the accuracy of the GAM obtained using the proposed procedure, several GAMs (fundamental, third, fifth, seventh, and ninth harmonics) are compared against the PLECS and the real-time simulation models. Table 4 showcases the GAM accuracy. All models are

TABLE 4. Evaluation of GAM Accuracy

d	0.0993	0.1486	0.1916	0.2333	0.2764
P_o	0.5 kW	1 kW	1.5 kW	2 kW	2.5 kW
Model Type	% Error				
PLECS	0.868	0.748	0.631	0.652	0.681
PLECS RT-Box	104.69	9.399	12.909	12.914	8.288
GAM ($M = 1$)	21.16	15.536	11.021	6.932	3.184
GAM ($M = 2$)	5.177	1.331	0.625	1.57	1.662
GAM ($M = 3$)	0.64	1.019	0.975	0.412	0.265
GAM ($M = 4$)	0.733	0.869	0.121	0.355	0.418
GAM ($M = 5$)	0.992	0.380	0.226	0.234	0.065

operated at five different operating power set-points starting from 0.5 kW up to the rated power of 2.5 kW. Absolute error is calculated by comparing the output power of a given model type with the analytically calculated power, P_o . Results indicate that the GAM ($M = 1$) is the most inaccurate model among the considered GAMs with an error of greater than 3% up to a maximum error of 21.16%. As M increases for the subsequent GAMs, this error significantly reduces. The GAM ($M = 5$) presents an error of less than 1% and has relatively higher accuracy than other GAMs, PLECS simulation, and real-time simulation models on the RT-Box-2. It is interesting to note two observations. First, the contribution up to the ninth harmonic ($M = 5$) is sufficient to model the transformer currents of the converter accurately. Second, the

transformer currents obtained from the real-time simulation present a similar wave shape; however, they contain inaccuracies in their amplitudes. An error of 104.69% was observed for PLECS RT-Box-2 at $P_o = 0.5$ kW. This error reduces significantly as the operating power gradually increases. The following two factors can justify the amplitude inaccuracies throughout the operating power range and the large error at $P_o = 0.5$ kW. First, the RT-Box-2 contains four available FPGA cores; however, only three are available for computation during the real-time simulation. The remaining core is reserved for the Linux operating system, which runs internally. The discretization time step depends not only on the complexity of the real-time simulation model of the power electronics converter but also on the switching frequency. The best time step that could be achieved for RT-Box-2, considering the converter model and choice of switching frequency, was $T_{disc} = 1.5 \mu s$. Any discretization step below $1.5 \mu s$ leads to an over-run condition of the real-time simulator. The computation load distribution among the FPGA cores is inconsistent in this condition. This inconsistency causes overburden on the FPGA cores and causes the converter quantities visualized on the scope to contain frequent fluctuations and inaccuracies. Second, to achieve an operating power of 0.5 kW, the phase angle required is approximately $\phi = 0.312$ rad. This phase angle is expressed as $1.98 \mu s$ when translated into the switching period. For the chosen discretization time-step, the FPGA cannot accurately reconstruct the transformer current since the FPGA updates the converter quantities every $1.5 \mu s$. Assuming that $T = 0$ initially, information is lost between $T = 1.5 \mu s$ and $T = 3 \mu s$ as the FPGA updates every $1.5 \mu s$. However, this issue no longer exists as the phase angle gradually increases. Considering the operating power of $P_o = 1.5$ kW, the corresponding phase angle is approximately $\phi = 0.602$ rad. This phase angle is expressed as $3.83 \mu s$ when translated into the switching period. In this case, the FPGA can reconstruct the transformer currents more accurately than in the previous case, as the data points of the transformer current are retained within two samples of $1.5 \mu s$ each. This is evident by the decrease in error for PLECS RT-Box-2 as the operating power increases beyond 0.5 kW.

V. CONCLUSION

DC-DC converters such as a DAB cannot be modeled by considering the small ripple approximation as it contains an intermediate AC stage. As a result, the GAM technique is utilized to capture the contribution of higher-order harmonics. However, the GAM technique is often restricted to considering only the fundamental frequency leading to a sinusoidal current, which presents an inaccurate representation of the DAB transformer current. In this paper, a step-by-step procedure to obtain a DAB model with the help of the GAM technique using only Fourier transformations was proposed. Unlike the conventional GAM technique, which is often restricted to consideration of only the fundamental component, the proposed procedure provides general expressions that can be used for developing the GAM of the converter that can take into account higher-order harmonics. Moreover,

the impact of the transformer's turn ratio was considered within the modeling procedure. The fundamental GAM ($M = 1$) of the DAB converter was developed to describe the modeling procedure and extended further to develop a ninth harmonic GAM ($M = 5$) of the converter. The validation of the model obtained with the proposed method was carried out by comparing transformer currents obtained from several GAMs (fundamental, third, fifth, seventh, and ninth harmonics) against the transformer currents of the simulation model and real-time simulation model in PLECS and PLECS RT-Box-2, respectively. The steady-state output voltage was evaluated by comparing the PLECS simulation and real-time simulation response with GAM ($M = 1$ and $M = 5$). A quantitative evaluation of model accuracy indicated that the ninth harmonic GAM of the converter was more consistent in accuracy over the operating power range than the PLECS simulation and the real-time simulation model on the PLECS RT-Box-2.

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